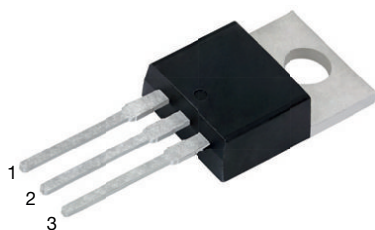
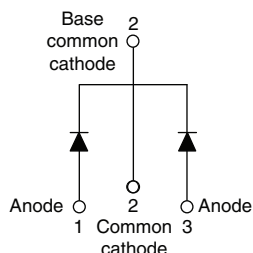


High Performance Schottky Rectifier, 2 x 20 A



TO-220AB 3L



FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 20 A
V_R	30 V
V_F at I_F	0.38 V
I_{RM} max.	183 mA at 125 °C
T_J max.	150 °C
E_{AS}	13 mJ
Package	TO-220AB 3L
Circuit configuration	Common cathode

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	40	A
V_{RRM}		30	V
I_{FSM}	$t_p = 5 \mu s$ sine	1100	A
V_F	20 A _{pk} , $T_J = 125$ °C (per leg)	0.38	V
T_J	Range	-55 to +150	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-42CTQ030-M3	UNITS
Maximum DC reverse voltage	V_R	30	V
Maximum working peak reverse voltage	V_{RWM}		

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current, see fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 121$ °C, rectangular waveform	20	A
per leg			40	
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse	1100	
		10 ms sine or 6 ms rect. pulse	360	
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25$ °C, $I_{AS} = 3$ A, $L = 2.90$ mH	13	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	3	A

**ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	20 A	0.48	V
		40 A	0.57	
		20 A	0.38	
		40 A	0.51	
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	3	mA
		$T_J = 125\text{ }^{\circ}\text{C}$	183	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$	0.22	V
Forward slope resistance	r_t		6.76	m Ω
Maximum junction capacitance per leg	C_T	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^{\circ}\text{C}$	2840	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V_R	10 000	V/ μ s

Note(1) Pulse width < 300 μ s, duty cycle < 2 %**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	2.0	°C/W
Maximum thermal resistance, junction to case per package			1.0	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.50	
Approximate weight			2	g
			0.07	oz.
Mounting torque			6 (5)	kgf · cm (lbf · in)
			12 (10)	
Marking device		Case style 3L TO-220AB	42CTQ030	

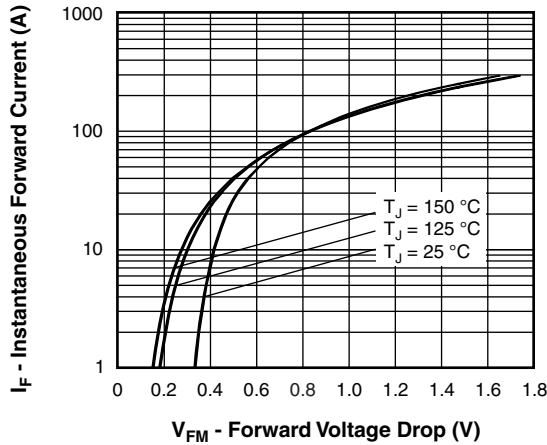


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

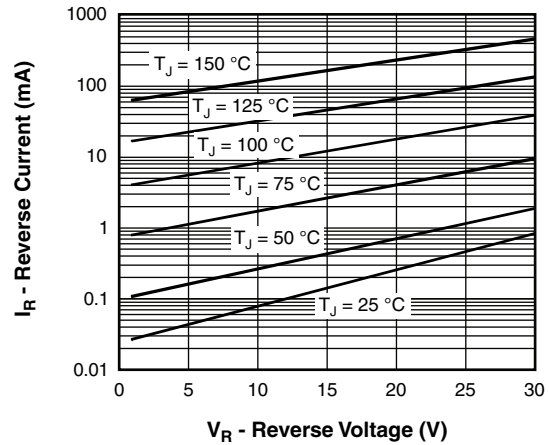


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

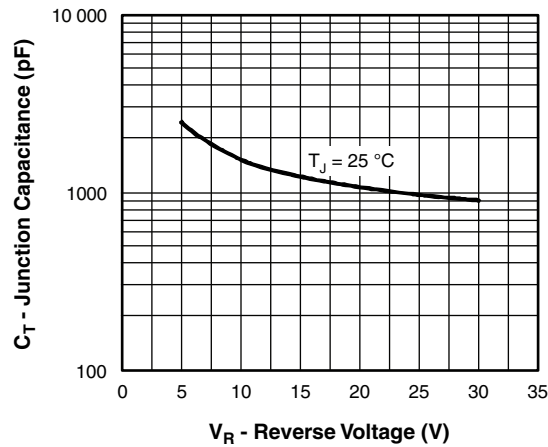
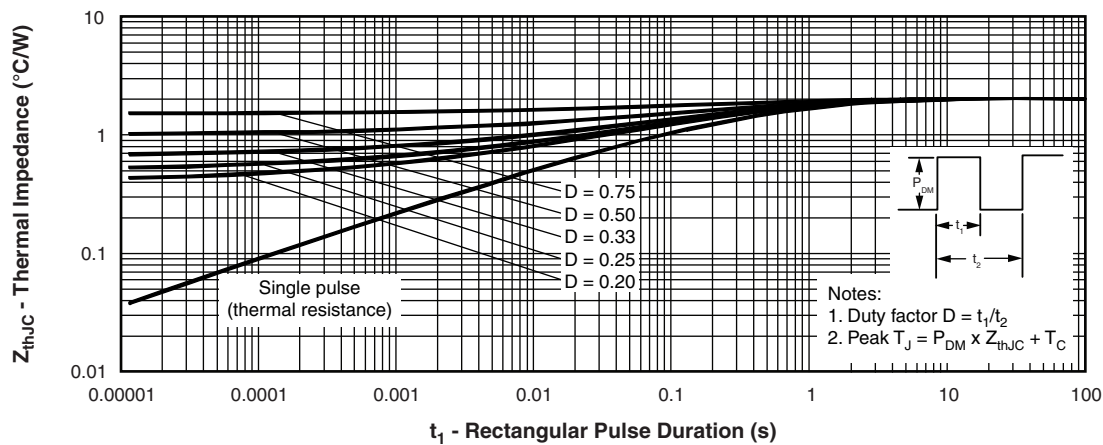


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

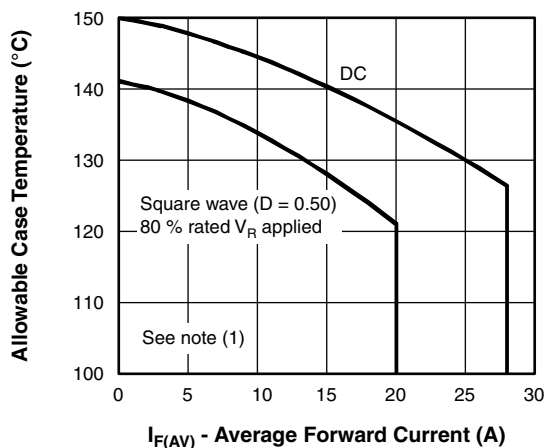


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

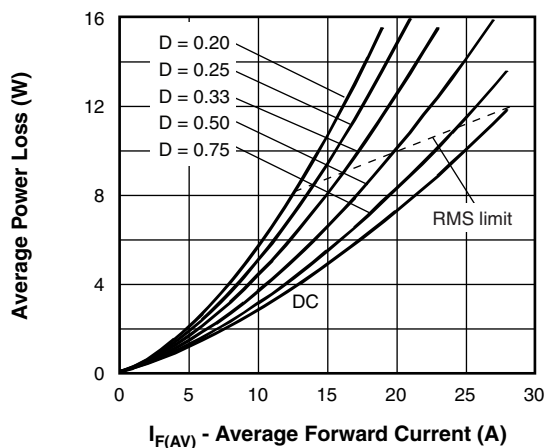


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

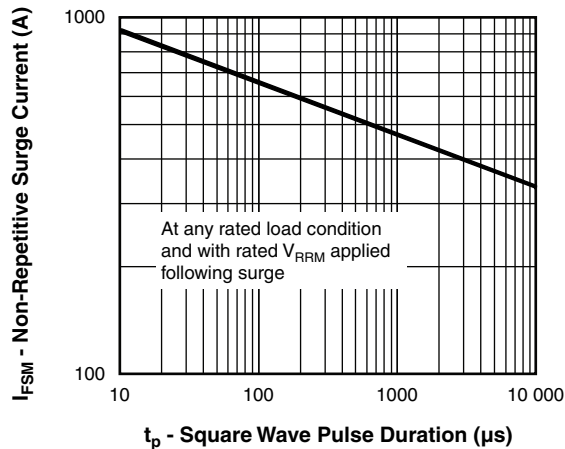


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

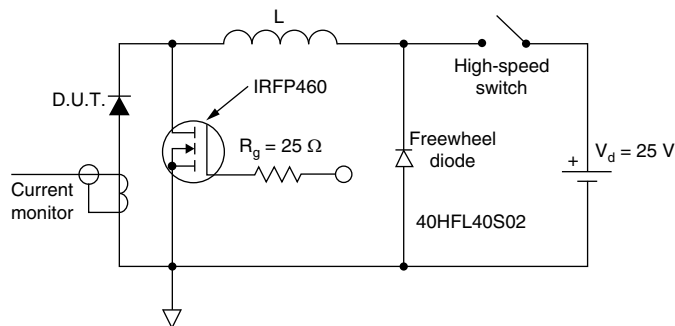


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 10\text{ V}$



ORDERING INFORMATION TABLE

Device code	VS-	42	C	T	Q	030	-M3
	1	2	3	4	5	6	7

- | | | |
|---|---|--|
| 1 | - | Vishay Semiconductors product |
| 2 | - | Current rating (40 A) |
| 3 | - | Circuit configuration
C = common cathode |
| 4 | - | Package
T = TO-220 |
| 5 | - | Schottky "Q" series |
| 6 | - | Voltage rating (030 = 30 V) |
| 7 | - | Environmental digit
-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free |

ORDERING INFORMATION (Example)

PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-42CTQ030-M3	50	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?96154
Part marking information	www.vishay.com/doc?95028



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