

Comparator, Single Channel, Open Collector, Low Power, Wide Supply Range

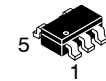
TL331, TL331V

Description

The TL331 is an open collector, low-power comparator designed specifically to operate over a wide supply range from 2 V to 36 V single supply and ± 1 V to ± 18 V for split supplies. The input common-mode voltage range includes ground, even when operated from a single power supply voltage. TL331 comes in a space saving TSOP-5 package and is also available in an automotive qualified version.

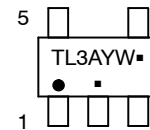
Features

- Wide Single Supply Voltage Range or Dual Supplies
- Low Supply Current: 0.5 mA Typical
- Low Input Bias Current: 25 nA Typical
- Low Input Offset Current: ± 5 nA Typical
- Low Input Offset Voltage: ± 2 mV Typical
- Input Common Mode Voltage Range includes Ground
- Low Output Saturation Voltage: 150 mV Typ at $I_O = 4$ mA
- Differential Input Voltage Range Equal to the Supply Voltage
- TTL, DTL, ECL, CMOS Compatible Devices
- TL331V for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant



TSOP-5
SN SUFFIX
CASE 483

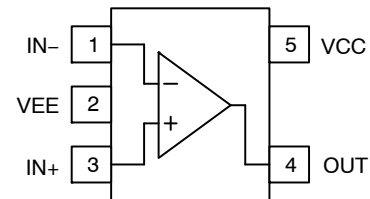
MARKING DIAGRAM



TL3 = Specific Device Code
A = Assembly Location
Y = Year
W = Work Week
■ = Pb-Free Package

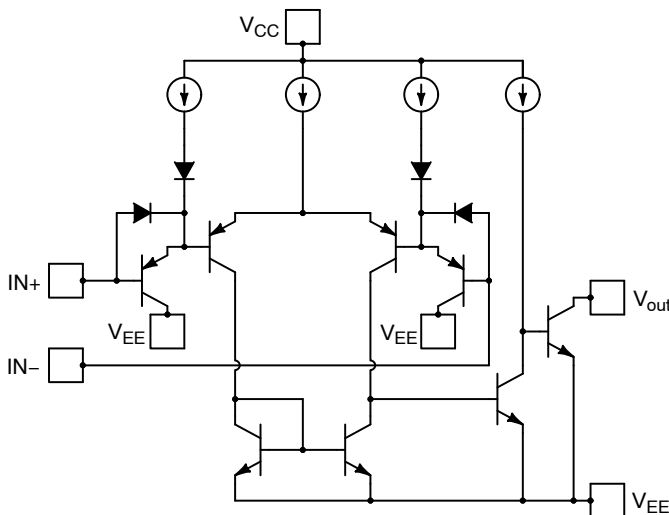
(Note: Microdot may be in either location)

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.



TL331, TL331V

DEVICE ORDERING INFORMATION

Device	Automotive	Package	Shipping†
TL331SN4T1G	No	TSOP-5 (Pb-Free)	3000 / Tape & Reel
TL331SN4T3G*	No		
TL331VSN4T1G	Yes		
TL331VSN4T3G*	Yes		

*Discontinued part number. Not recommended for new designs.

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Table 1. MAXIMUM RATINGS (Over operating free-air temperature, unless otherwise stated)

Parameter	Symbol	Limit	Unit
Supply Voltage ($V_{CC} - V_{EE}$)	V_S	36	V

INPUT AND OUTPUT PINS

Input Voltage (Note 1)	V_{IN}	± 36	V
Differential Input Voltage (Note 1)	V_{ID}	-0.3 to 36	V
Output Short Circuit Current (Note 2)	I_{SC}	20	mA

TEMPERATURE

Storage Temperature	T_{STG}	-65 to +150	°C
Junction Temperature	T_J	+150	°C

ESD RATINGS

Human Body Model	HBM	2000	V
Charged Device Model	CDM	2500	V
Machine Model	MM	150	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Positive excursions of the input voltage may exceed the power supply level. The low input voltage state must not be less than 0.3 V below the negative supply rail.
2. Short circuits from the output to V_{CC} can cause excessive heating and potential destruction. The maximum short circuit current is independent of the magnitude of V_{CC} .

Table 2. THERMAL INFORMATION (Note 3)

Parameter	Symbol	Single Layer Board (Note 4)	Multi-Layer Board (Note 5)	Unit
Junction to Ambient Thermal Resistance	θ_{JA}	274	209	°C/W

3. Short-circuits can cause excessive heating and destructive dissipation. These values are typical.

4. Values based on a 1S standard PCB according to JEDEC 51-3 with 1.0 oz copper and a 400 mm² copper area

5. Values based on a 1S2P standard PCB according to JEDEC 51-7 with 1.0 oz copper and a 25 mm² copper area

Table 3. OPERATING CONDITIONS

Parameter	Symbol	Limit	Unit
Operating Supply Voltage	V_S	2 to 36	V
Specified Operating Range	T_A	-40 to +125	°C

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

TL331, TL331V

Table 4. ELECTRICAL CHARACTERISTICS ($V_S = +5.0$ V, At $T_A = +25^\circ\text{C}$, $V_{CM} = \text{mid-supply}$, unless otherwise noted)

Boldface limits apply over the specified temperature range, $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
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INPUT CHARACTERISTICS

Input Offset Voltage	V_{OS}	$V_O = 1.4$ V, $R_S = 0$ Ω , $V_S = 5$ V to 30 V	$V_{CM} = 0$ to $V_{CC} - 1.5$ V		1	5	mV
			$V_{CM} = 0$ to $V_{CC} - 2$ V			9	mV
Input Bias Current	I_{IB}			-25	-250		nA
					-400		nA
Input Offset Current	I_{OS}			5	50		nA
					150		nA
Input Common Mode Range (Note 6)	V_{ICMR}		0		$V_{CC} - 1.5$		V
Differential Input Voltage (Note 7)	V_{ID}				V_{CC}		V

OUTPUT CHARACTERISTICS

Output Voltage Low	V_{OL}	$V_{ID} = -1$ V, $I_O = 4$ mA		150	400		mV
					700		mV
Output Sink Current	I_O	$V_{ID} = -1$ V, $V_O = 1.5$ V	6	16			mA
Output Leakage Current	I_{OH}	$V_{ID} = 1$ V, $V_{CC} = V_O = 5$ V		0.1	50		nA
		$V_{ID} = 1$ V, $V_{CC} = V_O = 30$ V			1		μA

DYNAMIC PERFORMANCE

Large Signal Differential Voltage Gain	A_{VD}	$V_{CC} = 15$ V, $R_{PU} = 15$ k Ω , $V_O = 1.4$ V to 11.4 V	50	200			V/mV
Propagation Delay L-H (Note 8)	t_{PLH}	5 mV overdrive, $R_{PU} = 5.1$ k Ω		850			ns
		20 mV overdrive, $R_{PU} = 5.1$ k Ω		600			ns
		100 mV overdrive, $R_{PU} = 5.1$ k Ω		400			ns
		TTL Input, $V_{ref} = +1.4$ V, $R_{PU} = 5.1$ k Ω		300			ns
Propagation Delay H-L	t_{PHL}	5 mV overdrive, $R_{PU} = 5.1$ k Ω		700			ns
		20 mV overdrive, $R_{PU} = 5.1$ k Ω		400			ns
		100 mV overdrive, $R_{PU} = 5.1$ k Ω		250			ns
		TTL Input, $V_{ref} = +1.4$ V, $R_{PU} = 5.1$ k Ω		300			ns

POWER SUPPLY

Quiescent Current	I_{CC}	No load, $V_{CC} = 5$ V		0.5	0.7		mA
		No load, $V_{CC} = 30$ V		0.6	1.25		mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- The input common mode voltage of either input signal should not be allowed to go negative by more than 0.3 V. The upper end of the common mode voltage range is $V_{CC} - 1.5$ V, but either or both inputs can go to +36 V without damage.
- Positive excursions of the input voltage may exceed the power supply level. As long as the other voltage remains within the common mode range, the comparator will provide a proper output stage. The low input voltage state must not be less than 0.3 V below the negative supply rail.
- TL331 is an open collector comparator. Rise time is a function of the RC time constant. A 5.1 k Ω pull-up resistor was used for these measurements.

TYPICAL CHARACTERISTICS

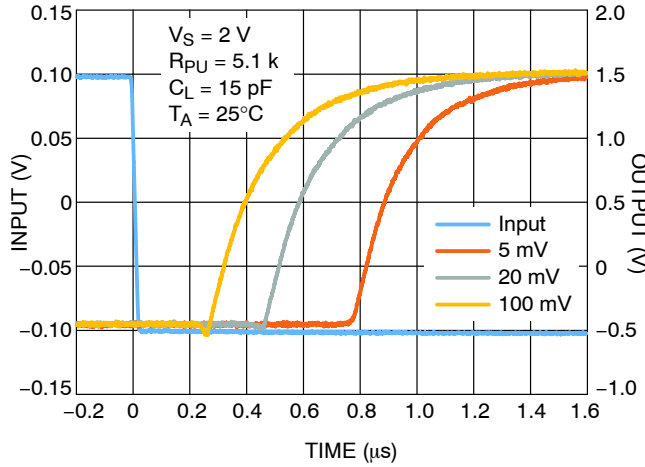


Figure 1. Low-to-High Propagation Delay vs. Overdrive at 2 V Supply

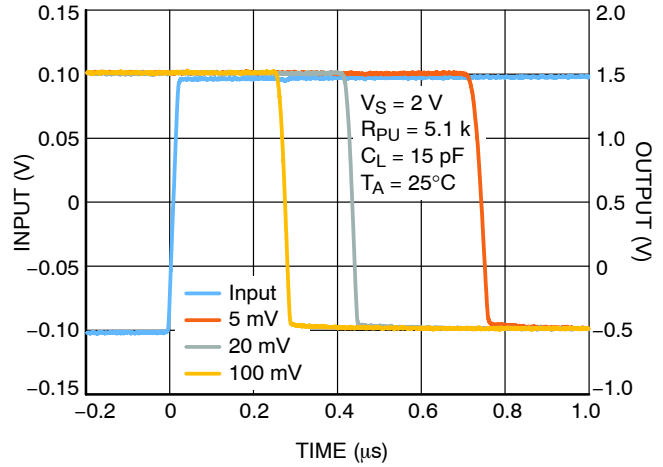


Figure 2. High-to-Low Propagation Delay vs. Overdrive at 2 V Supply

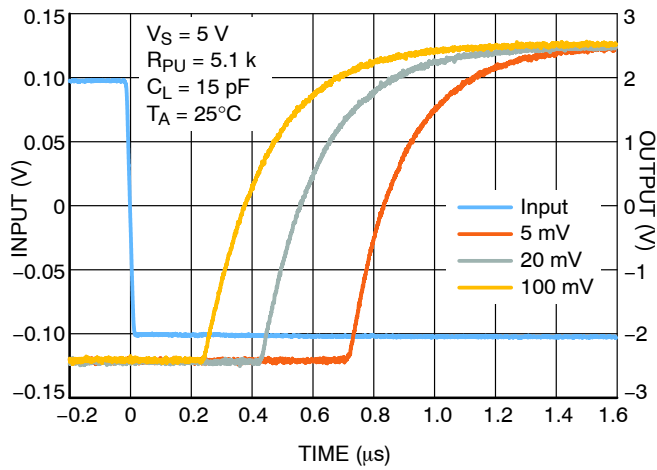


Figure 3. Low-to-High Propagation Delay vs. Overdrive at 5 V Supply

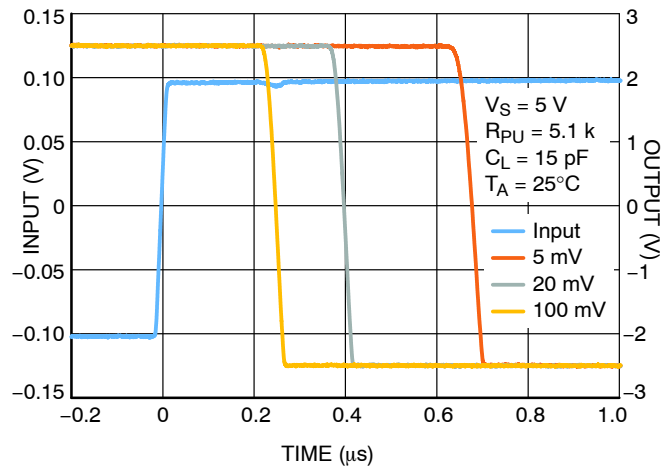


Figure 4. High-to-Low Propagation Delay vs. Overdrive at 5 V Supply

TYPICAL CHARACTERISTICS

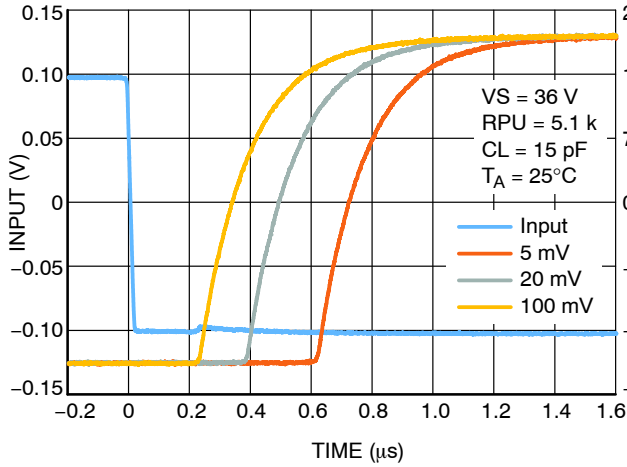


Figure 5. Low-to-High Propagation Delay vs. Overdrive at 36 V Supply

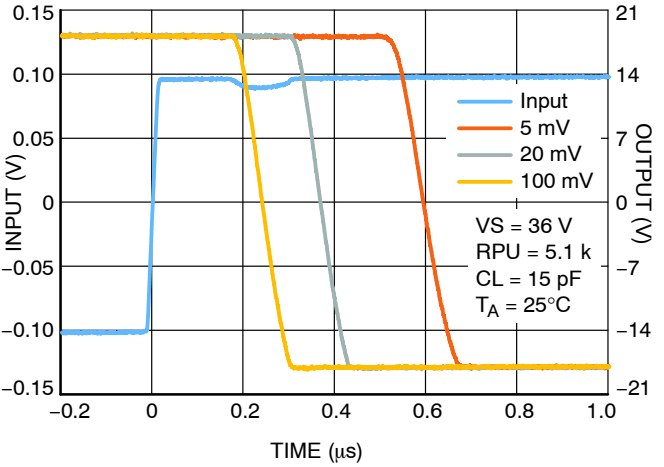


Figure 6. High-to-Low Propagation Delay vs. Overdrive at 36 V Supply

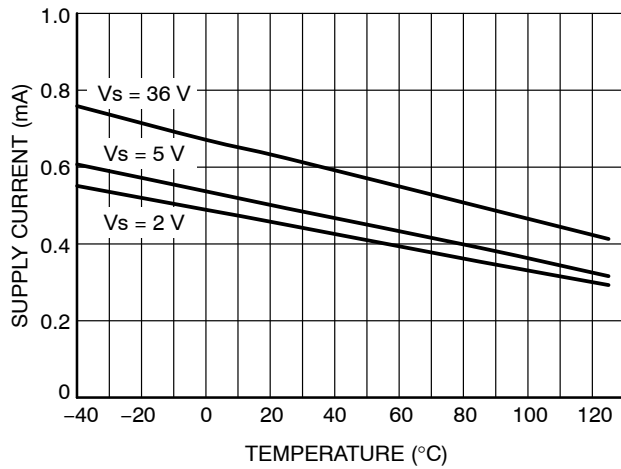


Figure 7. Quiescent Current vs. Temperature

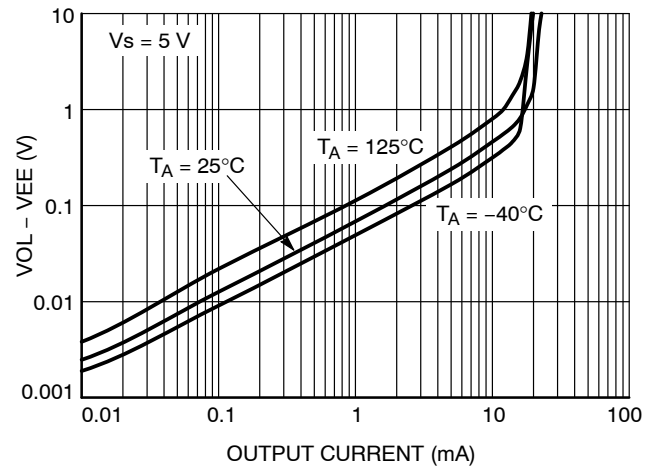
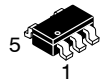


Figure 8. Low Level Output Voltage vs. Output Current at 5 V Supply

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

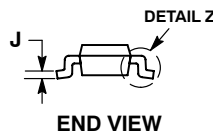
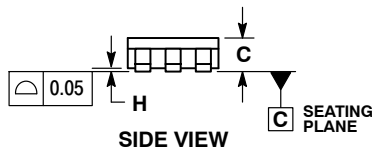
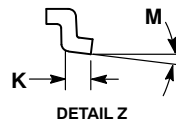
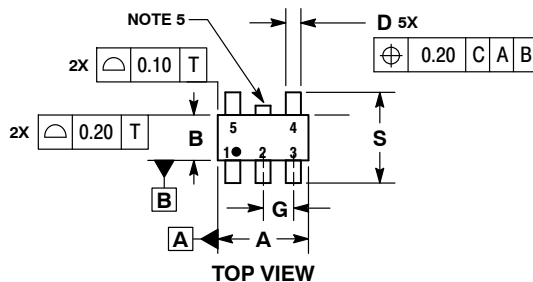
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SCALE 2:1

TSOP-5 CASE 483 ISSUE N

DATE 12 AUG 2020

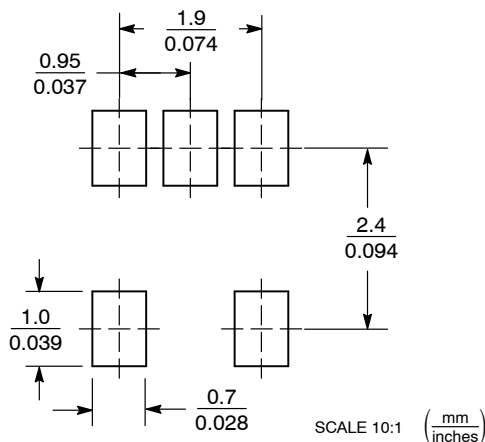


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSION A.
5. OPTIONAL CONSTRUCTION: AN ADDITIONAL TRIMMED LEAD IS ALLOWED IN THIS LOCATION. TRIMMED LEAD NOT TO EXTEND MORE THAN 0.2 FROM BODY.

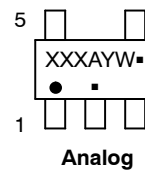
DIM	MILLIMETERS	
	MIN	MAX
A	2.85	3.15
B	1.35	1.65
C	0.90	1.10
D	0.25	0.50
G	0.95 BSC	
H	0.01	0.10
J	0.10	0.26
K	0.20	0.60
M	0°	10°
S	2.50	3.00

SOLDERING FOOTPRINT*

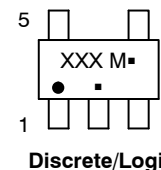


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



Analog



Discrete/Logic

XXX = Specific Device Code
 A = Assembly Location
 Y = Year
 W = Work Week
 ■ = Pb-Free Package

XXX = Specific Device Code
 M = Date Code
 ■ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present.

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