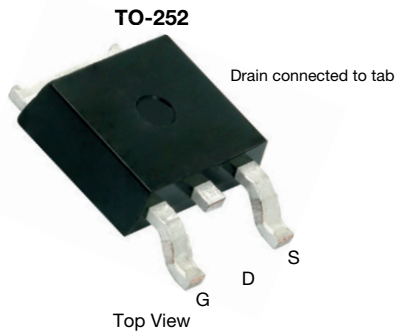
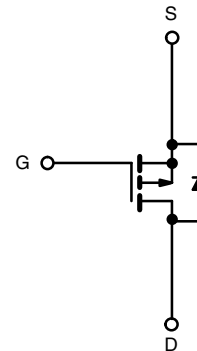


P-Channel 60 V (D-S), 175 °C MOSFET



FEATURES

- TrenchFET® power MOSFET
- 175 °C junction temperature
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT


P-Channel MOSFET

PRODUCT SUMMARY

V_{DS} (V)	-60
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -10$ V	0.015
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5$ V	0.020
I_D (A) ^d	-50
Configuration	Single

ORDERING INFORMATION

Package	TO-252
Lead (Pb)-free and halogen-free	SUD50P06-15L-E3

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V_{DS}	-60	V
Gate-source voltage	V_{GS}	± 20	
Continuous drain current ($T_J = 175$ °C)	I_D	$T_C = 25$ °C	-50 ^d
		$T_C = 125$ °C	-39
Pulsed drain current	I_{DM}	-80	A
Avalanche current	I_{AR}	-50	
Repetitive avalanche energy ^a	E_{AR}	125	mJ
Power dissipation	P_D	$T_C = 25$ °C	136 ^c
		$T_A = 25$ °C	3 ^{b, c}
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-ambient ^b	R_{thJA}	$t \leq 10$ s	15	°C/W
		Steady state	40	
Junction-to-case	R_{thJC}	0.82	1.1	

Notes

- Duty cycle $\leq 1\%$
- When mounted on 1" square PCB (FR4 material)
- See SOA curve for voltage derating
- Package limited



SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0\text{ V}$, $I_D = -250\text{ }\mu\text{A}$	-60	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250\text{ }\mu\text{A}$	-1	-	-3	
Gate-body leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -48\text{ V}$, $V_{GS} = 0\text{ V}$	-	-	-1	μA
		$V_{DS} = -48\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 125\text{ }^\circ\text{C}$	-	-	-50	
		$V_{DS} = -48\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 175\text{ }^\circ\text{C}$	-	-	-150	
On-state drain current ^a	$I_{D(on)}$	$V_{DS} = -5\text{ V}$, $V_{GS} = -10\text{ V}$	-50	-	-	A
Drain-source on-state resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}$, $I_D = -17\text{ A}$	-	0.012	0.015	Ω
		$V_{GS} = -10\text{ V}$, $I_D = -50\text{ A}$, $T_J = 125\text{ }^\circ\text{C}$	-	-	0.025	
		$V_{GS} = -10\text{ V}$, $I_D = -50\text{ A}$, $T_J = 175\text{ }^\circ\text{C}$	-	-	0.030	
		$V_{GS} = -4.5\text{ V}$, $I_D = -14\text{ A}$	-	-	0.020	
Forward transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}$, $I_D = -17\text{ A}$	-	61	-	S
Dynamic ^b						
Input capacitance	C_{ISS}	$V_{GS} = 0\text{ V}$, $V_{DS} = -25\text{ V}$, $f = 1\text{ MHz}$	-	4950	-	μF
Output capacitance	C_{OSS}		-	480	-	
Reverse transfer capacitance	C_{RSS}		-	405	-	
Total gate charge ^c	Q_g	$V_{DS} = -30\text{ V}$, $V_{GS} = -10\text{ V}$, $I_D = -50\text{ A}$	-	110	165	nC
Gate-source charge ^c	Q_{gs}		-	19	-	
Gate-drain charge ^c	Q_{gd}		-	28	-	
Turn-on delay time ^c	$t_{d(on)}$	$V_{DD} = -30\text{ V}$, $R_L = 0.6\text{ }\Omega$ $I_D \cong -50\text{ A}$, $V_{GEN} = -10\text{ V}$, $R_G = 6\text{ }\Omega$	-	15	23	ns
Rise time ^c	t_r		-	70	105	
Turn-off delay time ^c	$t_{d(off)}$		-	175	260	
Fall time ^c	t_f		-	175	260	
Source-Drain Diode Ratings and Characteristics ($T_C = 25\text{ }^\circ\text{C}$) ^b						
Continuous current	I_S		-	-	-50	A
Pulsed current	I_{SM}		-	-	-80	
Forward voltage ^a	V_{SD}	$I_F = -50\text{ A}$, $V_{GS} = 0\text{ V}$	-	1	1.6	V
Reverse recovery time	t_{rr}	$I_F = -50\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$	-	45	70	ns

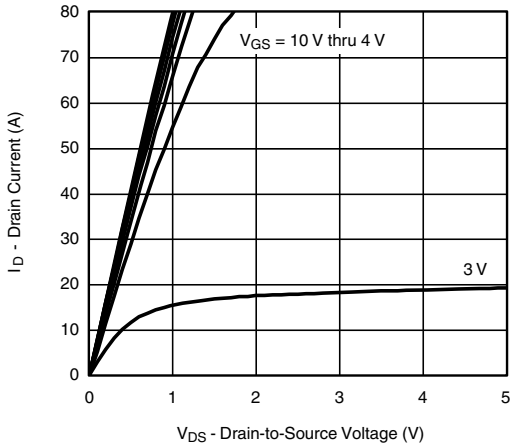
Notes

- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
- Guaranteed by design, not subject to production testing
- Independent of operating temperature

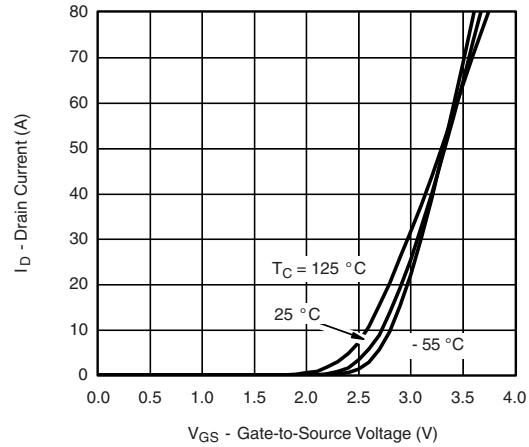
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



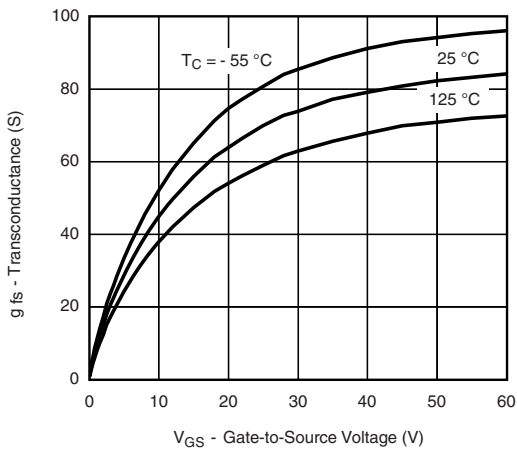
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



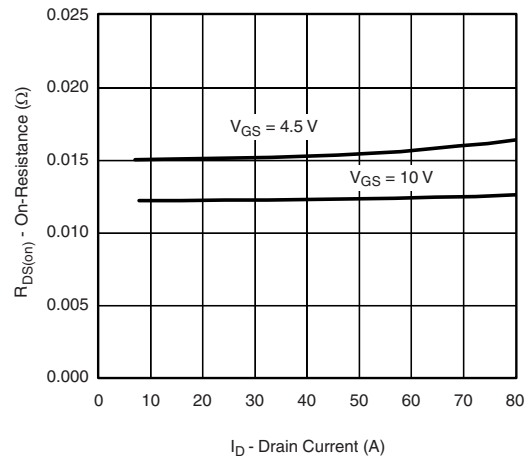
Output Characteristics



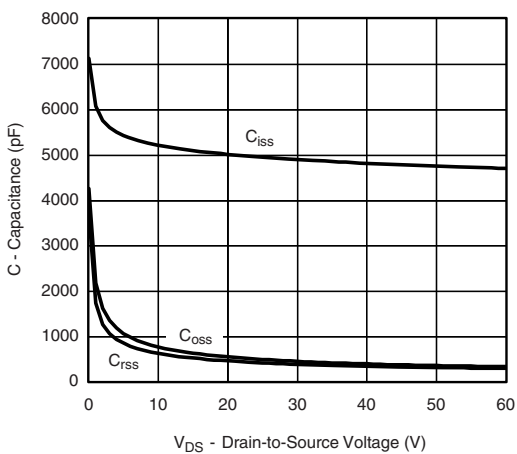
Transfer Characteristics



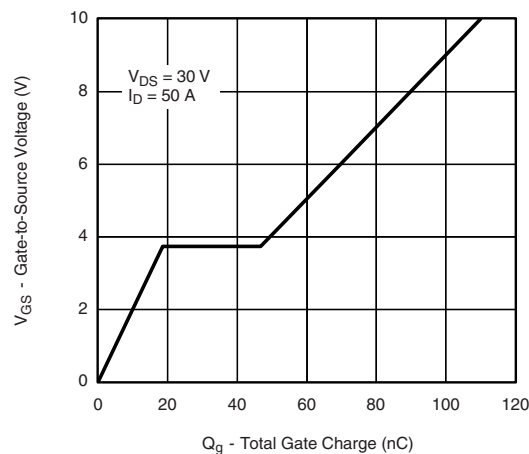
Transconductance



On-Resistance vs. Drain Current



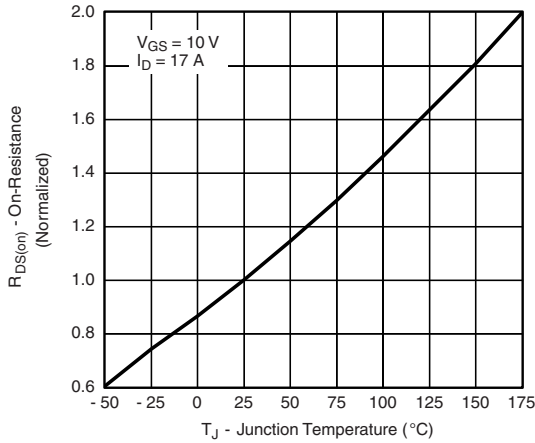
Capacitance



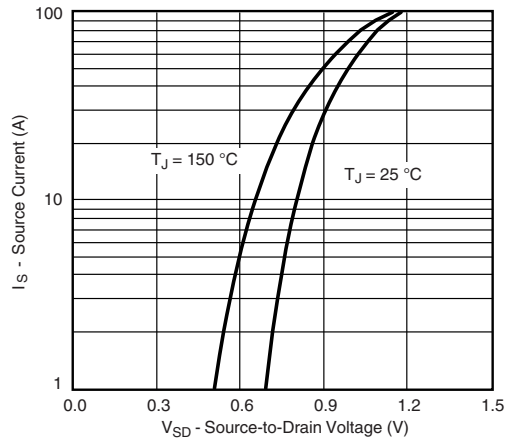
Gate Charge



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

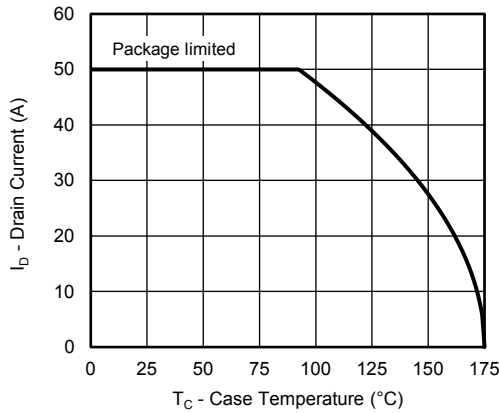


On-Resistance vs. Junction Temperature

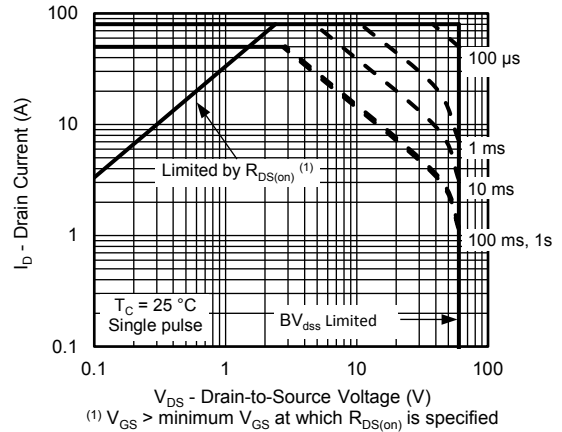


Source-Drain Diode Forward Voltage

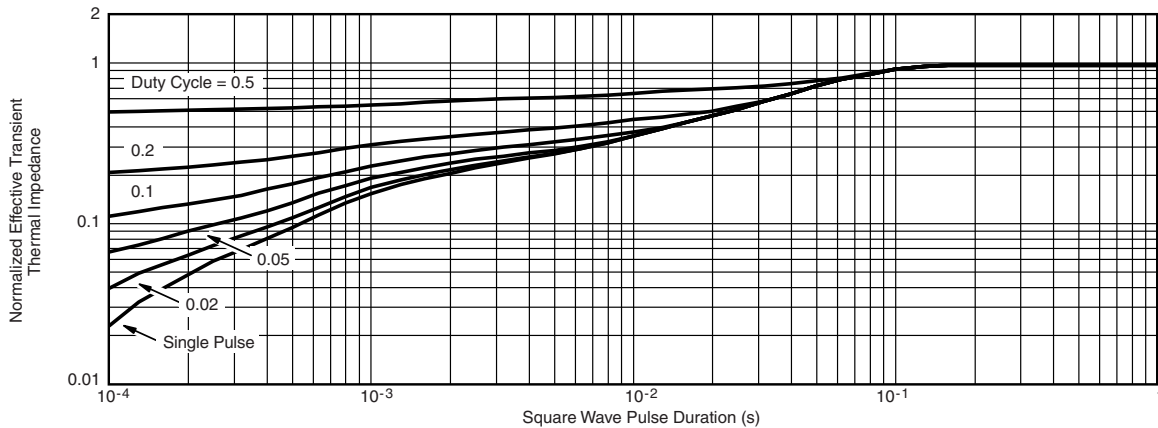
THERMAL RATINGS



Max. Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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