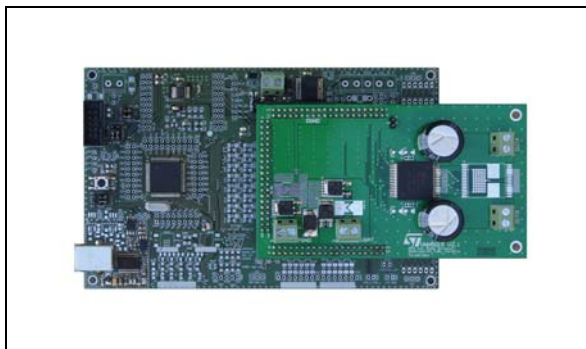


Motor driver evaluation board based on VNH5019A

Data brief



Features

Type	$R_{DS(on)}$	I_{out}	$V_{CC(max)}$
VNH5019A-E	18 mΩ typ (per leg)	30 A	41 V

- Handling up to 30 A of maximum motor current output
- Undervoltage and overvoltage shutdown
- Device thermal protection
- Cross-conduction protection
- Very low standby power consumption
- Programmable PWM operation (up to 20 kHz)
- Protection against loss of ground and loss of V_{CC}
- Motor current monitoring (thanks to VNH5019A current sense output)
- On-board charge pump output for reverse polarity protection
- Device output protected against short to ground and short to V_{CC}
- Graphic User Interface (GUI)

Description

STEVAL-VNH5019A offers dedicated power stage and controls suitable for electric DC motor driving. This evaluation board comes pre-assembled with VNH5019A H-bridge belonging to the VNH Motor Driver series based on VIPower® proprietary technology. Typical applications are Windows Lift, Sliding Door, Seat regulation.

This Evaluation Board consists of a motherboard (STM8 Universal Board) and a daughterboard.

The motherboard, based on STM8 microcontroller, provides the logic section for monitoring and driving the VNH5019A assembled in the daughterboard.

With the aim to make simpler the board usage and settings, ST provides a dedicated and user-friendly software with a Graphic User Interface (GUI). This enables the user to set VNH5019A parameters (PWM, Motor direction...) and at the same time it shows real time device diagnostic information like current output evolution, battery voltage monitoring, board temperature and much more.

Table 1. Device summary

Order code	Reference
STEVAL-VNH5019A	VNH5019A evaluation board

1 Application schematics and layouts

1.1 VNH5019A daughterboard

Figure 1. VNH5019A daughterboard top layer

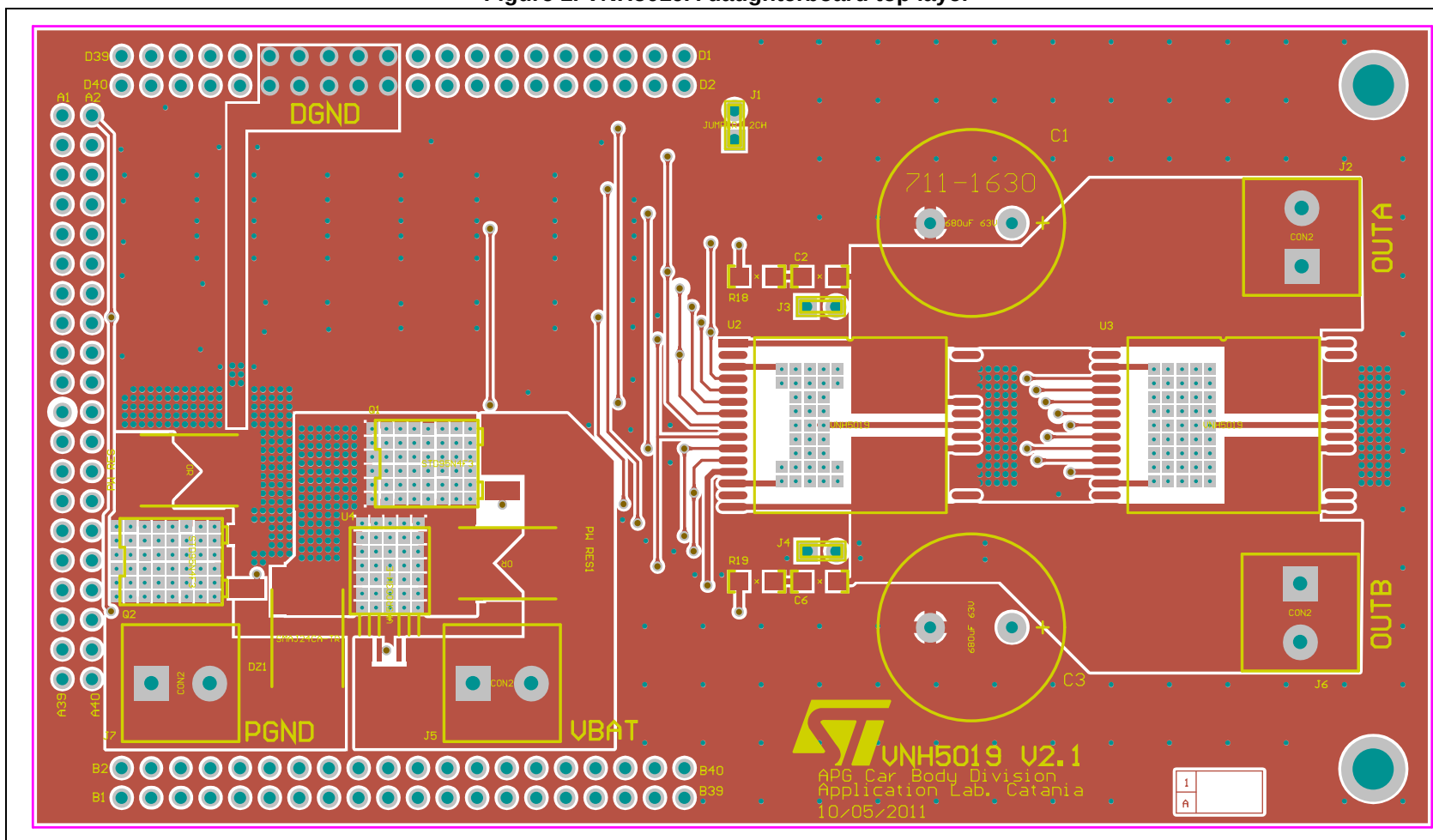
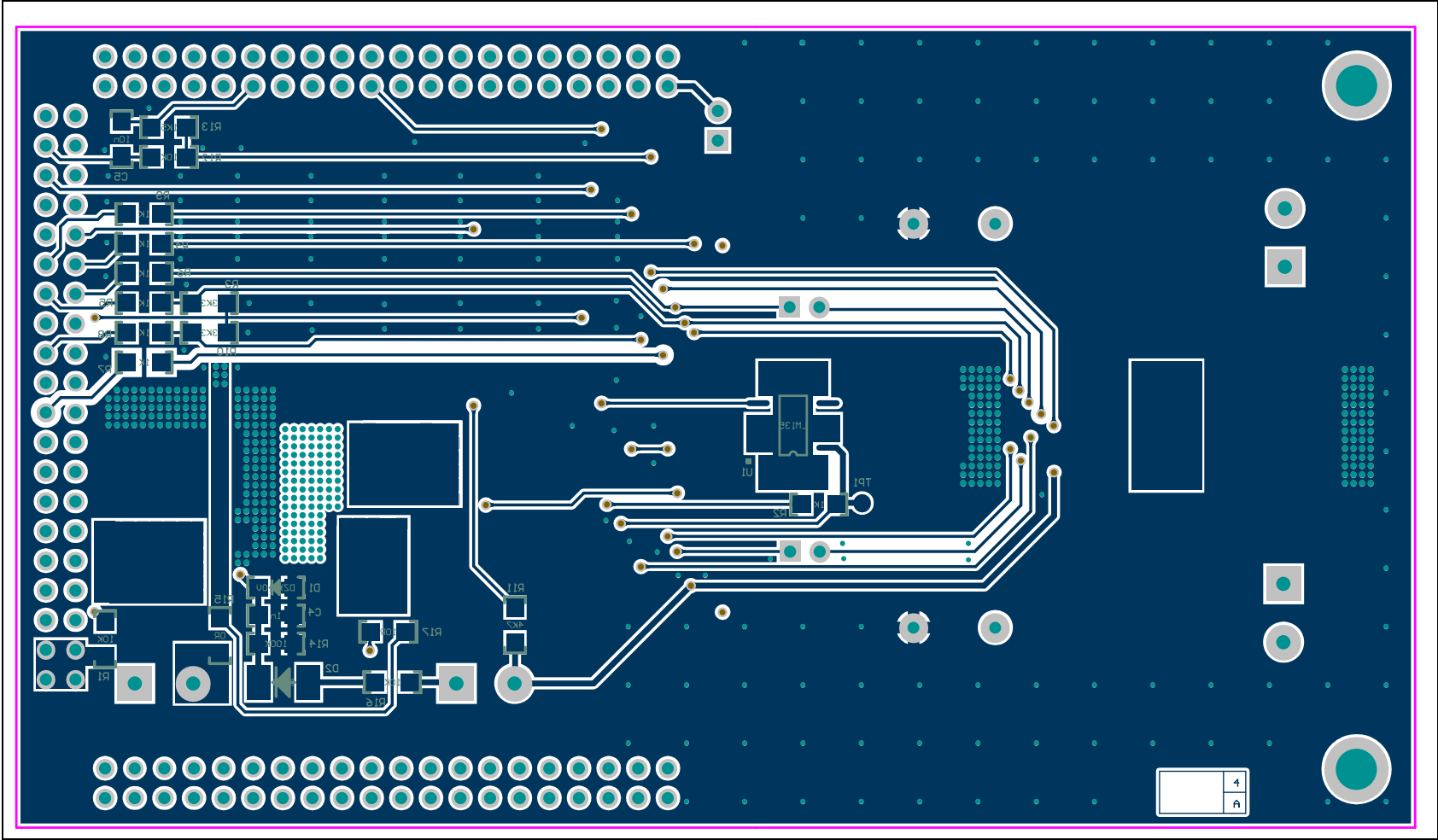
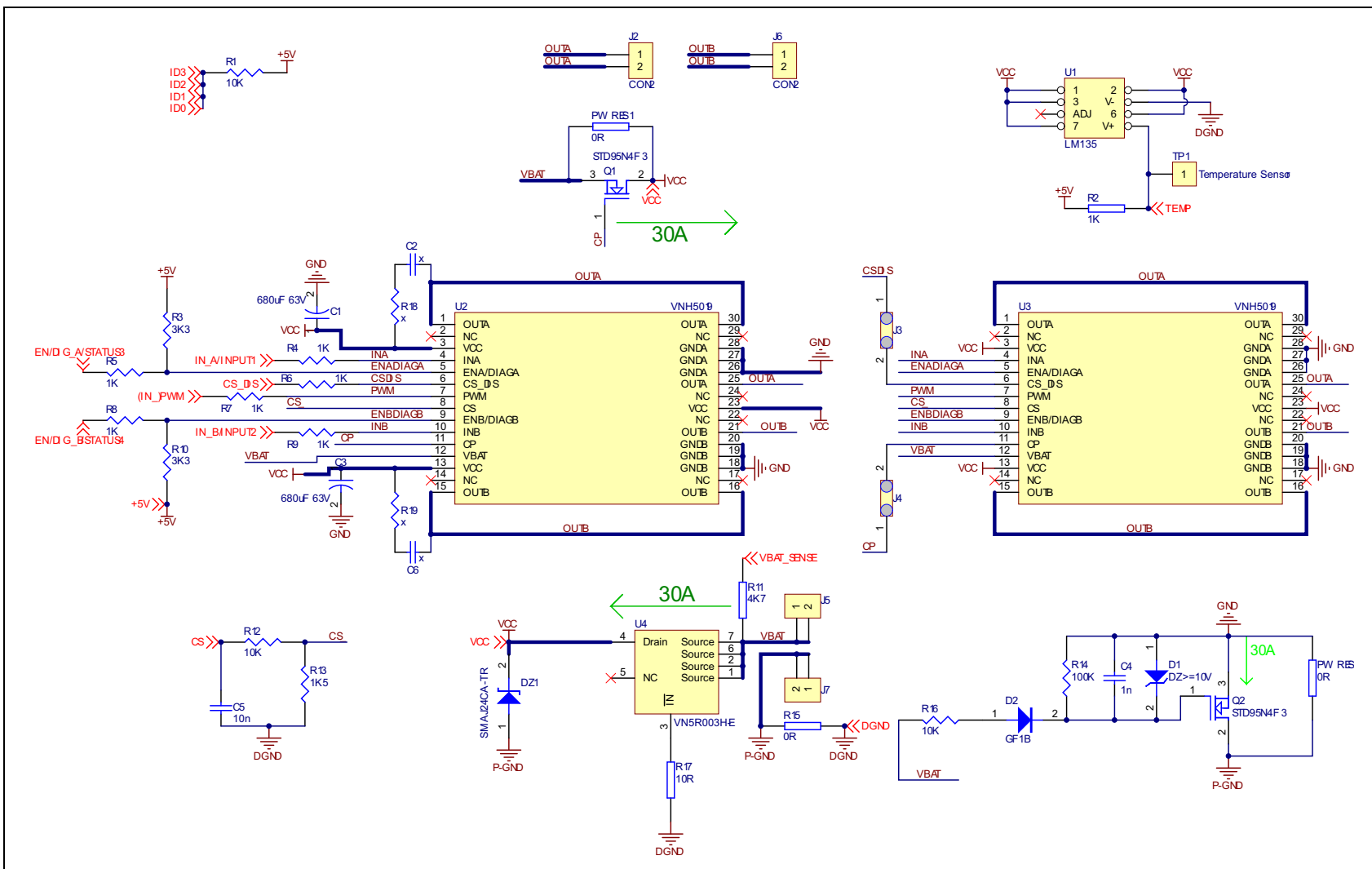


Figure 2. VNH5019A daughterboard bottom layer



Pinout diagram for the STM8UB_DBG1 daughterboard. The diagram shows a 40-pin header on the left and a 40-pin header on the right. The left header pins are labeled with various signals including power (V5V_DB, V5V_Perm), I/O (PB0/AI_N0/TI_M1_NCC1, etc.), and control (NRESET, PA6/USART_CX, etc.). The right header pins are labeled with power (V5V_DB, V5V_Perm), I/O (V5V_A, V5V_B, etc.), and control (CP). A red arrow points to the DGNDD pin on the left header. A red arrow points to the VCC pin on the right header. A red arrow points to the GND pin on the right header. A red arrow points to the CP pin on the right header. A red arrow points to the D1 pin on the right header. A red arrow points to the D2 pin on the right header. A red arrow points to the D3 pin on the right header. A red arrow points to the D4 pin on the right header. A red arrow points to the D5 pin on the right header. A red arrow points to the D6 pin on the right header. A red arrow points to the D7 pin on the right header. A red arrow points to the D8 pin on the right header. A red arrow points to the D9 pin on the right header. A red arrow points to the D10 pin on the right header. A red arrow points to the D11 pin on the right header. A red arrow points to the D12 pin on the right header. A red arrow points to the D13 pin on the right header. A red arrow points to the D14 pin on the right header. A red arrow points to the D15 pin on the right header. A red arrow points to the D16 pin on the right header. A red arrow points to the D17 pin on the right header. A red arrow points to the D18 pin on the right header. A red arrow points to the D19 pin on the right header. A red arrow points to the D20 pin on the right header. A red arrow points to the D21 pin on the right header. A red arrow points to the D22 pin on the right header. A red arrow points to the D23 pin on the right header. A red arrow points to the D24 pin on the right header. A red arrow points to the D25 pin on the right header. A red arrow points to the D26 pin on the right header. A red arrow points to the D27 pin on the right header. A red arrow points to the D28 pin on the right header. A red arrow points to the D29 pin on the right header. A red arrow points to the D30 pin on the right header. A red arrow points to the D31 pin on the right header. A red arrow points to the D32 pin on the right header. A red arrow points to the D33 pin on the right header. A red arrow points to the D34 pin on the right header. A red arrow points to the D35 pin on the right header. A red arrow points to the D36 pin on the right header. A red arrow points to the D37 pin on the right header. A red arrow points to the D38 pin on the right header. A red arrow points to the D39 pin on the right header. A red arrow points to the D40 pin on the right header.

Figure 4. VNH5019A daughterboard application schematic (part 2)



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Figure 7. STM8 motherboard - I/O & Body application schematic

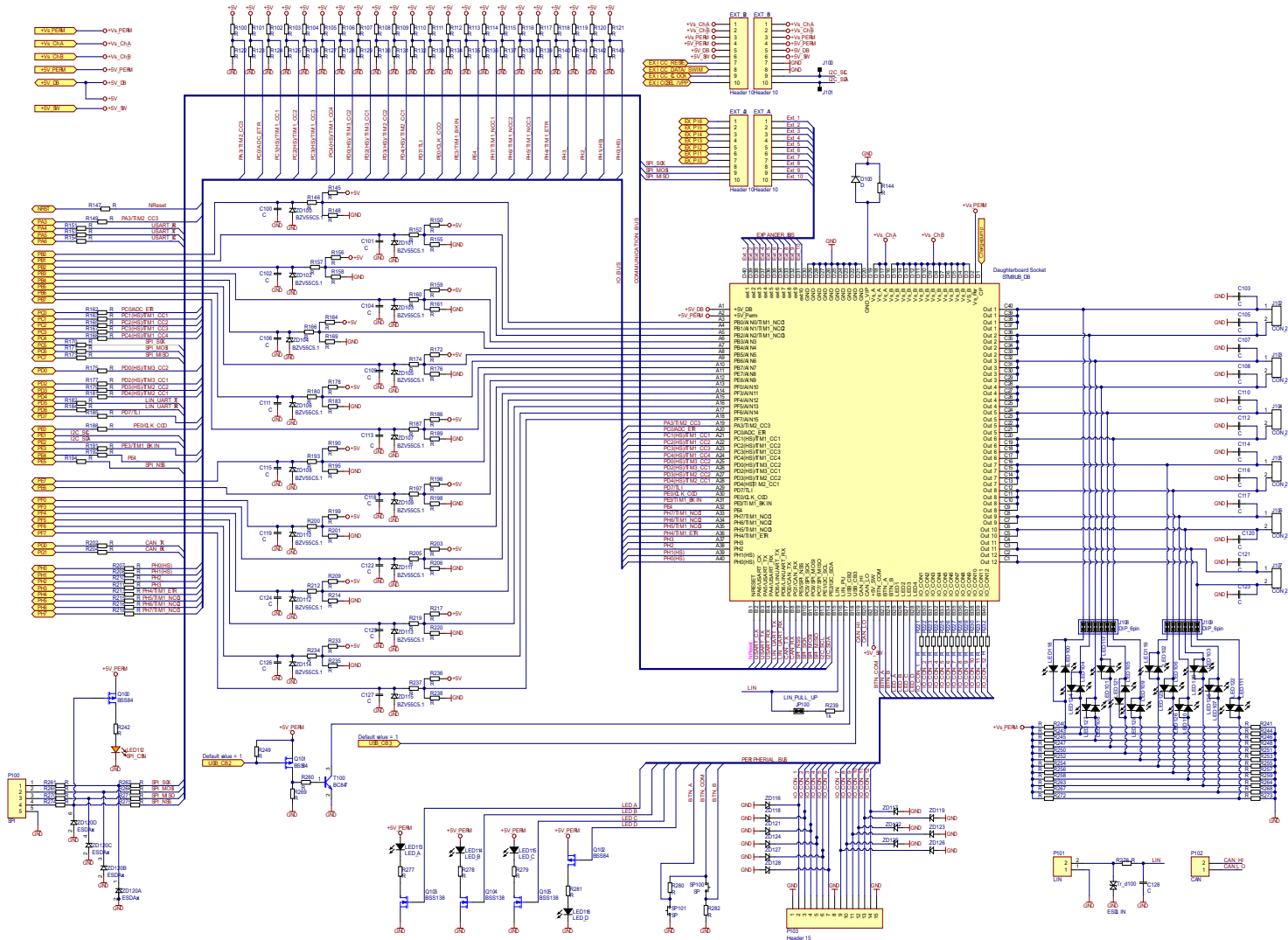


Figure 8. STM8 motherboard - STM8 & Supply application schematic

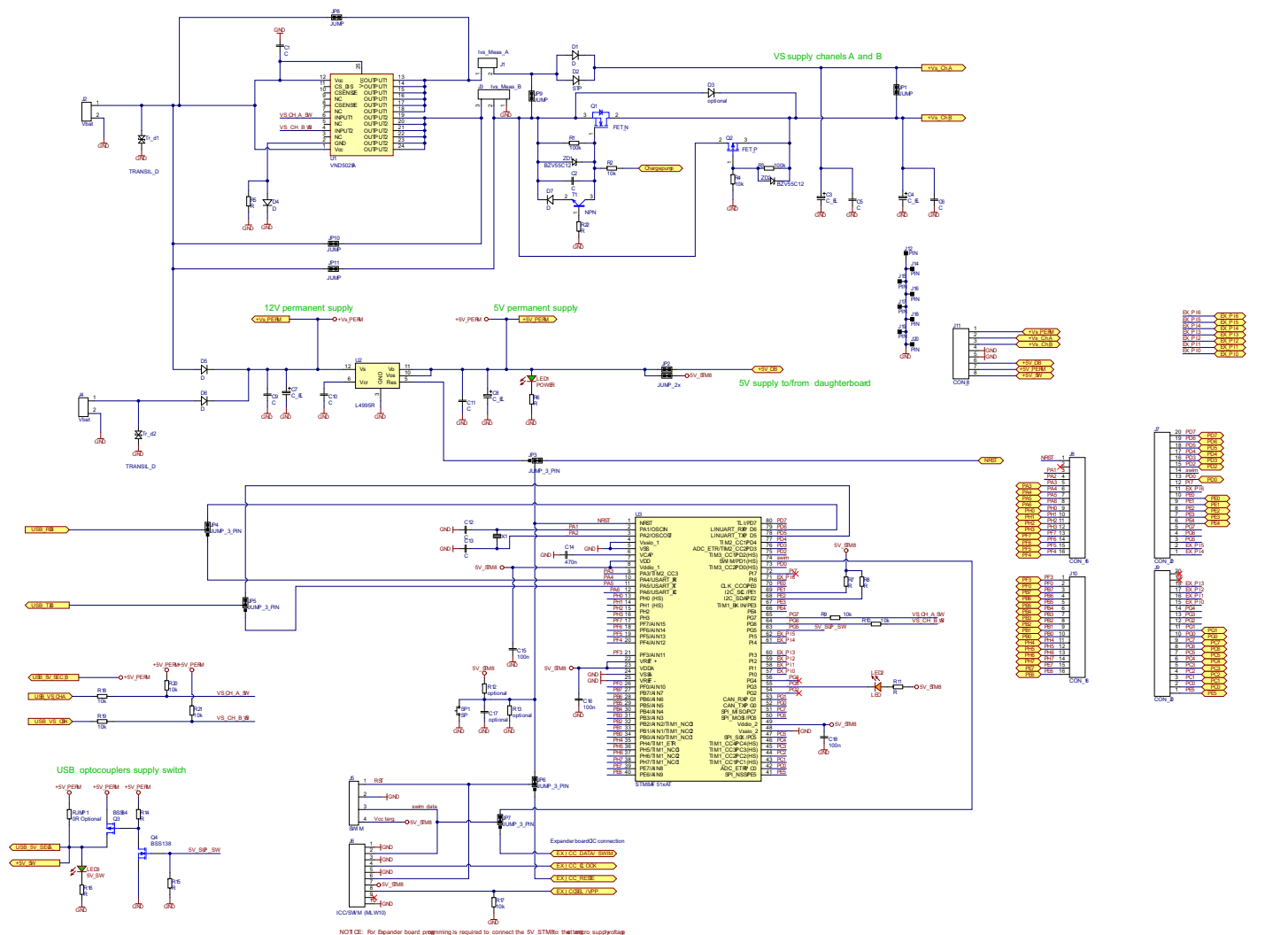
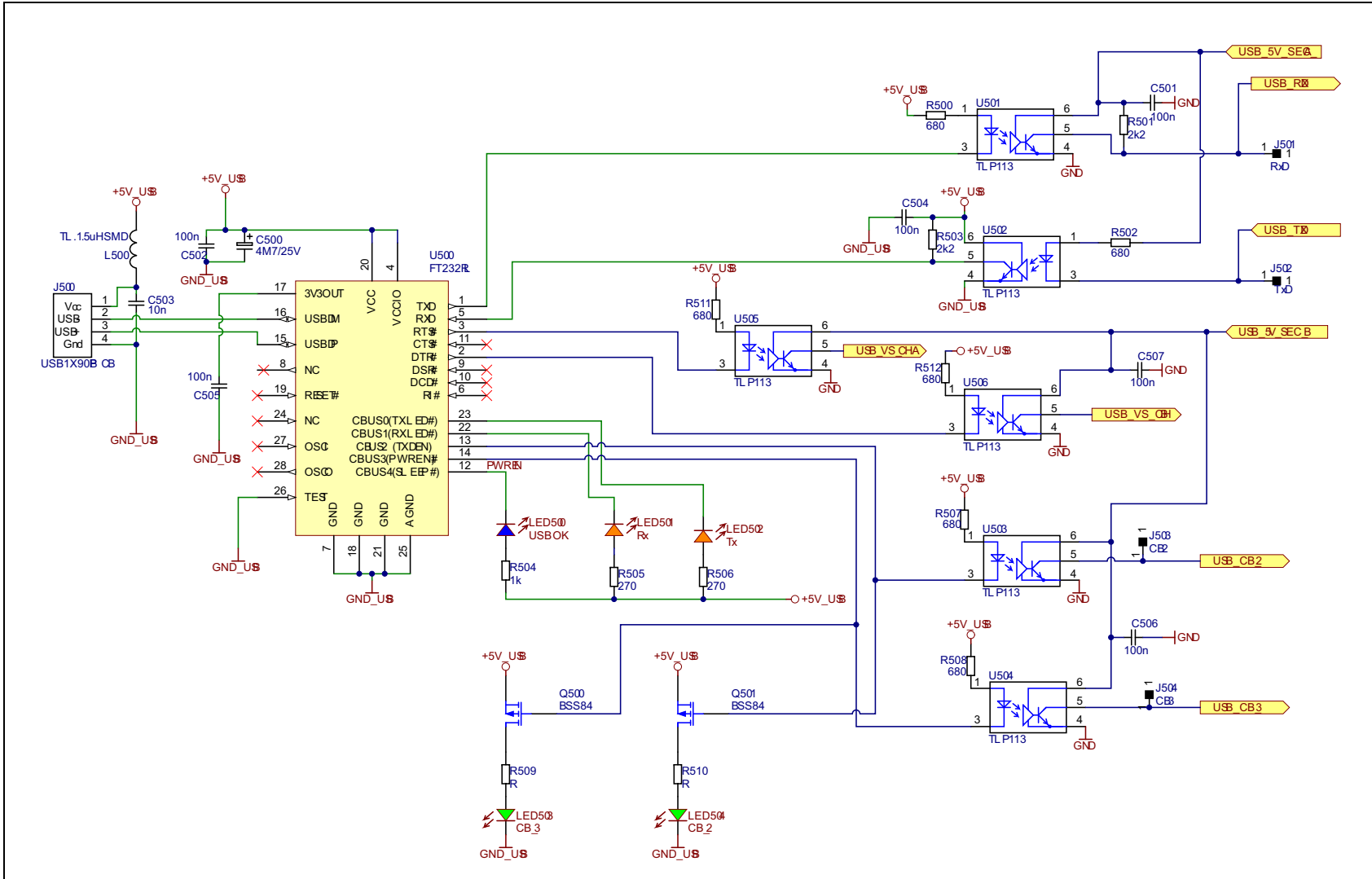


Figure 9. STM8 motherboard - USB Interface application schematic



2 Revision history

Table 2. Document revision history

Date	Revision	Changes
22-Jul-2013	1	Initial release.
06-Sep-2013	2	Updated Section 1.2: STM8 motherboard
16-Sep-2013	3	Updated disclaimer.

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