

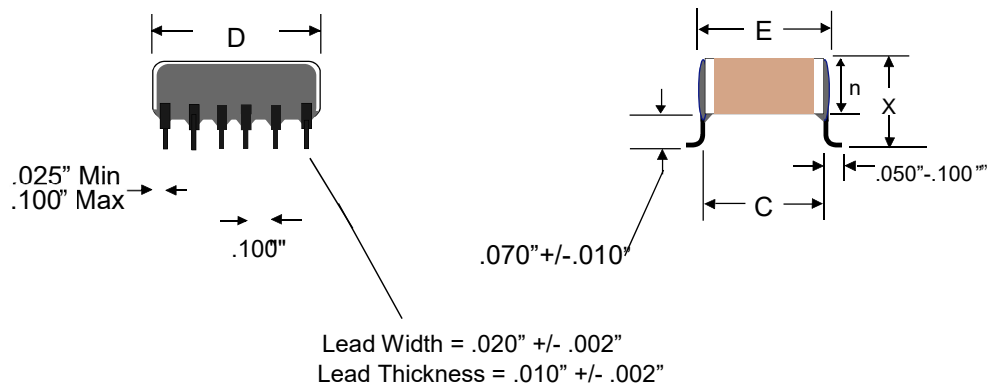
1. SCOPE

This specification covers the characteristics of the "ST" series size 6560, R2D dielectric, 0.30 uF, +/- 10% tolerance, 1500 VDC rated stacked ceramic chip capacitor with "L" shaped leads.

2. DIELECTRIC CHARACTERISTICS

Operating Temperature Range:	-55°C to +125°C
Temperature Coefficient:	+25°C to +125°C -2200ppm/°C +/- 500 ppm/°C
Temperature Coefficient:	+25°C to -55°C -3330 to -1700 ppm/°C
Dissipation Factor:	0.1% Max. @ 25°C
Insulation Resistance, 25°C	>3.3 Giga-Ohms
Insulation Resistance, 125°C	>330 Mega-Ohms
Dielectric Withstanding Voltage:	1800 VDC
Aging Rate:	0% per decade
Test Parameters:	1KHz, 1.0 Volts +/- 0.2 Vrms, 25°C
Lead Material	Solder Coated Phosphor Bronze

3. PHYSICAL DIMENSIONS (NOT SCALED)



dimensions	E	D	X	C	Number of Leads per Side	n= Number of Units Stacked
inches (mm)	.730 (18.5)	.600 (15.2)	.380 (9.65)	.670 (17.0)	6	1
+/- in (mm)	max	.030 (.762)	max	.035 (.889)	n/a	n/a

4. PART NUMBER DESCRIPTION: **ST 6560 R 304 K 152 LL X - 1**

- ST = Stacked Capacitors Assembly
- 6560 = Size
- R = R2D Dielectric
- 304 = Cap Code (pF): First two digits are significant, third digit denotes number of zeros
- K = Capacitance tolerance: K = +/- 10%
- 152 = DC Voltage Rating: First two digits are significant, third digit denotes number of zeros
- LL = Leded, "L" Shaped Configuration
- X = Overall Height: .380" Max. (9.65mm Max.)
- 1 = Single Stacked Capacitor

REVISION: A

NOVACAP P/N: **ST6560R304K152LLX-1**

SPEC DESIGNER MK 9/22/20
SPEC MANAGER MK FOR BN 9/22/20
ENGINEERING MK FOR BN 9/22/20

