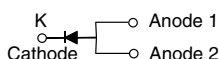


High Current Density Surface-Mount Schottky Barrier Rectifiers

eSMP® Series



SMPC (TO-277A)



FEATURES

- Very low profile - typical height of 1.1 mm
- Ideal for automated placement
- Guardring for overvoltage protection
- Low forward voltage drop, low power losses
- High efficiency
- Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
- Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

**AUTOMOTIVE
GRADE**
Available



**RoHS
COMPLIANT
HALOGEN
FREE**

LINKS TO ADDITIONAL RESOURCES



[3D Models](#)

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	10 A
V_{RRM}	30 V, 40 V
I_{FSM}	280 A
E_{AS}	20 mJ
V_F at $I_F = 10$ A	0.41 V
T_J max.	150 °C
Package	SMPC (TO-277A)
Circuit configuration	Single

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating
Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3_X - halogen-free, RoHS-compliant and AEC-Q101 qualified
("X" denotes revision code e.g. A, B,.....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise noted)

PARAMETER	SYMBOL	SS10P3	SS10P4	UNIT
Device marking code		S103	S104	
Maximum repetitive peak reverse voltage	V_{RRM}	30	40	V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$	10		A
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I_{FSM}	280		A
Non-repetitive avalanche energy at $I_{AS} = 2.0$ A, $T_J = 25$ °C	E_{AS}	20		mJ
Operating junction and storage temperature range	T_J, T_{STG}	-55 to +150		°C

**ELECTRICAL CHARACTERISTICS** ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	TEST CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	$I_F = 5\text{ A}$	$V_F^{(1)}$	0.41	-	V
	$I_F = 10\text{ A}$		0.48	0.56	
	$I_F = 5\text{ A}$		0.31	-	
	$I_F = 10\text{ A}$		0.41	0.49	
Reverse current	Rated V_R	$I_R^{(2)}$	100	800	μA
			50	100	mA
Typical junction capacitance	4.0 V, 1 MHz	C_J	750	-	pF

Notes(1) Pulse test: 300 μs pulse width, 1 % duty cycle(2) Pulse test: Pulse width $\leq 40\text{ ms}$ **THERMAL CHARACTERISTICS** ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	SS10P3	SS10P4	UNIT
Typical thermal resistance	R _{θJA} (1)	60		°C/W
	R _{θJL}	3		

Note

(1) Units mounted on recommended PCB 1 oz. pad layout

ORDERING INFORMATION (Example)

PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
SS10P4-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel
SS10P4-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel
SS10P4HM3_A/H ⁽¹⁾	0.10	H	1500	7" diameter plastic tape and reel
SS10P4HM3_A/I ⁽¹⁾	0.10	I	6500	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified

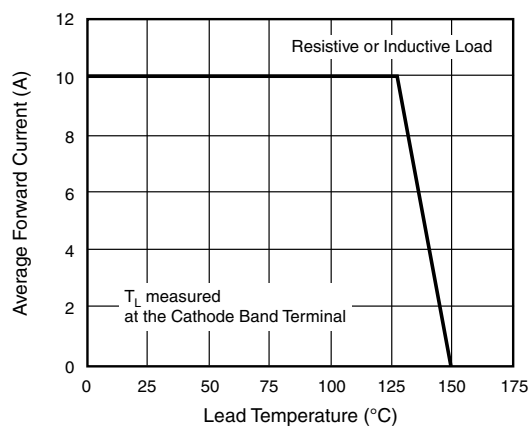
RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)


Fig. 1 - Maximum Forward Current Derating Curve

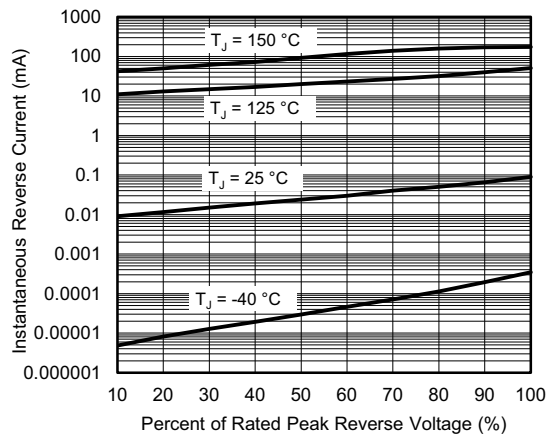


Fig. 4 - Typical Reverse Leakage Characteristics

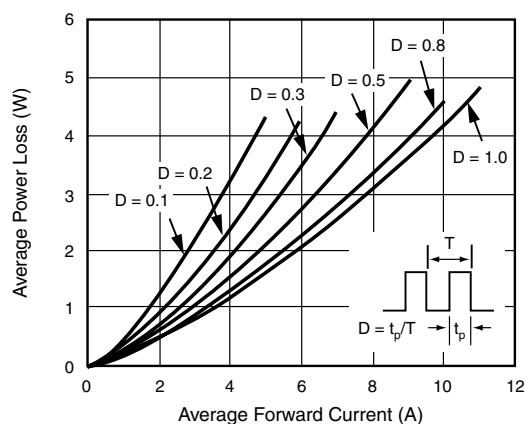


Fig. 2 - Forward Power Loss Characteristics

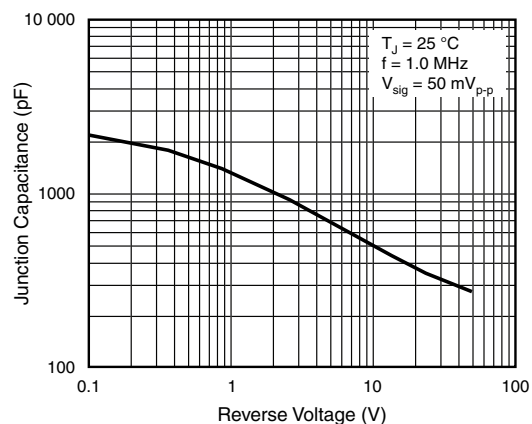


Fig. 5 - Typical Junction Capacitance

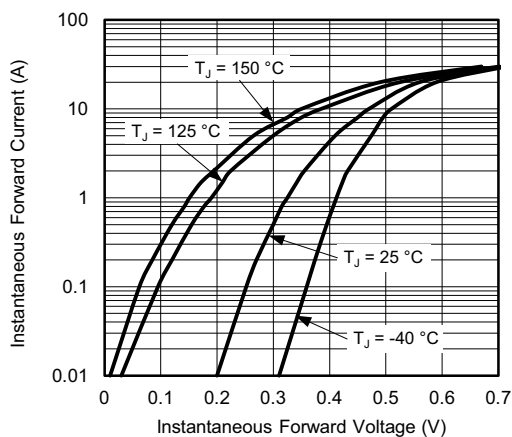


Fig. 3 - Typical Instantaneous Forward Characteristics

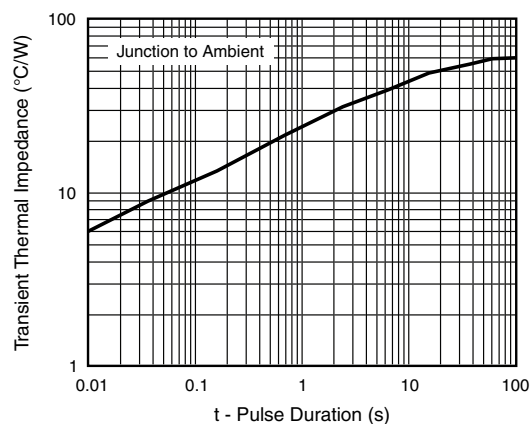
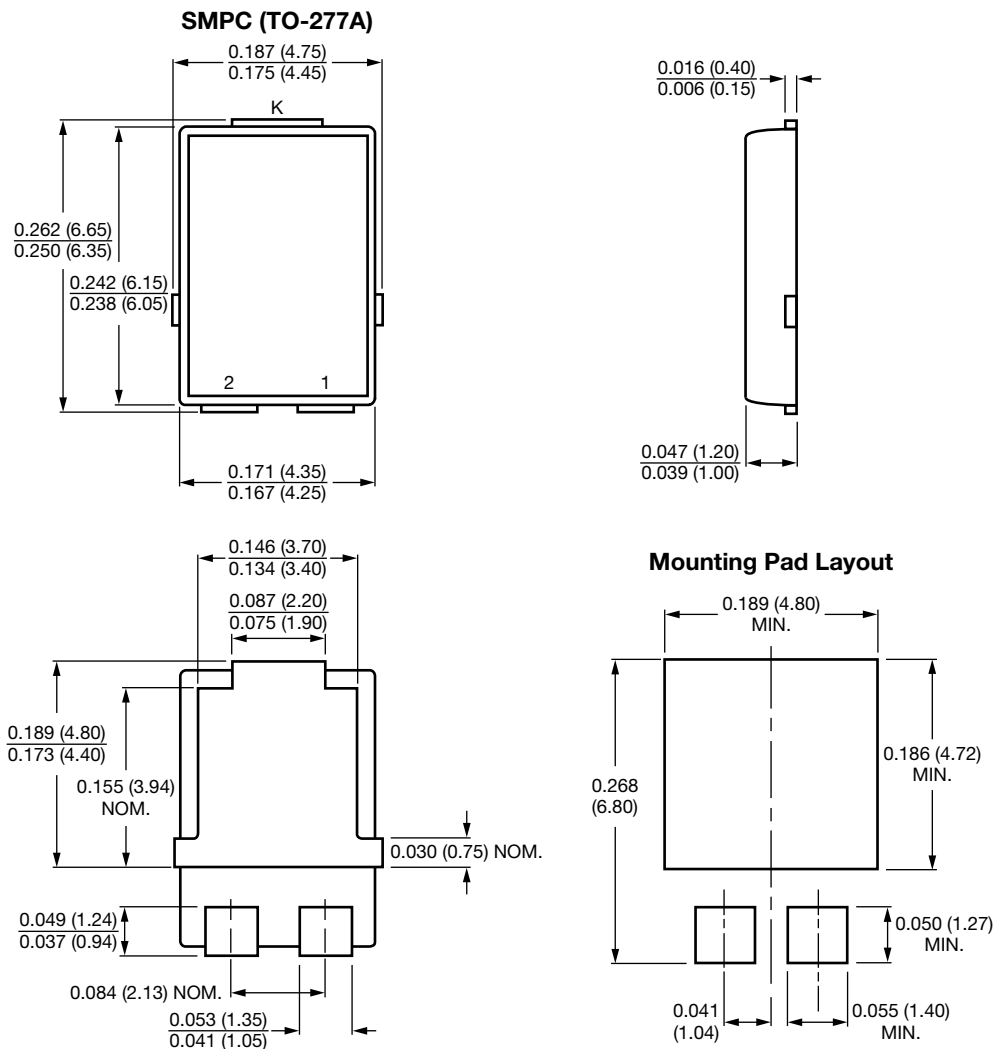


Fig. 6 - Typical Transient Thermal Impedance



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



Conform to JEDEC® TO-277A



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