



SEMITRANS® 3

Fast IGBT4 Modules

SKM400GM12T4

Features

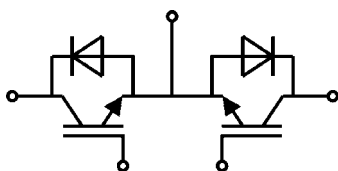
- $V_{CE(sat)}$ with positive temperature coefficient
- High short circuit capability, self limiting to $6 \times I_{Cnom}$
- Fast & soft inverse CAL diodes
- Large clearance (10 mm) and creepage distances (20 mm)
- Isolated copper baseplate using DBC Technology (Direct Copper Bonding)

Typical Applications

- Matrix Inverter

Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max, recomm.
 $T_{op} = -40 \dots +150^\circ\text{C}$, product rel. results valid for $T_j = 150^\circ$



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Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
IGBT				
V _{CES}			1200	V
I _C	T _j = 175 °C	T _c = 25 °C	618	A
		T _c = 80 °C	475	A
I _{Cnom}			400	A
I _{CRM}	I _{CRM} = 3xI _{Cnom}		1200	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 800 V V _{GE} ≤ 15 V V _{CES} ≤ 1200 V	T _j = 150 °C	10	µs
T _j			-40 ... 175	°C
Inverse diode				
I _F	T _j = 175 °C	T _c = 25 °C	440	A
		T _c = 80 °C	329	A
I _{Fnom}			400	A
I _{FRM}	I _{FRM} = 3xI _{Fnom}		1200	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		1980	A
T _j			-40 ... 175	°C
Module				
I _{t(RMS)}			500	A
T _{stg}			-40 ... 125	°C
V _{isol}	AC sinus 50Hz, t = 1 min		4000	V

Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_C = 400\text{ A}$ $V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25^\circ\text{C}$	1.8	2.05	V
		$T_j = 150^\circ\text{C}$	2.2	2.4	V
V_{CE0}		$T_j = 25^\circ\text{C}$	0.8	0.9	V
		$T_j = 150^\circ\text{C}$	0.7	0.8	V
r_{CE}	$V_{GE} = 15\text{ V}$	$T_j = 25^\circ\text{C}$	2.5	2.9	$\text{m}\Omega$
		$T_j = 150^\circ\text{C}$	3.8	4.0	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}$, $I_C = 15.2\text{ mA}$	5	5.8	6.5	V
I_{CES}	$V_{GE} = 0\text{ V}$ $V_{CE} = 1200\text{ V}$	$T_j = 25^\circ\text{C}$	0.1	0.3	mA
		$T_j = 150^\circ\text{C}$			mA
C_{ies}	$V_{CE} = 25\text{ V}$ $V_{GE} = 0\text{ V}$	$f = 1\text{ MHz}$	24.6		nF
C_{oes}		$f = 1\text{ MHz}$	1.62		nF
C_{res}		$f = 1\text{ MHz}$	1.38		nF
Q_G	$V_{GE} = -8\text{ V} \dots +15\text{ V}$		2260		nC
R_{Gint}	$T_j = 25^\circ\text{C}$		1.9		Ω
$t_{d(on)}$	$V_{CC} = 600\text{ V}$	$T_j = 150^\circ\text{C}$	220		ns
t_r	$I_C = 400\text{ A}$ $V_{GE} = \pm 15\text{ V}$	$T_j = 150^\circ\text{C}$	47		ns
		$T_j = 150^\circ\text{C}$	33		mJ
E_{on}	$R_{G on} = 1\text{ }\Omega$	$T_j = 150^\circ\text{C}$	505		ns
$t_{d(off)}$	$R_{G off} = 1\text{ }\Omega$	$T_j = 150^\circ\text{C}$	78		ns
t_f	$di/dt_{on} = 9700\text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$	42		mJ
E_{off}	$di/dt_{off} = 4300\text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$			mJ
$R_{th(j-c)}$	per IGBT			0.072	K/W



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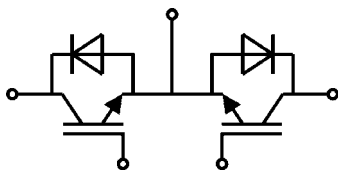
Typical Applications

- Matrix Inverter

Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max, recomm.
 $T_{op} = -40 \dots +150^\circ\text{C}$, product rel. results valid for $T_j = 150^\circ$

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse diode						
V _F = V _{EC}	I _F = 400 A	T _J = 25 °C		2.2	2.52	V
	V _{GE} = 0 V chip	T _J = 150 °C		2.15	2.47	V
V _{F0}		T _J = 25 °C		1.3	1.5	V
		T _J = 150 °C		0.9	1.1	V
r _F		T _J = 25 °C		2.3	2.5	mΩ
		T _J = 150 °C		3.1	3.4	mΩ
I _{RRM}	I _F = 400 A	T _J = 150 °C		450		A
Q _{rr}	di/dt _{off} = 8800 A/μs	T _J = 150 °C		68		μC
E _{rr}	V _{GE} = ±15 V	T _J = 150 °C		30.5		mJ
	V _{CC} = 600 V					
R _{th(j-c)}	per diode				0.14	K/W
Module						
L _{CE}				15	20	nH
R _{CC'+EE'}	terminal-chip	T _C = 25 °C		0.25		mΩ
		T _C = 125 °C		0.5		mΩ
R _{th(c-s)}	per module			0.02	0.038	K/W
M _s	to heat sink M6		3		5	Nm
M _t		to terminals M6	2.5		5	Nm
						Nm
w					325	g



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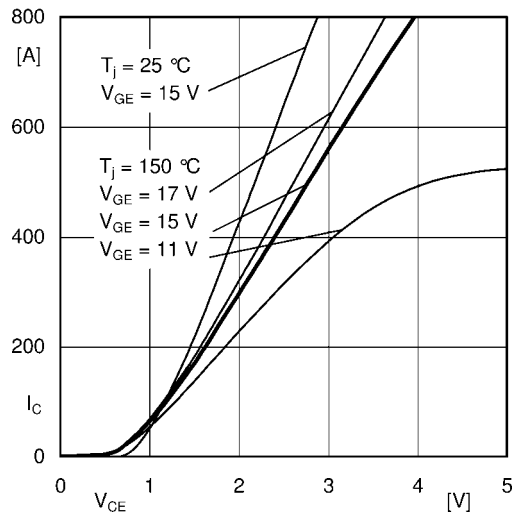


Fig. 1: Typ. output characteristic, inclusive $R_{CC'} + E_{E'}$

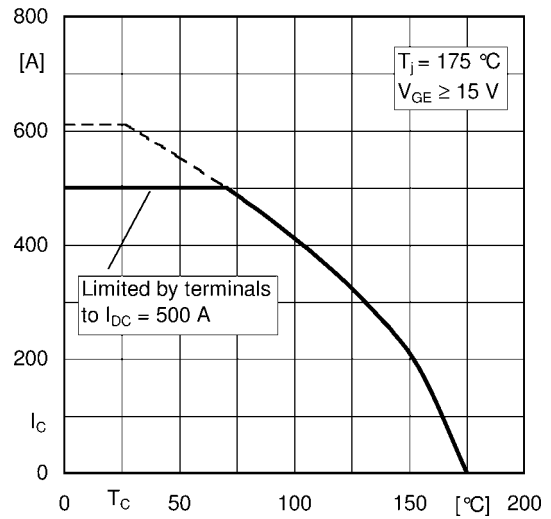


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

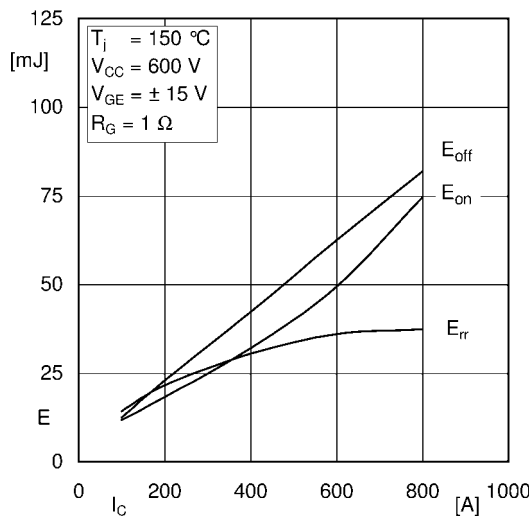


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

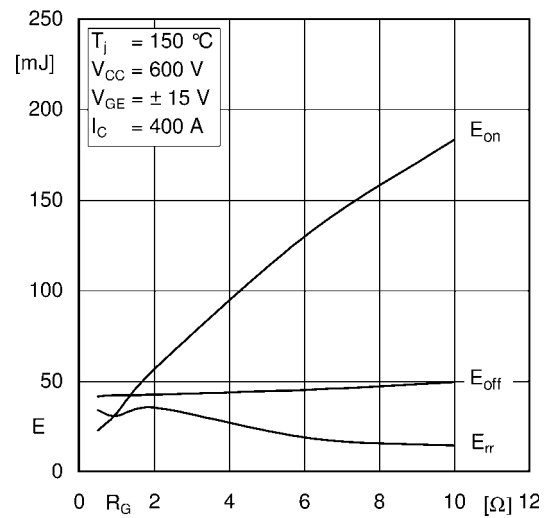


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

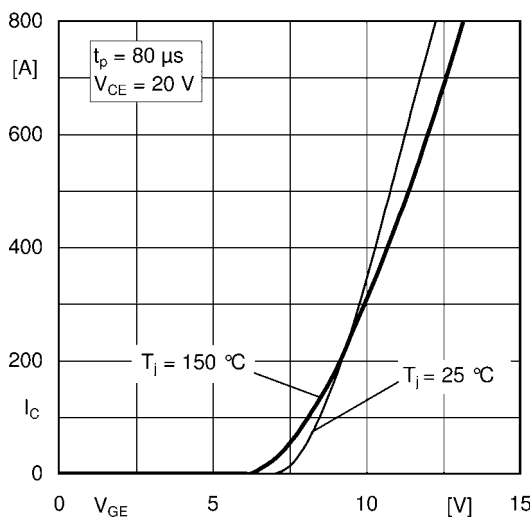


Fig. 5: Typ. transfer characteristic

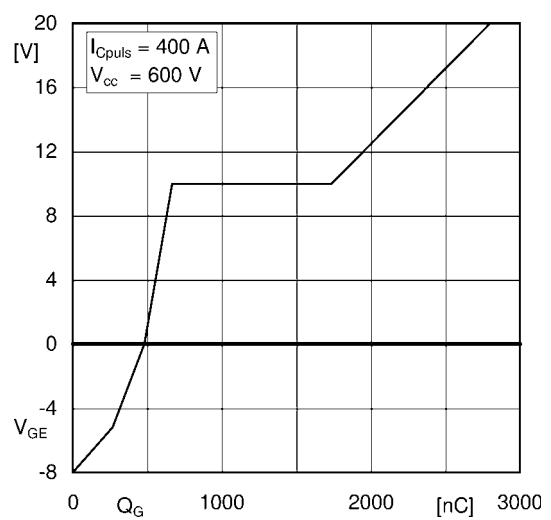


Fig. 6: Typ. gate charge characteristic

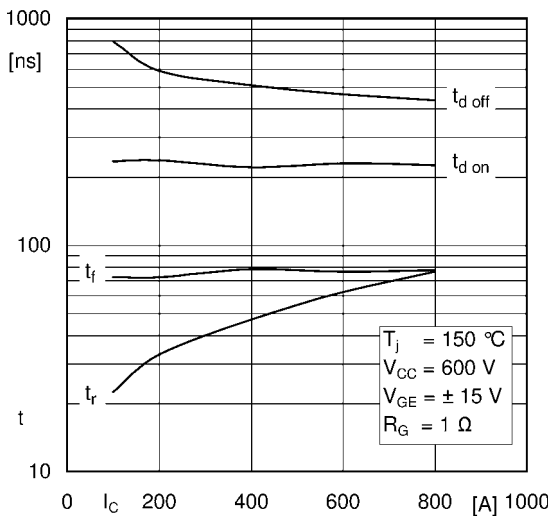


Fig. 7: Typ. switching times vs. I_C

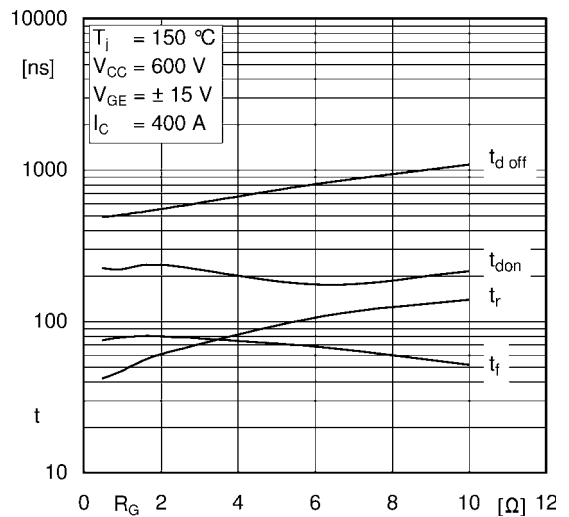


Fig. 8: Typ. switching times vs. gate resistor R_G

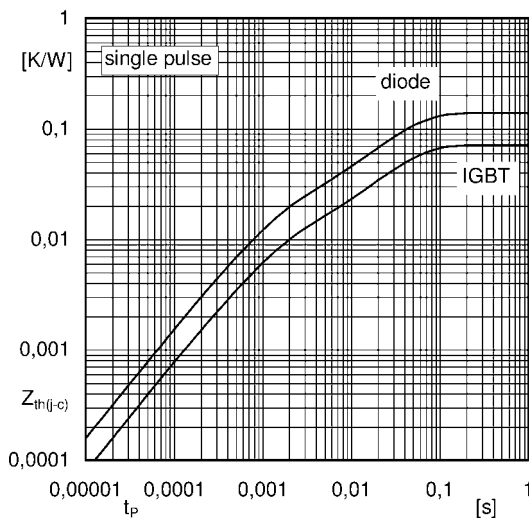


Fig. 9: Transient thermal impedance

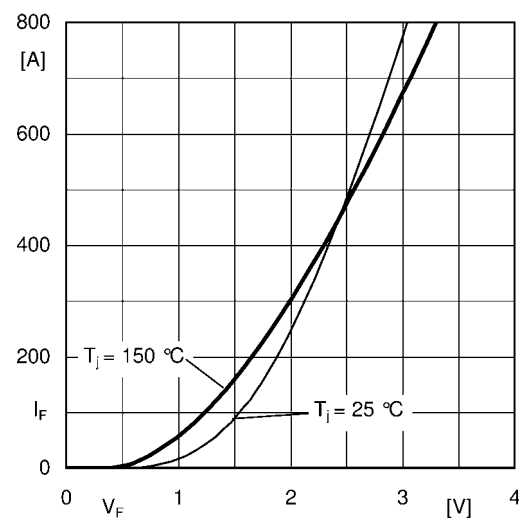


Fig. 10: CAL diode forward characteristic

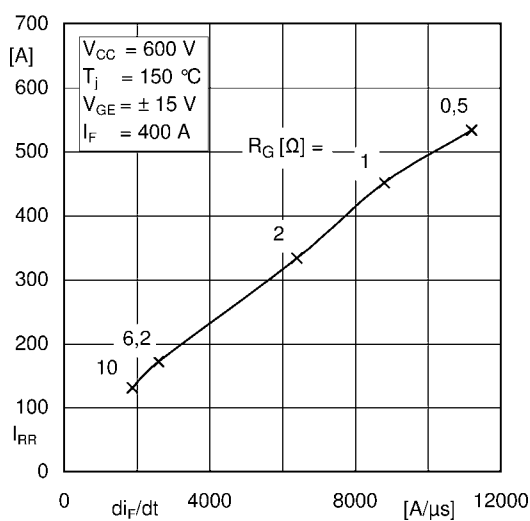


Fig. 11: CAL diode peak reverse recovery current

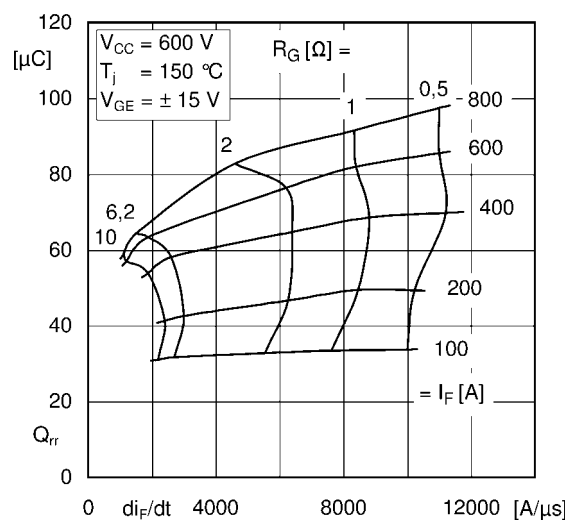
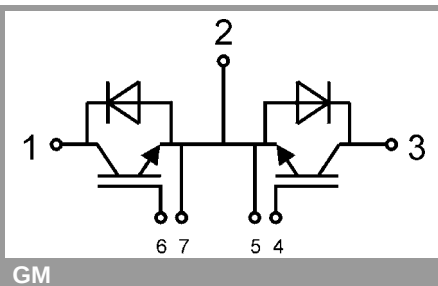


Fig. 12: Typ. CAL diode peak reverse recovery charge



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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

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