

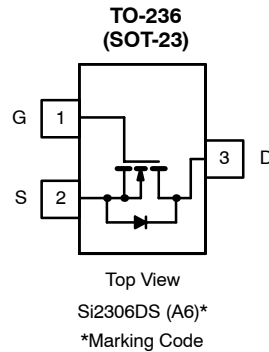


## N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
30	0.057 @ $V_{GS} = 10$ V	3.5
	0.094 @ $V_{GS} = 4.5$ V	2.8

### FEATURES

- TrenchFET® Power MOSFET
- 100%  $R_g$  Tested



Ordering Information: Si2306DS-T1

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a, b</sup>	$T_A = 25^\circ\text{C}$	$I_D$	3.5	A
	$T_A = 70^\circ\text{C}$		2.8	
Pulsed Drain Current		$I_{DM}$	16	
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		$I_S$	1.25	
Maximum Power Dissipation <sup>a, b</sup>	$T_A = 25^\circ\text{C}$	$P_D$	1.25	W
	$T_A = 70^\circ\text{C}$		0.80	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 5$ sec	$R_{thJA}$		100	$^\circ\text{C/W}$
	Steady State		130		

**Notes**

- a. Surface Mounted on FR4 Board.  
b.  $t \leq 5$  sec.

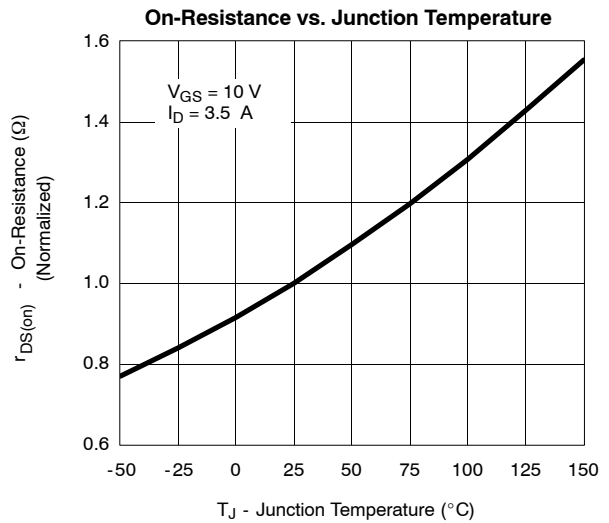
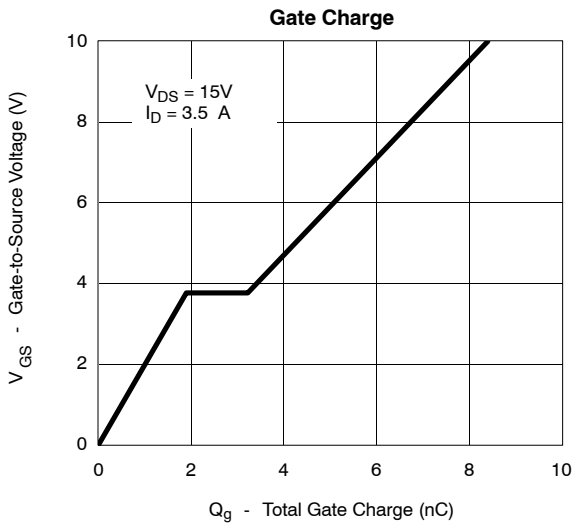
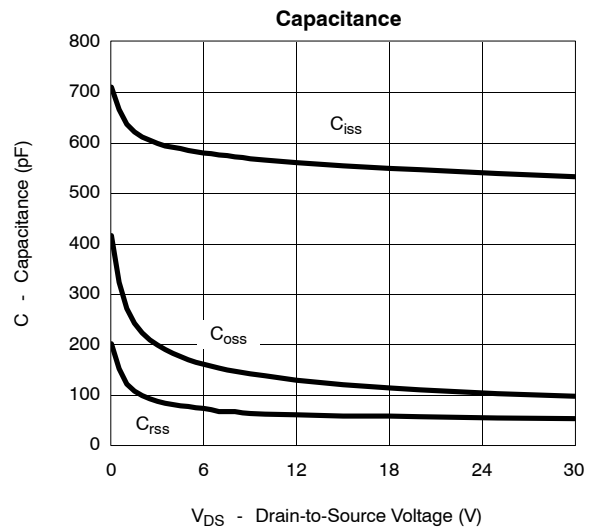
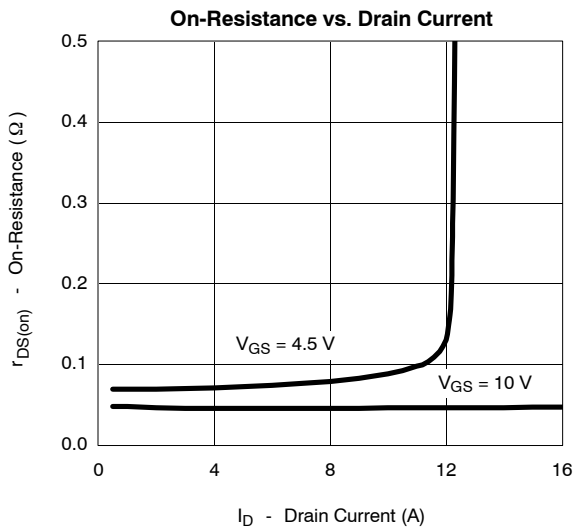
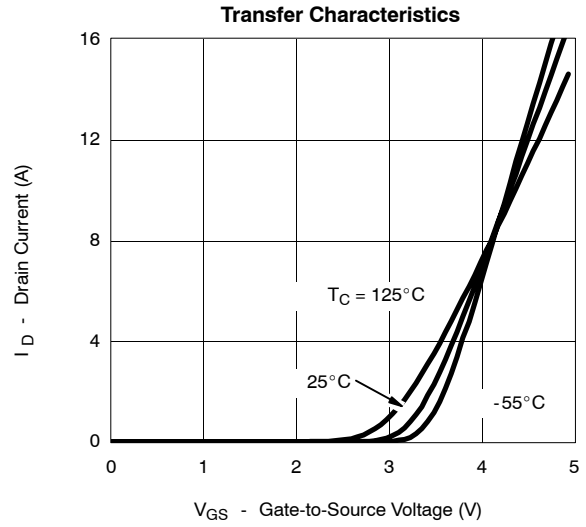
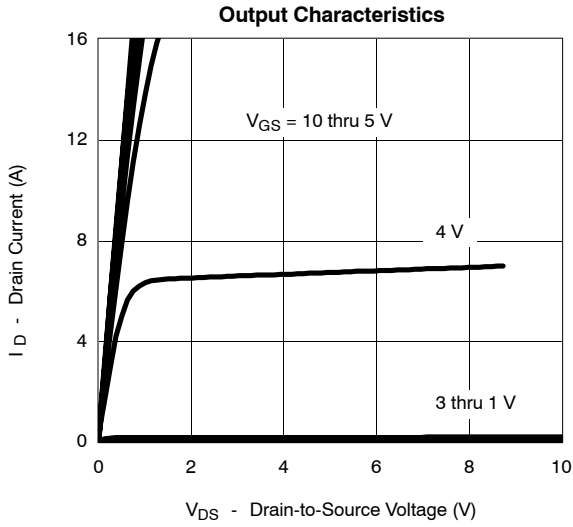
<b>SPECIFICATIONS (T<sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)</b>						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>DS</sub> = 0 V, I <sub>D</sub> = 250 μA	30			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1			
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			0.5	μA
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 4.5 V, V <sub>GS</sub> = 10 V	6			A
		V <sub>DS</sub> ≥ 4.5 V, V <sub>GS</sub> = 4.5 V	4			
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.5 A		0.046	0.057	Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 2.8 A		0.070	0.094	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 4.5 V, I <sub>D</sub> = 3.5 A		6.9		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.25 A, V <sub>GS</sub> = 0 V		0.8	1.2	V
<b>Dynamic<sup>b</sup></b>						
Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 5 V, I <sub>D</sub> = 3.5 A		4.2	7	nC
Total Gate Charge	Q <sub>gt</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.5 A		8.5	20	
Gate-Source Charge	Q <sub>gs</sub>			1.9		
Gate-Drain Charge	Q <sub>gd</sub>			1.35		
Gate Resistance	R <sub>g</sub>		0.5		2.4	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		555		pF
Output Capacitance	C <sub>oss</sub>			120		
Reverse Transfer Capacitance	C <sub>rss</sub>			60		
<b>Switching</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15 V, R <sub>L</sub> = 15 Ω I <sub>D</sub> = 1 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 6 Ω		9	20	ns
Rise Time	t <sub>r</sub>			7.5	18	
Turn-Off Delay Time	t <sub>d(off)</sub>			17	35	
Fall Time	t <sub>f</sub>			5.2	12	

## Notes

- a. Guaranteed by design, not subject to production testing.  
b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.

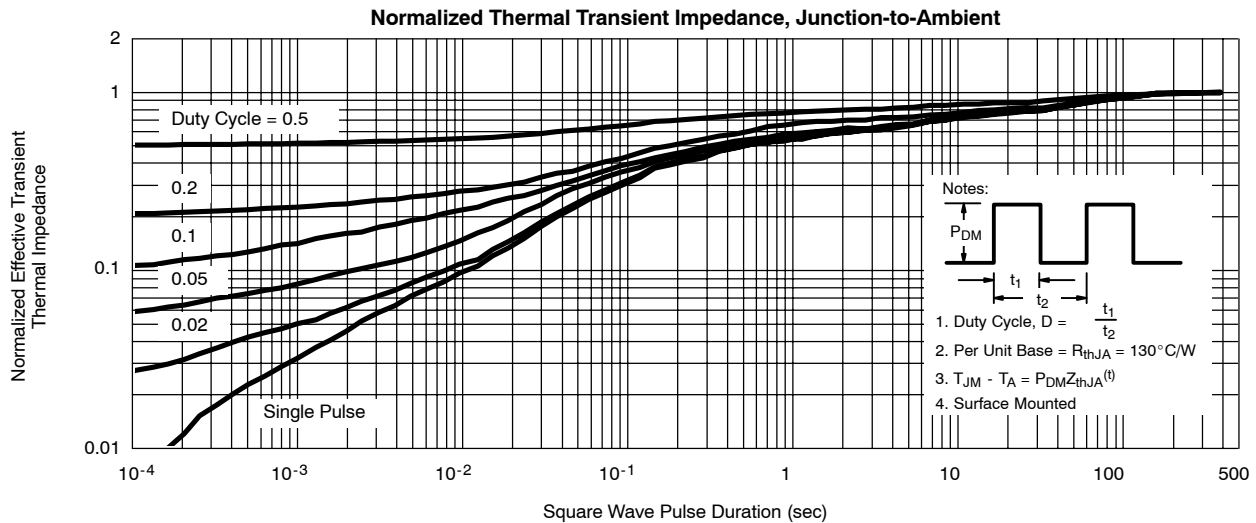
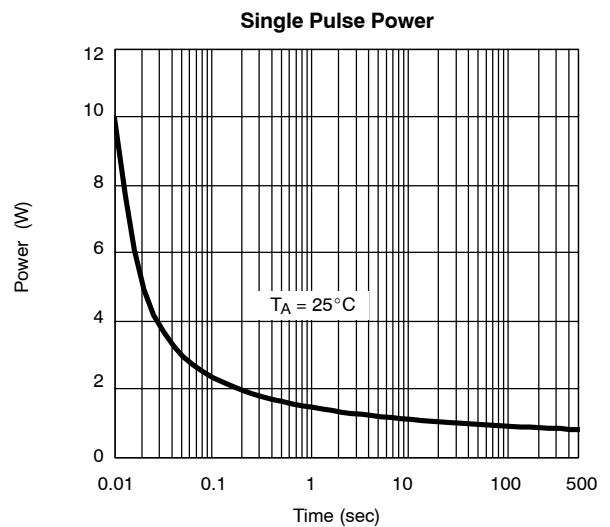
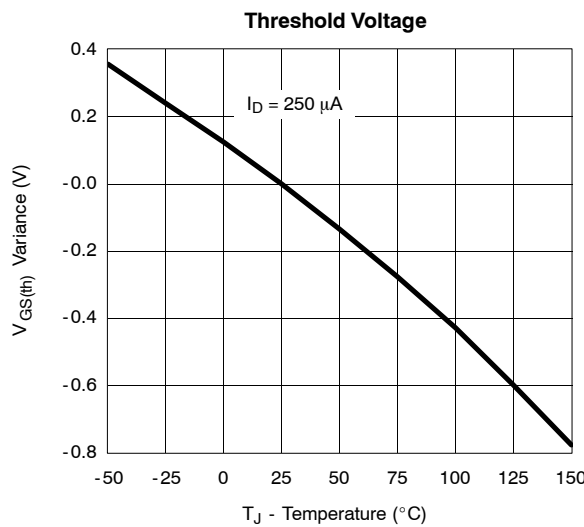
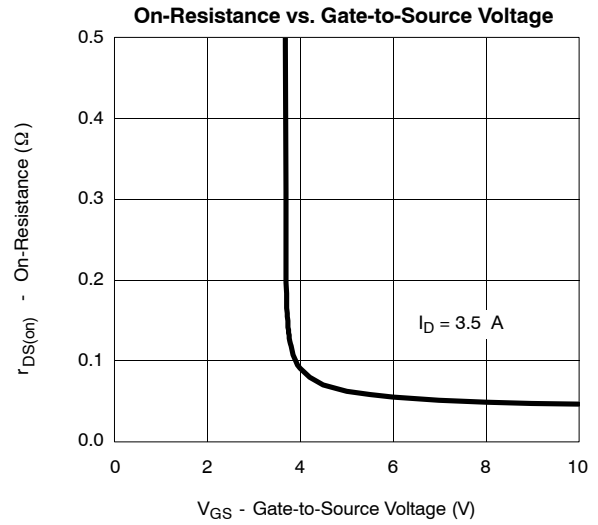
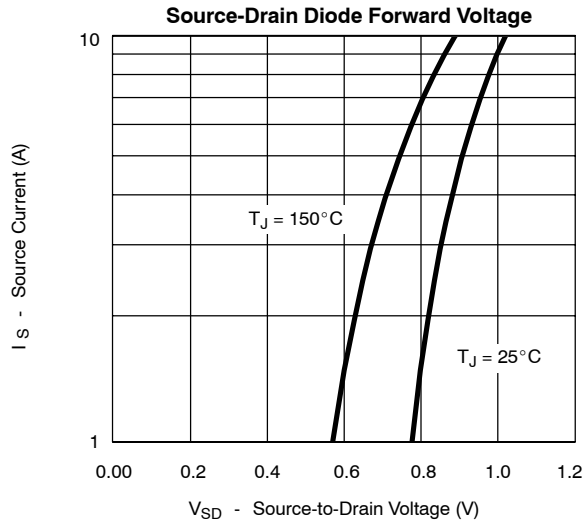


**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**





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