



# PMV30XPA

20 V, P-channel Trench MOSFET

7 January 2020

Product data sheet

## 1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Low threshold voltage
- Extended temperature range  $T_j = 175\text{ °C}$
- Trench MOSFET technology
- Very fast switching
- AEC-Q101 qualified

## 3. Applications

- Relay driver
- High-speed line driver
- High-side load switch
- Switching circuits

## 4. Quick reference data

Table 1. Quick reference data

| Symbol                        | Parameter                        | Conditions  | Min | Typ | Max  | Unit       |
|-------------------------------|----------------------------------|---|-----|-----|------|------------|
| $V_{DS}$                      | drain-source voltage             | $T_j = 25\text{ °C}$  | -   | -   | -20  | V          |
| $V_{GS}$                      | gate-source voltage              |   | -12 | -   | 12   | V          |
| $I_D$                         | drain current                    | $V_{GS} = -4.5\text{ V}; T_{amb} = 25\text{ °C}$                  | [1] | -   | -4.9 | A          |
| <b>Static characteristics</b> |                                  |   |     |     |      |            |
| $R_{DS(on)}$                  | drain-source on-state resistance | $V_{GS} = -4.5\text{ V}; I_D = -4.9\text{ A}; T_j = 25\text{ °C}$ | -   | 30  | 38   | m $\Omega$ |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

## 5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol   |
|-----|--------|-------------|--------------------|------------------|
| 1   | G      | gate        | <p>SOT23</p>       | <p>017aaa257</p> |
| 2   | S      | source      |                    |                  |
| 3   | D      | drain       |                    |                  |

## 6. Ordering information

Table 3. Ordering information

| Type number | Package |  |         |
|-------------|---------|--|---------|
|             | Name    | Description  | Version |
| PMV30XPA    | SOT23   | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23   |

## 7. Marking

Table 4. Marking codes

| Type number | Marking code[1] |
|-------------|-----------------|
| PMV30XPA    | %HH             |

[1] % = placeholder for manufacturing site code

## 8. Limiting values

Table 5. Limiting values

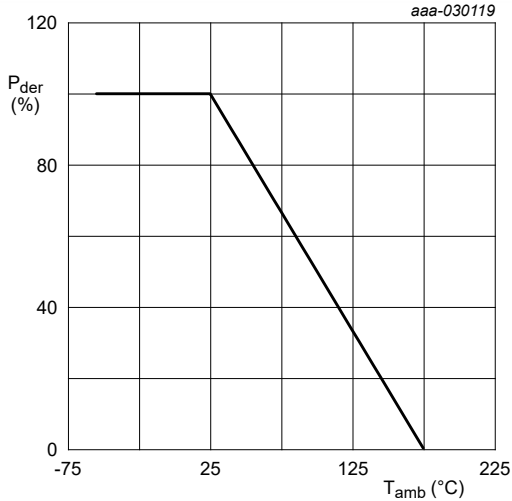
In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol                      | Parameter                                    | Conditions   |     | Min | Max  | Unit |
|-----------------------------|--|--|-----|-----|------|------|
| $V_{DS}$                    | drain-source voltage                         | $T_j = 25\text{ °C}$   |     | -   | -20  | V    |
| $V_{GS}$                    | gate-source voltage                          |  |     | -12 | 12   | V    |
| $I_D$                       | drain current                                | $V_{GS} = -4.5\text{ V}; T_{amb} = 25\text{ °C}$   | [1] | -   | -4.9 | A    |
|                             |  | $V_{GS} = -4.5\text{ V}; T_{amb} = 100\text{ °C}$  | [1] | -   | -3.1 | A    |
| $I_{DM}$                    | peak drain current                           | $T_{amb} = 25\text{ °C}$ ; single pulse; $t_p \leq 10\text{ }\mu\text{s}$                  |     | -   | -20  | A    |
| $P_{tot}$                   | total power dissipation                      | $T_{amb} = 25\text{ °C}$   | [2] | -   | 610  | mW   |
|                             |  |  | [1] | -   | 1.4  | W    |
|                             |  | $T_{sp} = 25\text{ °C}$  |     | -   | 8.3  | W    |
| $T_j$                       | junction temperature                         |  |     | -55 | 175  | °C   |
| $T_{amb}$                   | ambient temperature                          |  |     | -55 | 175  | °C   |
| $T_{stg}$                   | storage temperature                          |  |     | -65 | 175  | °C   |
| <b>Source-drain diode</b>   |  |  |     |     |      |      |
| $I_S$                       | source current                               | $T_{amb} = 25\text{ °C}$   | [1] | -   | -1.5 | A    |
| <b>ESD maximum rating</b>   |  |  |     |     |      |      |
| $V_{ESD}$                   | electrostatic discharge voltage              | HBM  | [3] | -   | 500  | V    |
| <b>Avalanche ruggedness</b> |  |  |     |     |      |      |
| $E_{DS(AL)S}$               | non-repetitive drain-source avalanche energy | $T_{j(\text{init})} = 25\text{ °C}$ ; $I_D = -1.5\text{ A}$ ; DUT in avalanche (unclamped) |     | -   | 15.5 | mJ   |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

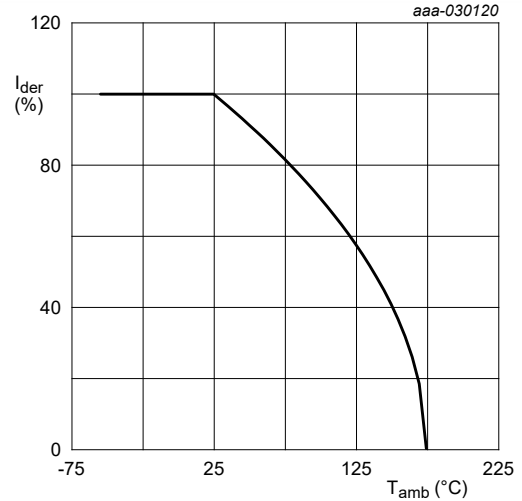
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[3] Measured between all pins.



$$P_{der} = \frac{P_{tot}}{P_{tot(25^\circ C)}} \times 100 \%$$

Fig. 1. Normalized total power dissipation as a function of ambient temperature



$$I_{der} = \frac{I_D}{I_{D(25^\circ C)}} \times 100 \%$$

Fig. 2. Normalized continuous drain current as a function of ambient temperature

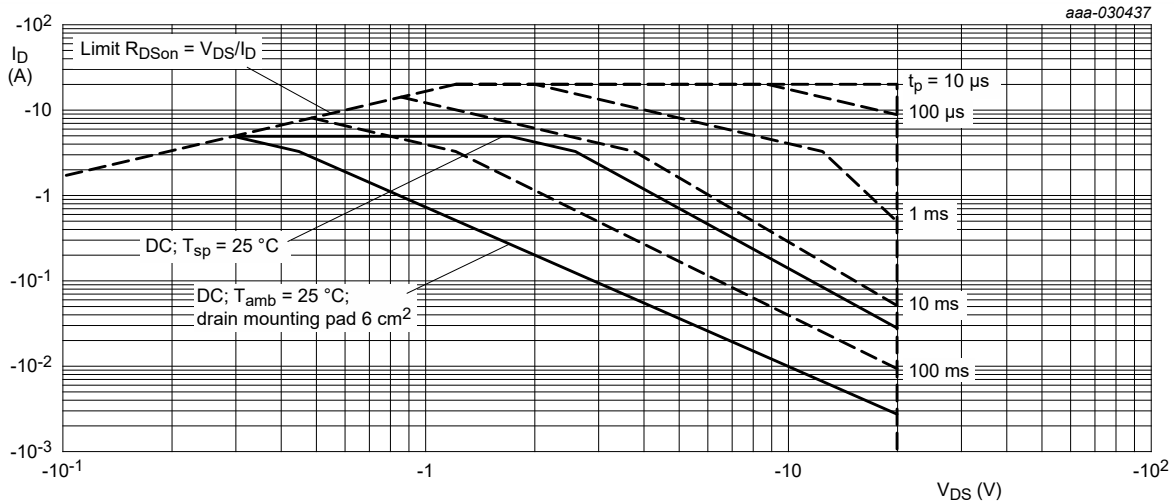


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

### 9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol         | Parameter  | Conditions  |     | Min | Typ | Max | Unit |
|----------------|--|-------------|-----|-----|-----|-----|------|
| $R_{th(j-a)}$  | thermal resistance from junction to ambient      | in free air | [1] | -   | 208 | 245 | K/W  |
|                |  |             | [2] | -   | 88  | 104 | K/W  |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point |             |     | -   | 13  | 18  | K/W  |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

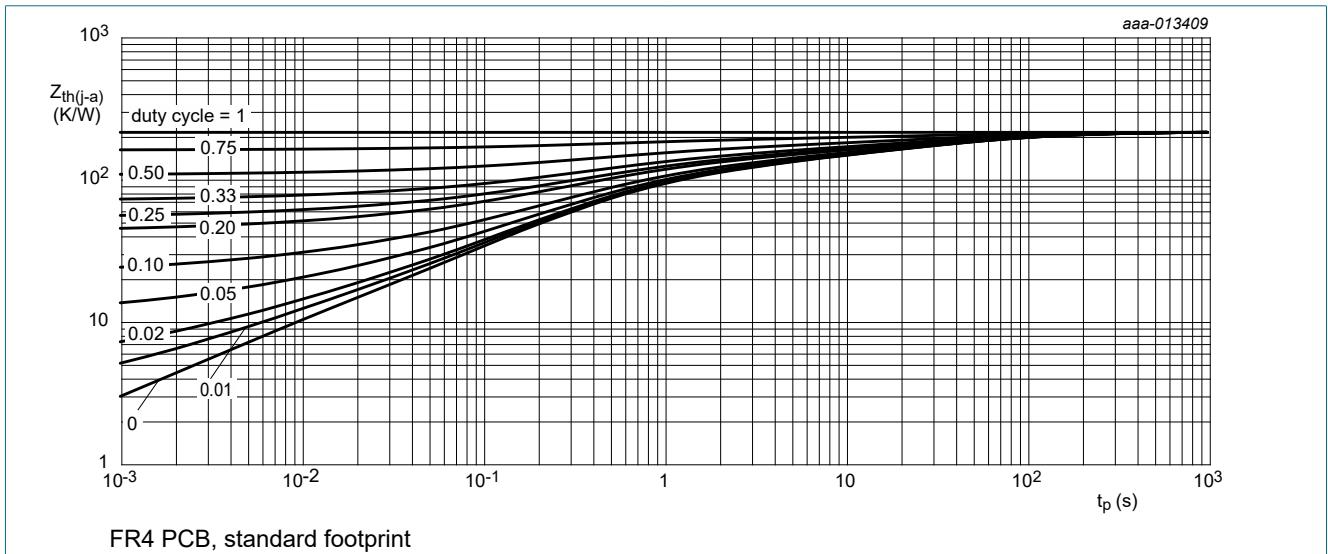


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

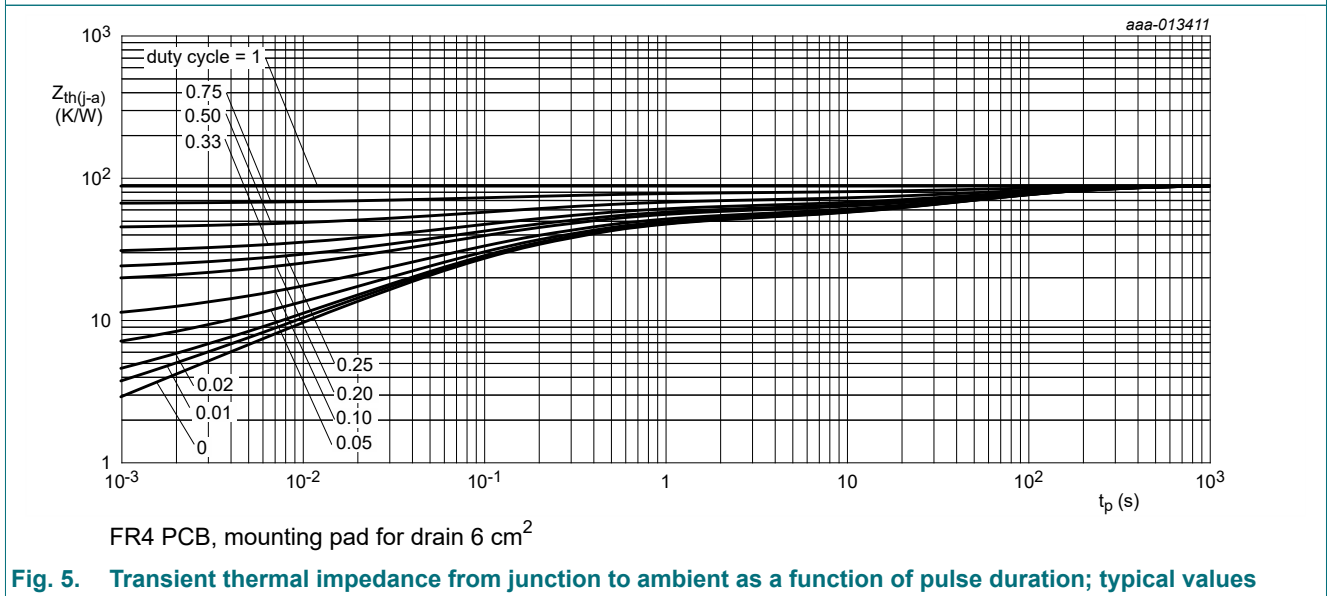


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

Table 7. Characteristics

| Symbol                         | Parameter                        | Conditions   | Min  | Typ   | Max  | Unit       |
|--------------------------------|----------------------------------|--|------|-------|------|------------|
| <b>Static characteristics</b>  |                                  |  |      |       |      |            |
| $V_{(BR)DSS}$                  | drain-source breakdown voltage   | $I_D = -250 \mu A$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$  | -20  | -     | -    | V          |
| $V_{GSth}$                     | gate-source threshold voltage    | $I_D = -250 \mu A$ ; $V_{DS} = V_{GS}$ ; $T_j = 25 \text{ }^\circ C$   | -0.6 | -0.95 | -1.3 | V          |
| $I_{DSS}$                      | drain leakage current            | $V_{DS} = -20 V$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$  | -    | -     | -1   | $\mu A$    |
| $I_{GSS}$                      | gate leakage current             | $V_{GS} = -12 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$  | -    | -     | -100 | nA         |
|                                |                                  | $V_{GS} = 12 V$ ; $V_{DS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$   | -    | -     | 100  | nA         |
| $R_{DSon}$                     | drain-source on-state resistance | $V_{GS} = -8 V$ ; $I_D = -4.9 A$ ; $T_j = 25 \text{ }^\circ C$   | -    | 25    | 33   | m $\Omega$ |
|                                |                                  | $V_{GS} = -8 V$ ; $I_D = -4.9 A$ ; $T_j = 175 \text{ }^\circ C$  | -    | 40    | 53   | m $\Omega$ |
|                                |                                  | $V_{GS} = -4.5 V$ ; $I_D = -4.9 A$ ; $T_j = 25 \text{ }^\circ C$   | -    | 30    | 38   | m $\Omega$ |
|                                |                                  | $V_{GS} = -2.5 V$ ; $I_D = -3 A$ ; $T_j = 25 \text{ }^\circ C$   | -    | 45    | 62   | m $\Omega$ |
| $g_{fs}$                       | forward transconductance         | $V_{DS} = -10 V$ ; $I_D = -4.9 A$ ; $T_j = 25 \text{ }^\circ C$  | -    | 18    | -    | S          |
| $R_G$                          | gate resistance                  | $f = 1 \text{ MHz}$  | -    | 1.8   | -    | $\Omega$   |
| <b>Dynamic characteristics</b> |                                  |  |      |       |      |            |
| $Q_{G(tot)}$                   | total gate charge                | $V_{DS} = -10 V$ ; $I_D = -5 A$ ; $V_{GS} = -4.5 V$ ; $T_j = 25 \text{ }^\circ C$                                    | -    | 11    | 16   | nC         |
| $Q_{GS}$                       | gate-source charge               |  | -    | 1.9   | -    | nC         |
| $Q_{GD}$                       | gate-drain charge                |  | -    | 3.4   | -    | nC         |
| $C_{iss}$                      | input capacitance                | $V_{DS} = -10 V$ ; $f = 1 \text{ MHz}$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$                                | -    | 1039  | -    | pF         |
| $C_{oss}$                      | output capacitance               |  | -    | 124   | -    | pF         |
| $C_{riss}$                     | reverse transfer capacitance     |  | -    | 110   | -    | pF         |
| $t_{d(on)}$                    | turn-on delay time               | $V_{DS} = -10 V$ ; $I_D = -5 A$ ; $V_{GS} = -4.5 V$ ; $R_{G(ext)} = 6 \text{ } \Omega$ ; $T_j = 25 \text{ }^\circ C$ | -    | 8     | -    | ns         |
| $t_r$                          | rise time                        |  | -    | 30    | -    | ns         |
| $t_{d(off)}$                   | turn-off delay time              |  | -    | 40    | -    | ns         |
| $t_f$                          | fall time                        |  | -    | 23    | -    | ns         |
| <b>Source-drain diode</b>      |                                  |  |      |       |      |            |
| $V_{SD}$                       | source-drain voltage             | $I_S = -1.5 A$ ; $V_{GS} = 0 V$ ; $T_j = 25 \text{ }^\circ C$  | -    | -0.8  | -1.2 | V          |
| $t_{rr}$                       | reverse recovery time            | $I_S = -1.5 A$ ; $di_S/dt = -100 A/\mu s$ ; $V_{GS} = 0 V$ ; $V_{DS} = -10 V$ ; $T_j = 25 \text{ }^\circ C$          | -    | 13    | -    | ns         |
| $Q_r$                          | recovered charge                 | $di_S/dt = -100 A/\mu s$ ; $V_{GS} = 0 V$ ; $V_{DS} = -10 V$ ; $T_j = 25 \text{ }^\circ C$                           | -    | 3     | -    | nC         |

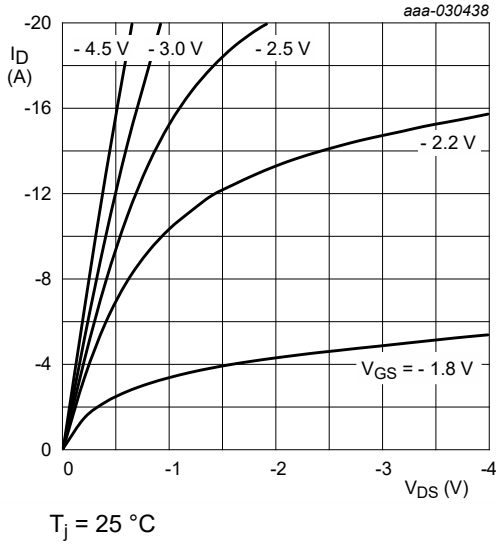


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

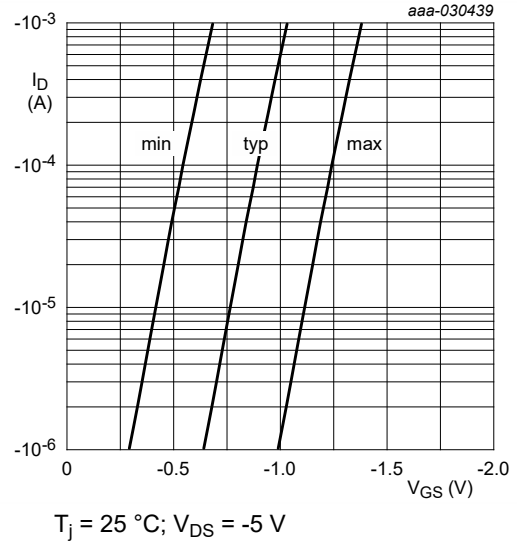


Fig. 7. Subthreshold drain current as a function of gate-source voltage

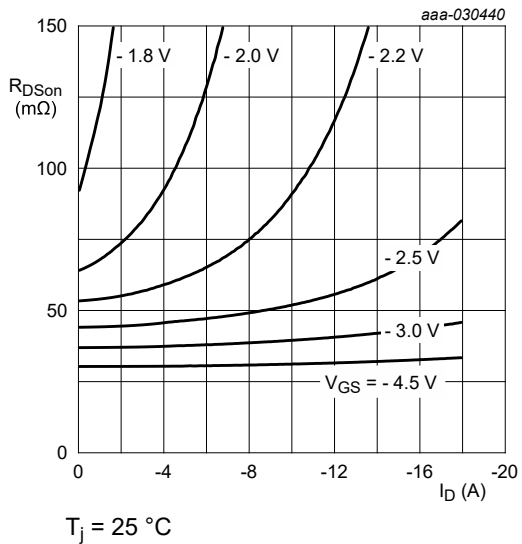


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

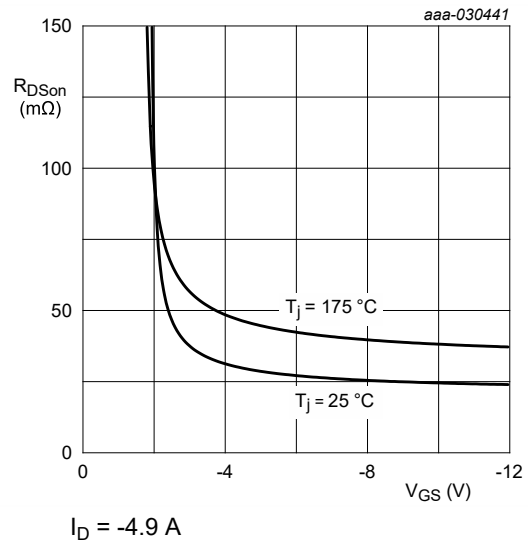


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

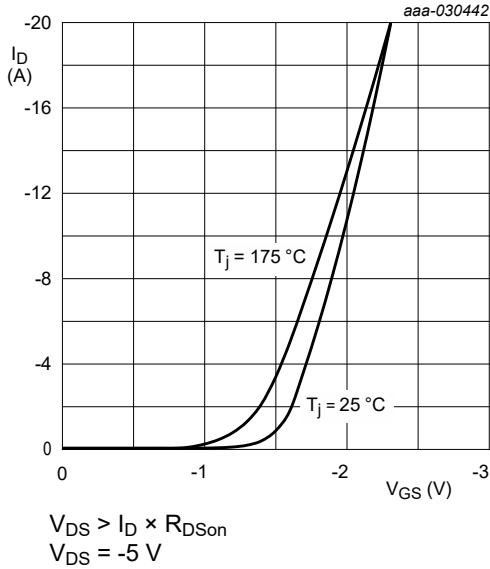


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

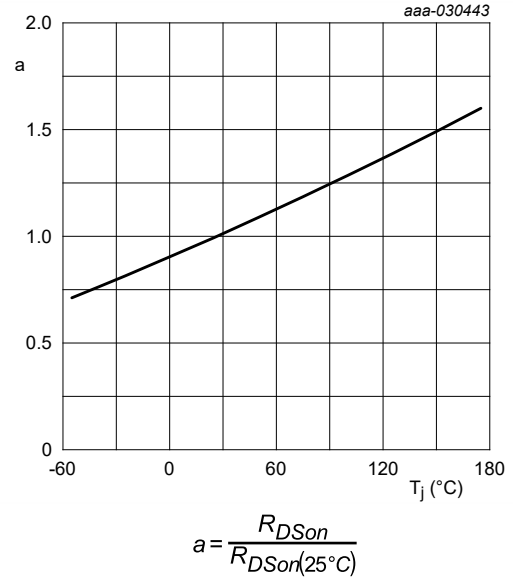


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

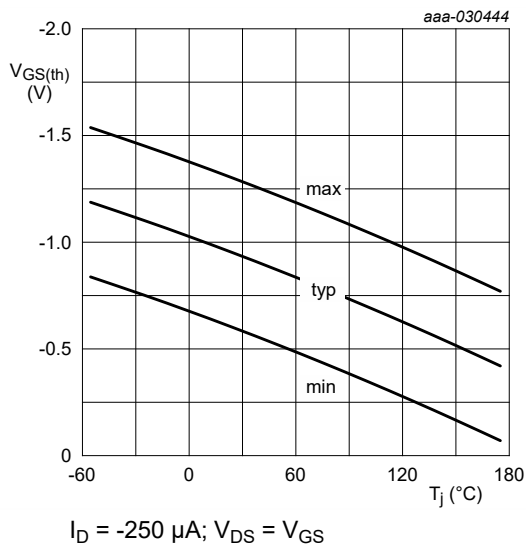


Fig. 12. Gate-source threshold voltage as a function of junction temperature

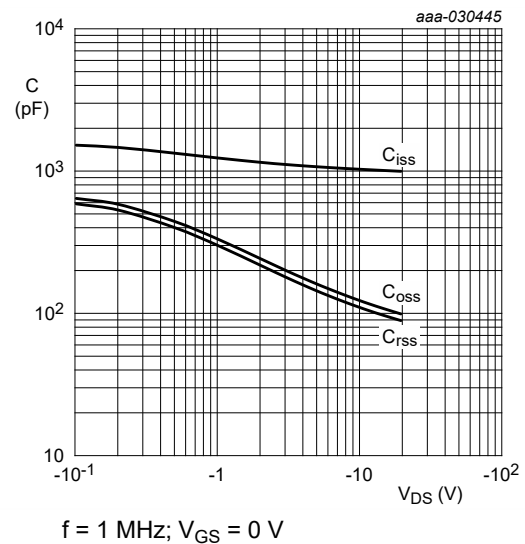


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

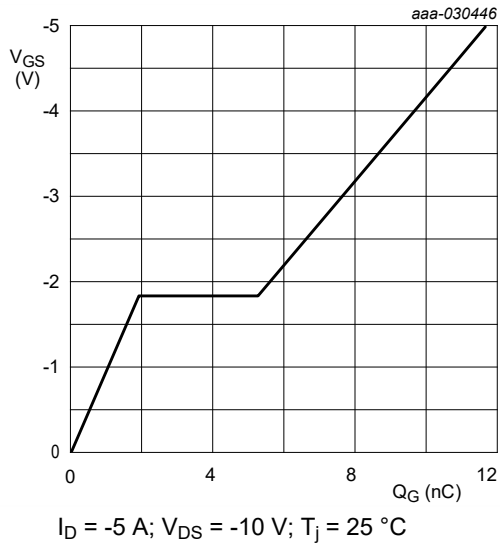


Fig. 14. Gate-source voltage as a function of gate charge; typical values

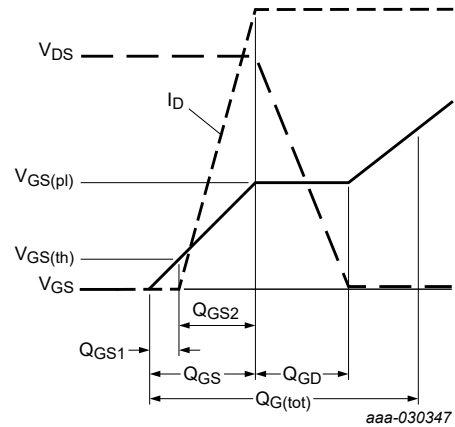


Fig. 15. Gate charge waveform definitions

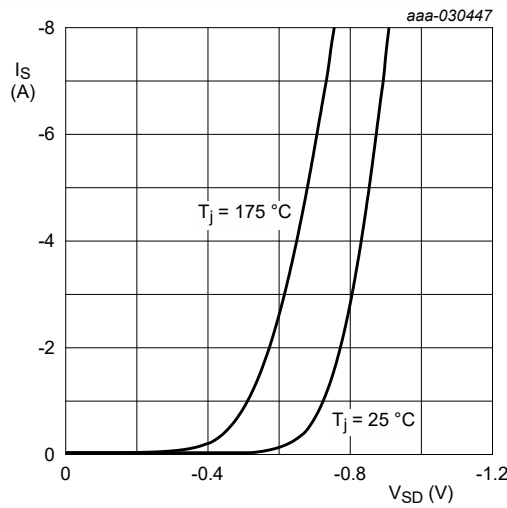


Fig. 16. Source current as a function of source-drain voltage; typical values

## 11. Test information

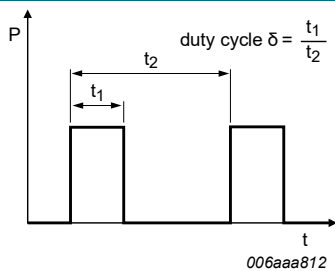


Fig. 17. Duty cycle definition

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

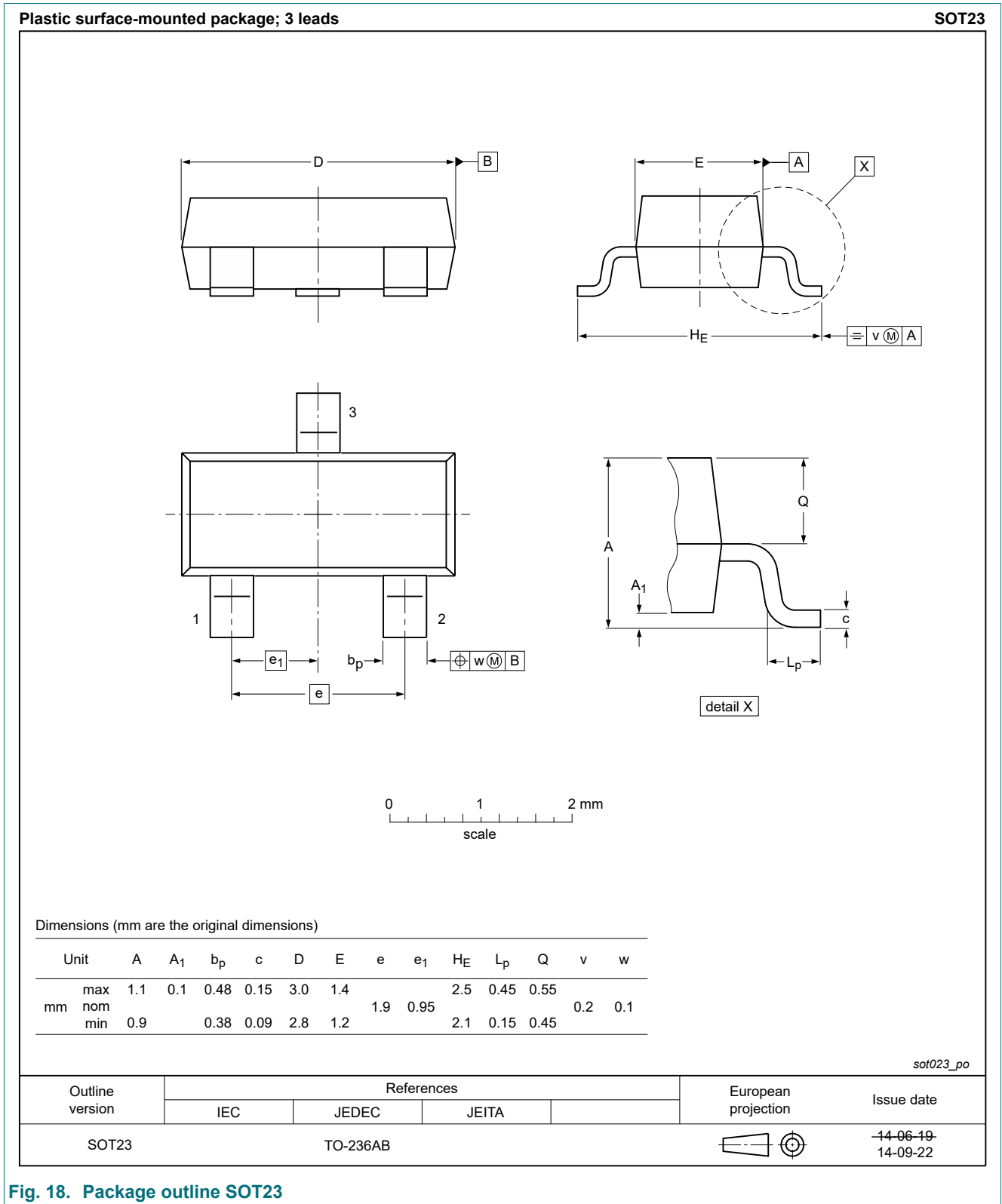


Fig. 18. Package outline SOT23

### 13. Soldering

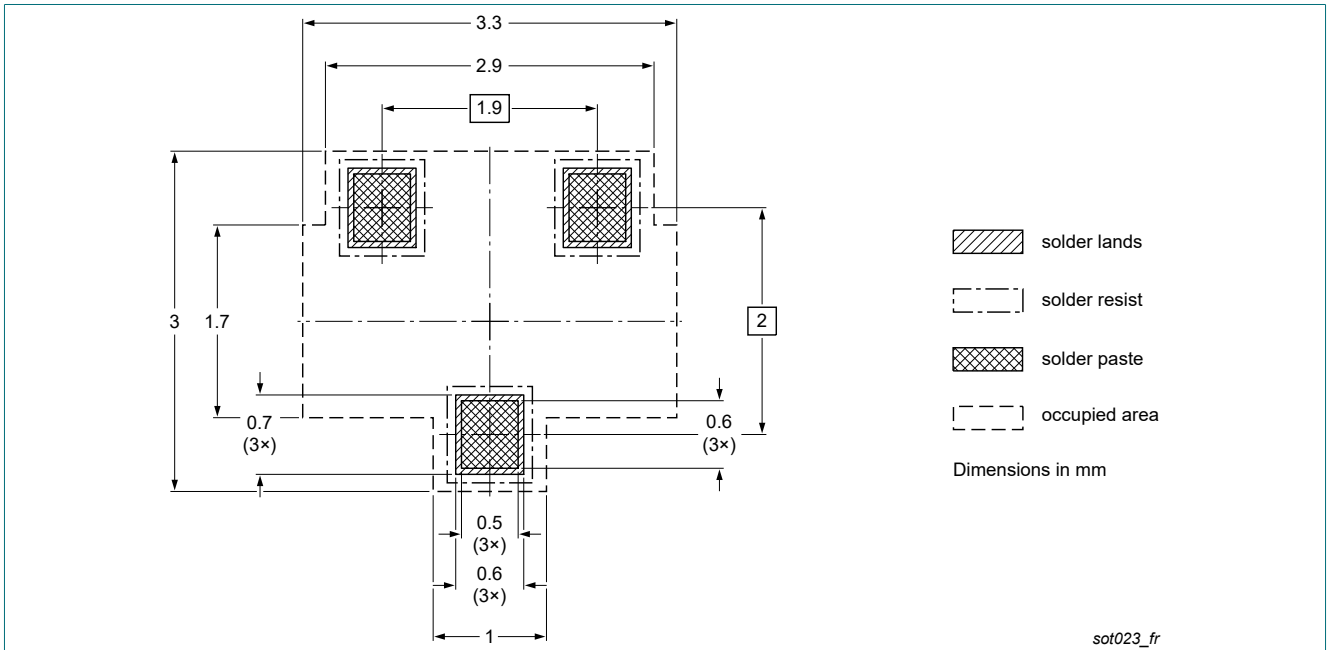


Fig. 19. Reflow soldering footprint for SOT23

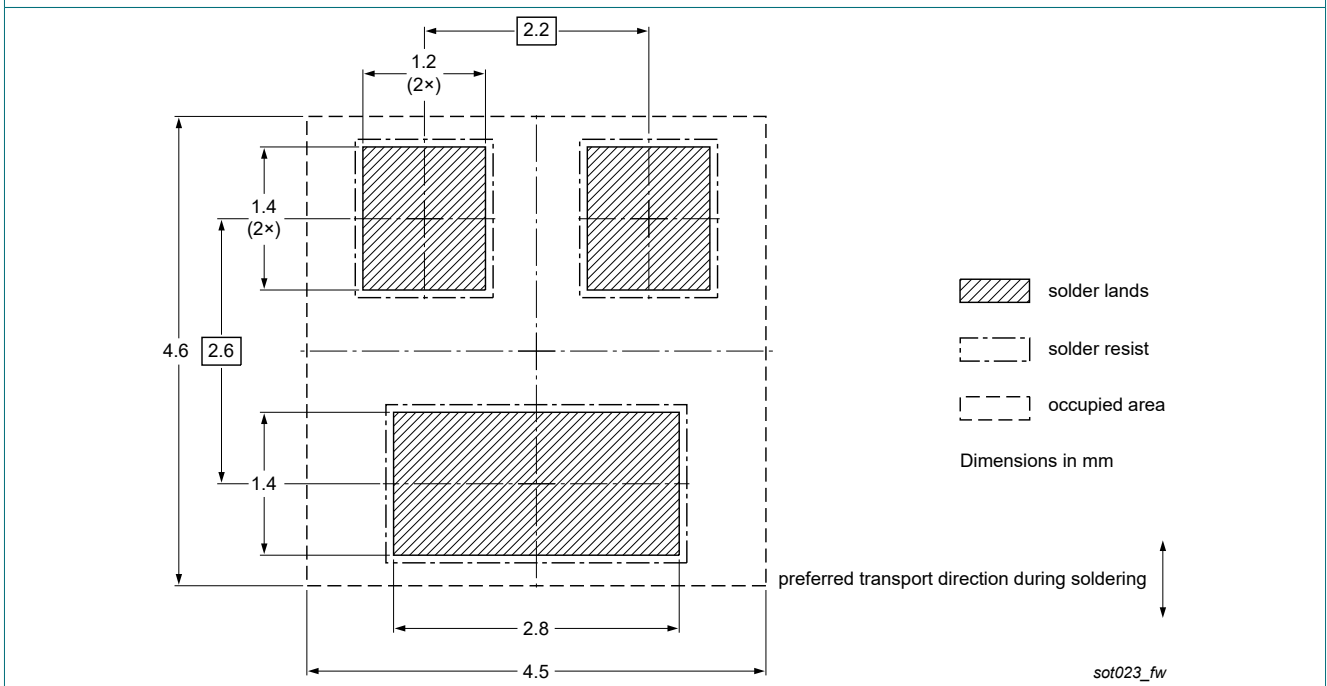


Fig. 20. Wave soldering footprint for SOT23

## 14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status  | Change notice | Supersedes |
|---------------|--------------|--------------------|---------------|------------|
| PMV30XPA v.1  | 20200107     | Product data sheet | -             | -          |

## 15. Legal information

### Data sheet status

| Document status [1][2]         | Product status [3] | Definition  |
|--------------------------------|--------------------|---|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 7 January 2020

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