
# PDTC114ET-Q

## NPN resistor-equipped transistors; R1 = 10 kΩ, R2 = 10 kΩ

**25 June 2021** **Product data sheet**

## 1. General description

NPN Resistor-Equipped Transistor (RET) in a small SOT23 Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- Digital application in automotive and industrial segments
- Control of IC inputs
- Cost-saving alternative for BC847/857 series in digital applications
- Switching loads

## 4. Quick reference data

**Table 1. Quick reference data**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $V_{CEO}$ | collector-emitter voltage | open base | | - | - | 50 | V |
| $I_O$ | output current | | | - | - | 100 | mA |
| R1 | bias resistor 1 | | | 7 | 10 | 13 | kΩ |
| R2/R1 | bias resistor ratio | | | 0.8 | 1 | 1.2 | |

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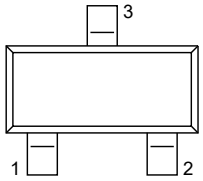
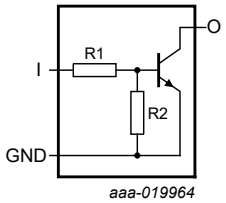
# 5. Pinning information

**Table 2. Pinning information**

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|---|---|---|---|---|
| 1 | I | input (base) |   SOT23 |   |
| 2 | GND | ground (emitter) | | |
| 3 | O | output (collector) | | |

# 6. Ordering information

**Table 3. Ordering information**

| Type number | Package | | |
|---|---|---|---|
| | Name | Description | Version |
| PDTC114ET-Q | SOT23 | plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body | SOT23 |

# 7. Marking

**Table 4. Marking codes**

| Type number | Marking code[1] |
|---|---|
| PDTC114ET-Q | %16 |

[1] % = placeholder for manufacturing site code

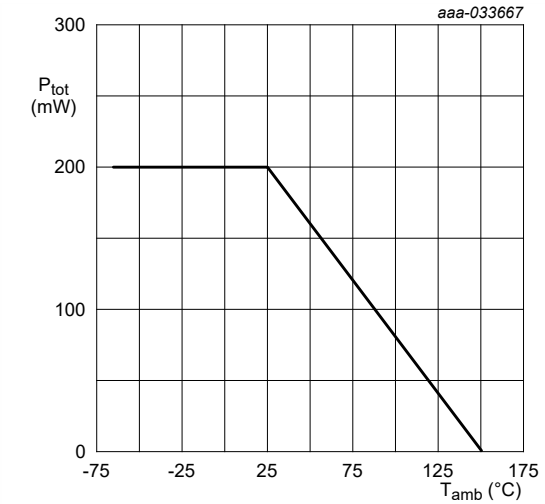
# 8. Limiting values

**Table 5. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|---|---|---|---|---|---|---|
| $V_{CBO}$ | collector-base voltage | open emitter | | - | 50 | V |
| $V_{CEO}$ | collector-emitter voltage | open base | | - | 50 | V |
| $V_{EBO}$ | emitter-base voltage | open collector | | - | 10 | V |
| $V_I$ | input voltage | positive | | - | 40 | V |
| | | negative | | - | -10 | V |
| $I_O$ | output current | | | - | 100 | mA |
| $I_{CM}$ | peak collector current | $t_p \leq$ 1 ms; single pulse | | - | 100 | mA |
| $P_{tot}$ | total power dissipation | $T_{amb} \leq$ 25 °C | [1] | - | 250 | mW |
| $T_j$ | junction temperature | | | - | 150 | °C |
| $T_{amb}$ | ambient temperature | | | -65 | 150 | °C |
| $T_{stg}$ | storage temperature | | | -65 | 150 | °C |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.




FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint

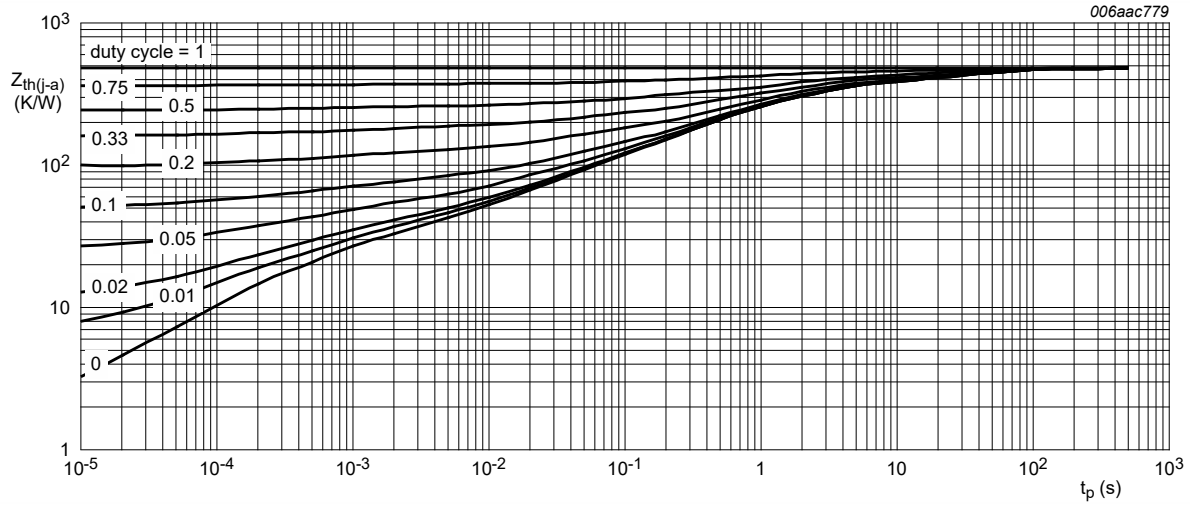
**Fig. 1. Power derating curve**

# 9. Thermal characteristics

**Table 6. Thermal characteristics**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] | - | - | 500 | K/W |

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.




FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

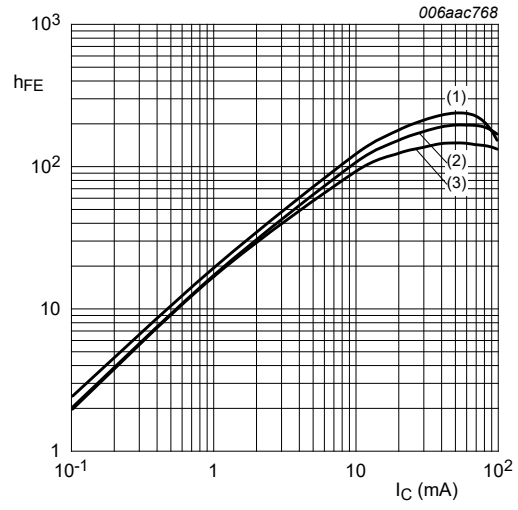
**Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**

# 10. Characteristics

**Table 7. Characteristics**

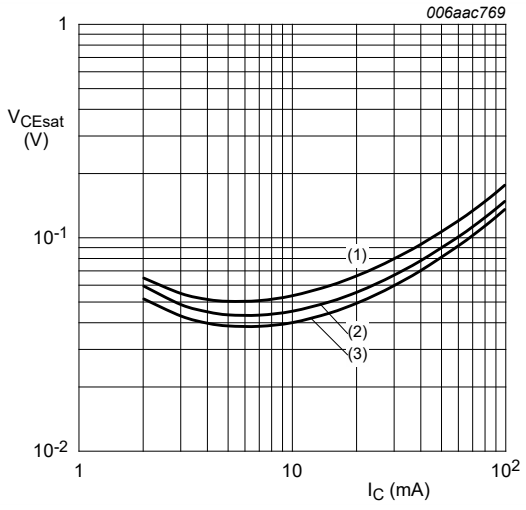
| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $V_{(BR)CBO}$ | collector-base breakdown voltage | $I_C$ = 100 µA; $I_E$ = 0 A; $T_{amb}$ = 25 °C | | 50 | - | - | V |
| $V_{(BR)CEO}$ | collector-emitter breakdown voltage | $I_C$ = 2 mA; $I_B$ = 0 A; $T_{amb}$ = 25 °C | | 50 | - | - | V |
| $V_{(BR)EBO}$ | emitter-base breakdown voltage | $I_C$ = 0 A; $I_E$ = 100 µA; $T_{amb}$ = 25 °C | | 10 | - | - | V |
| $I_{CBO}$ | collector-base cut-off current | $V_{CB}$ = 50 V; $I_E$ = 0 A; $T_{amb}$ = 25 °C | | - | - | 100 | nA |
| $I_{CEO}$ | collector-emitter cut-off current | $V_{CE}$ = 30 V; $I_B$ = 0 A; $T_{amb}$ = 25 °C | | - | - | 1 | µA |
| | | $V_{CE}$ = 30 V; $I_B$ = 0 A; $T_j$ = 150 °C | | - | - | 5 | µA |
| $I_{EBO}$ | emitter-base cut-off current | $V_{EB}$ = 5 V; $I_C$ = 0 A; $T_{amb}$ = 25 °C | | - | - | 400 | µA |
| $h_{FE}$ | DC current gain | $V_{CE}$ = 5 V; $I_C$ = 5 mA; $T_{amb}$ = 25 °C | | 30 | - | - | |
| $V_{CEsat}$ | collector-emitter saturation voltage | $I_C$ = 10 mA; $I_B$ = 0.5 mA; $T_{amb}$ = 25 °C | | - | - | 150 | mV |
| $V_{I(off)}$ | off-state input voltage | $V_{CE}$ = 5 V; $I_C$ = 100 µA; $T_{amb}$ = 25 °C | | - | 1.1 | 0.8 | V |
| $V_{I(on)}$ | on-state input voltage | $V_{CE}$ = 0.3 V; $I_C$ = 10 mA; $T_{amb}$ = 25 °C | | 2.5 | 1.8 | - | V |
| R1 | bias resistor 1 | | | 7 | 10 | 13 | kΩ |
| R2/R1 | bias resistor ratio | | | 0.8 | 1 | 1.2 | |
| $C_c$ | collector capacitance | $V_{CB}$ = 10 V; $I_E$ = 0 A; $i_e$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C | | - | - | 2.5 | pF |
| $f_T$ | transition frequency | $V_{CE}$ = 5 V; $I_C$ = 10 mA; f = 100 MHz; $T_{amb}$ = 25 °C | [1] | - | 230 | - | MHz |

[1] Characteristics of built-in transistor.



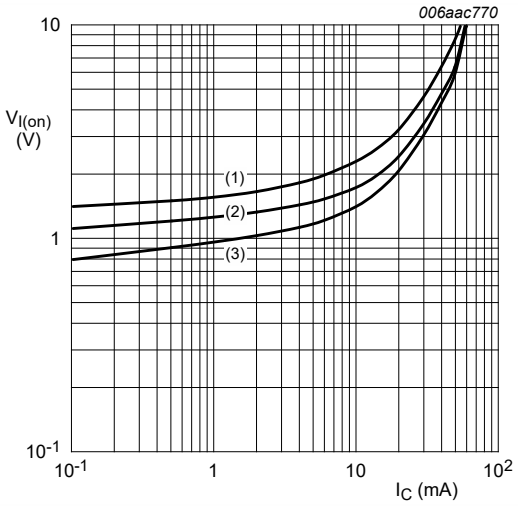

$V_{CE}$ = 5 V
(1) $T_{amb}$ = 100 °C
(2) $T_{amb}$ = 25 °C
(3) $T_{amb}$ = -40 °C

**Fig. 3. DC current gain as a function of collector current; typical values**



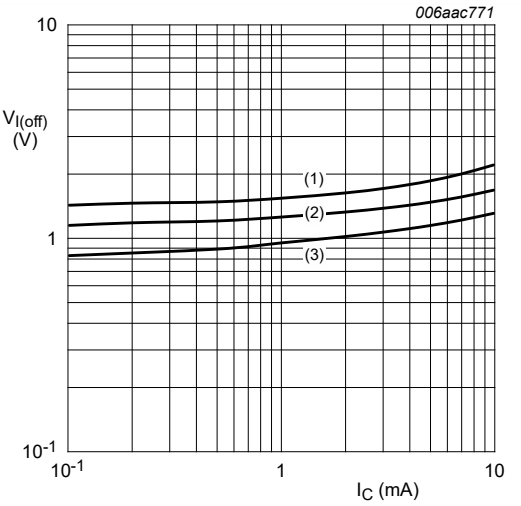

$I_C/I_B$ = 20
(1) $T_{amb}$ = 100 °C
(2) $T_{amb}$ = 25 °C
(3) $T_{amb}$ = -40 °C

**Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values**



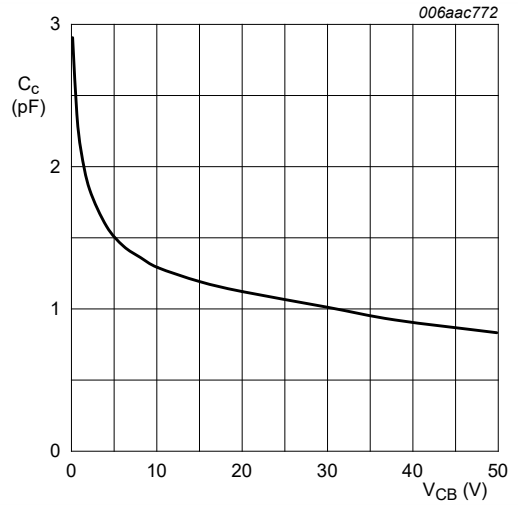

$V_{CE}$ = 0.3 V
(1) $T_{amb}$ = -40 °C
(2) $T_{amb}$ = 25 °C
(3) $T_{amb}$ = 100 °C

**Fig. 5. On-state input voltage as a function of collector current; typical values**



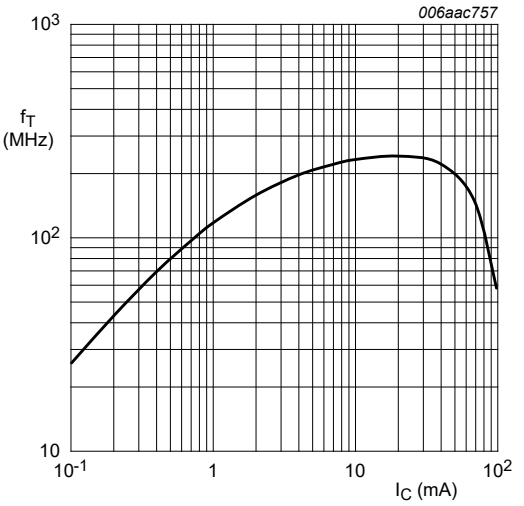

$V_{CE}$ = 5 V
(1) $T_{amb}$ = -40 °C
(2) $T_{amb}$ = 25 °C
(3) $T_{amb}$ = 100 °C

**Fig. 6. Off-state input voltage as a function of collector current; typical values**




f = 1 MHz; $T_{amb}$ = 25 °C

**Fig. 7. Collector capacitance as a function of collector-base voltage; typical values**




$V_{CE}$ = 5 V; $T_{amb}$ = 25 °C

**Fig. 8. Transition frequency as a function of collector current; typical values of built-in transistor**

# 11. Test information

## Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

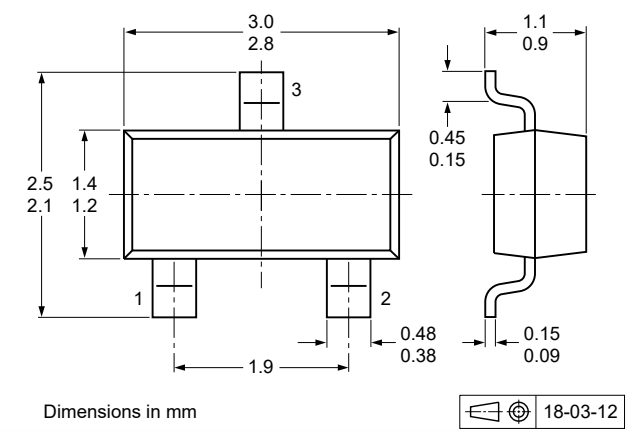



**Fig. 9. Package outline SOT23**

## 13. Soldering

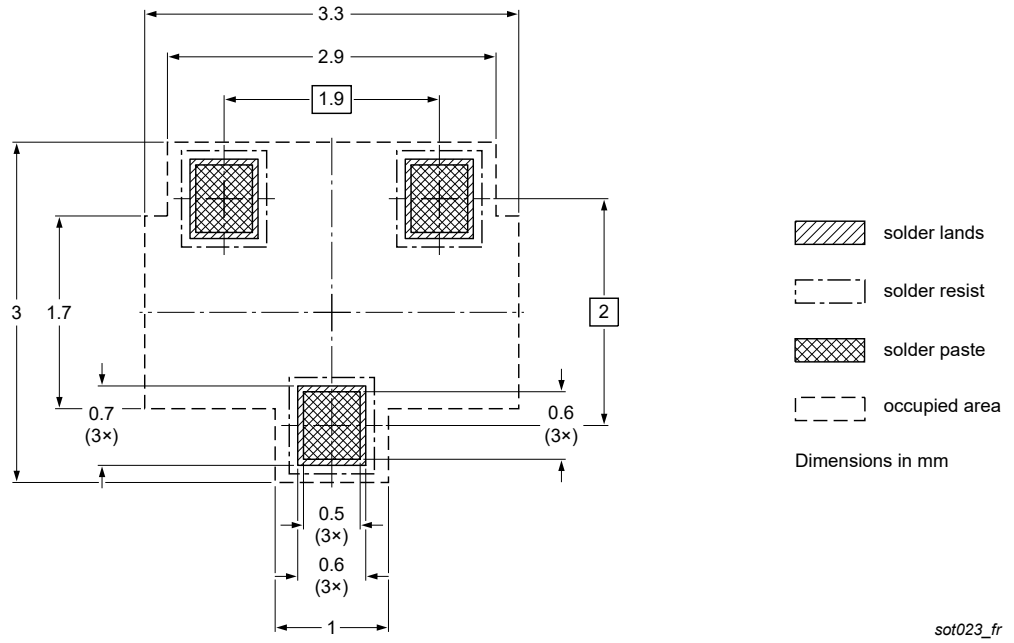



**Fig. 10. Reflow soldering footprint for SOT23**

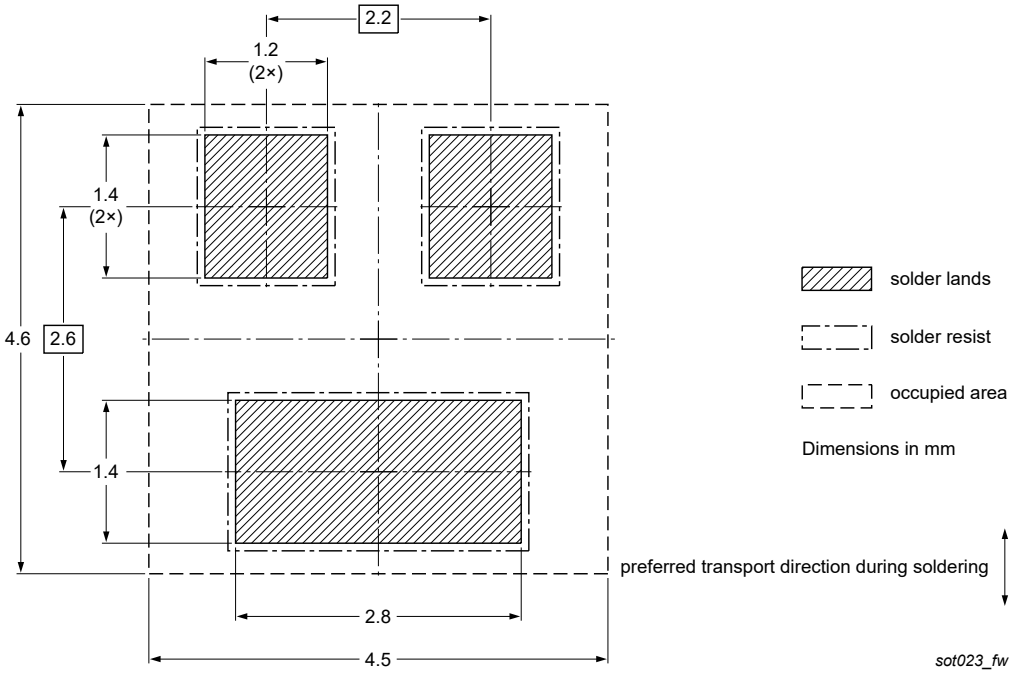



**Fig. 11. Wave soldering footprint for SOT23**

# 14. Revision history

**Table 8. Revision history**

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|---|---|---|---|---|
| PDTC114ET-Q v.1 | 20210625 | Product data sheet | - | - |

# 15. Legal information

## Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|---|---|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

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