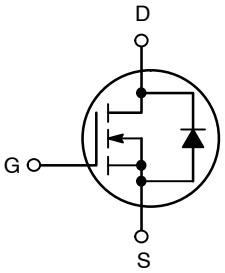
MOSFET – Power, N-Channel, SUPERFET® V, FAST
600 V, 185 mΩ, 15 A

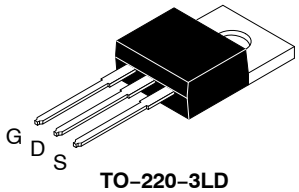
NTP185N60S5H

ON Semiconductor®

www.onsemi.com

V_{DSS}	$R_{DS(ON)}$ MAX	I_D MAX
600 V	185 mΩ @ 10 V	15 A





TO−220−3LD
CASE 340AT

Description

The SUPERFET V MOSFET is the fifth generation high voltage super−junction (SJ) MOSFET family from ON Semiconductor. SUPERFET V delivers best−in−class FOMs ($R_{DS(ON)}$·Q_G and $R_{DS(ON)}$·E_{OSS}) to improve not only heavy load but also light load efficiency. The 600 V SUPERET V series provides design benefits through reduced conduction and switching losses, while supporting extreme MOSFET dV_{DS}/dt ratings at 120 V/ns. Consequently, the SUPERFET V MOSFET FAST series helps maximize system efficiency and power density.

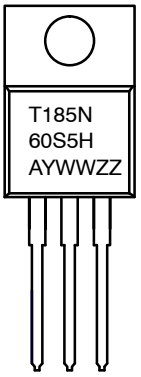
Features

- 650 V @ T_J = 150°C
- Typ. $R_{DS(on)}$ = 148 mΩ
- Ultra Low Gate Charge (Typ. Q_g = 25 nC)
- Low Time Related Output Capacitance (Typ. $C_{oss(tr.)}$ = 372 pF)
- 100% Avalanche Tested
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Telecom / Server Power Supplies
- Industrial Power Supplies
- EV Charger
- UPS / Solar

MARKING DIAGRAM



T185N60S5H = Specific Device Code
A = Assembly Plant Code
YWW = Date Code (Year & Week)
ZZ = Lot

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		600	V
V_{GSS}	Gate to Source Voltage	– DC	±30	V
		– AC (f > 1 Hz)	±30	
I_D	Drain Current	– Continuous (T_C = 25°C)	15	A
		– Continuous (T_C = 100°C)	9	
I_{DM}	Drain Current	– Pulsed (Note 1)	53	A
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		124	mJ
I_{AS}	Avalanche Current (Note 2)		3.6	A
E_{AR}	Repetitive Avalanche Energy (Note 1)		1.16	mJ
dv/dt	MOSFET dv/dt		120	V/ns
	Peak Diode Recovery dv/dt (Note 3)		20	
P_D	Power Dissipation	(T_C = 25°C)	116	W
		– Derate Above 25°C	0.93	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		–55 to +150	°C
T_L	Maximum Lead Temperature for Soldering, 1/8″ from Case for 5 seconds		260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse−width limited by maximum junction temperature.
2. I_{AS} = 3.6 A, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 7.5 A, di/dt ≤ 200 A/μs, V_{DD} ≤ 400 V, starting T_J = 25°C.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.08	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
NTP185N60S5H	T185N60S5H	TO−220−3LD (Pb-Free / Halogen Free)	50 Units / Tube

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
BV_{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 1 mA, T_J = 25°C	600	–	–	V
		V_{GS} = 0 V, I_D = 1 mA, T_J = 150°C	650	–	–	V
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temperature Coefficient	I_D = 10 mA, Referenced to 25°C	–	0.63	–	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 600 V, V_{GS} = 0 V	–	–	1	μA
		V_{DS} = 480 V, T_C = 125°C	–	0.69	–	
I_{GSS}	Gate to Body Leakage Current	V_{GS} = ±30 V, V_{DS} = 0 V	–	–	±100	nA
ON CHARACTERISTICS						
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 1.4 mA	2.7	–	4.3	V
$R_{DS(on)}$	Static Drain to Source On Resistance	V_{GS} = 10 V, I_D = 7.5 A	–	148	185	mΩ
g_{FS}	Forward Transconductance	V_{DS} = 20 V, I_D = 7.5 A	–	18	–	S
DYNAMIC CHARACTERISTICS						
C_{iss}	Input Capacitance	V_{DS} = 400 V, V_{GS} = 0 V, f = 250 kHz	–	1350	–	pF
C_{oss}	Output Capacitance		–	25	–	pF
$C_{oss(tr.)}$	Time Related Output Capacitance	I_D = Constant, V_{DS} = 0 V to 400 V, V_{GS} = 0 V	–	372	–	pF
$C_{oss(er.)}$	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	–	42	–	pF
$Q_{g(tot)}$	Total Gate Charge	V_{DD} = 400 V, I_D = 7.5 A, V_{GS} = 10 V	–	25	–	nC
Q_{gs}	Gate to Source Charge		–	7	–	nC
Q_{gd}	Gate to Drain Charge		–	8	–	nC
ESR	Equivalent Series Resistance	f = 1 MHz	–	0.9	–	Ω
SWITCHING CHARACTERISTICS						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 400 V, I_D = 7.5 A, V_{GS} = 10 V, R_g = 10 Ω	–	18	–	ns
t_r	Turn-On Rise Time		–	9	–	ns
$t_{d(off)}$	Turn-Off Delay Time		–	53	–	ns
t_f	Turn-Off Fall Time		–	4	–	ns
SOURCE-DRAIN DIODE CHARACTERISTICS						
I_S	Maximum Continuous Source to Drain Diode Forward Current		–	–	15	A
I_{SM}	Maximum Pulsed Source to Drain Diode Forward Current		–	–	53	A
V_{SD}	Source to Drain Diode Forward Voltage	V_{GS} = 0 V, I_{SD} = 7.5 A	–	–	1.2	V
t_{rr}	Reverse Recovery Time	V_{DD} = 400 V, I_{SD} = 7.5 A, dI_F/dt = 100 A/μs	–	251	–	ns
Q_{rr}	Reverse Recovery Charge		–	3	–	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

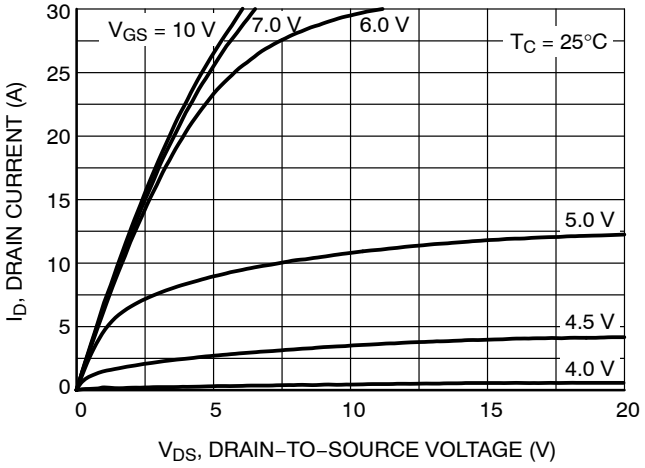


Figure 1. On−Region Characteristics

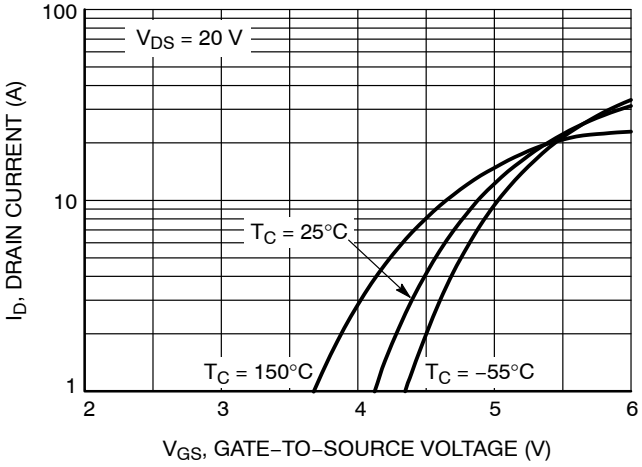


Figure 2. Transfer Characteristics

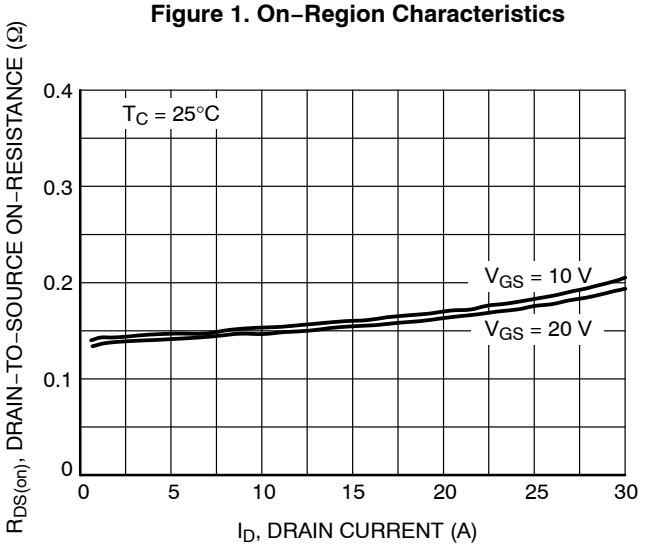


Figure 3. On−Resistance Variation vs. Drain Current and Gate Voltage

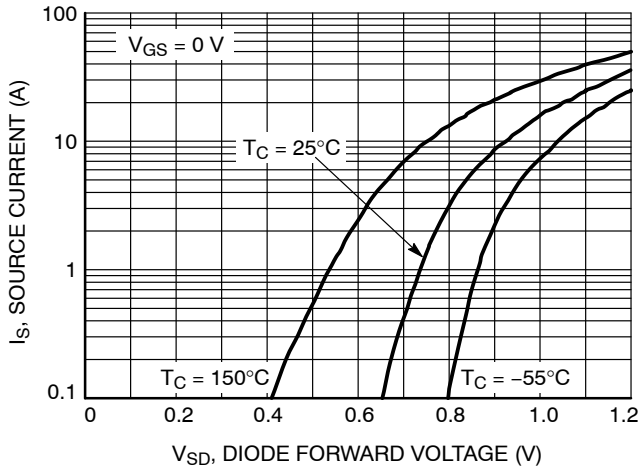


Figure 4. Diode Forward Voltage vs. Source Current

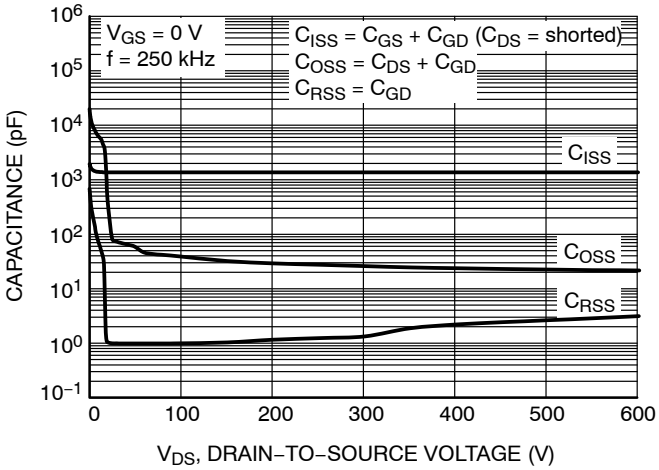


Figure 5. Capacitance Characteristics

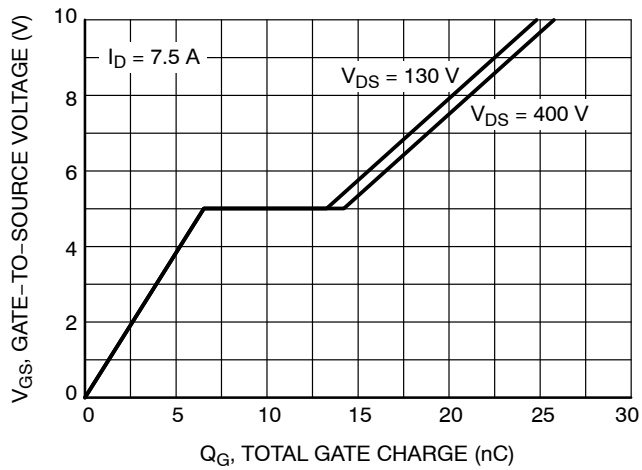


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS

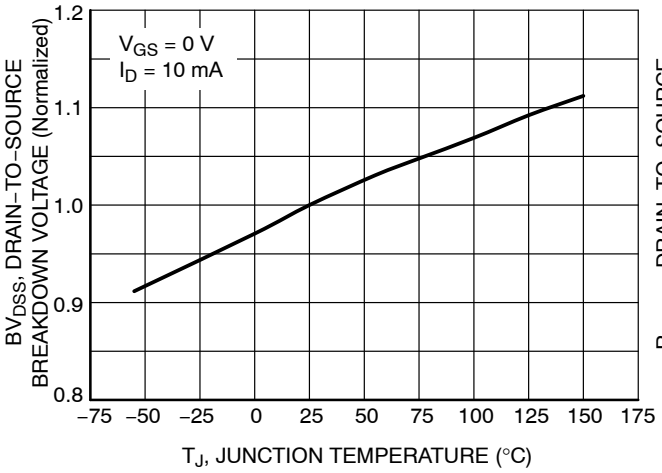


Figure 7. Breakdown Voltage Variation vs. Temperature

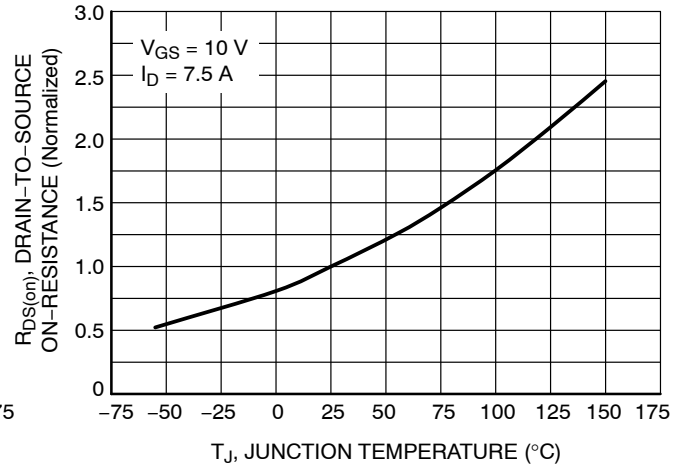


Figure 8. On−Resistance Variation vs. Temperature

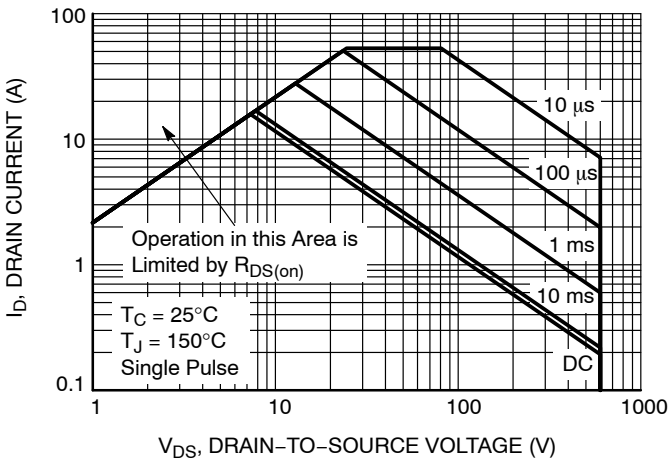


Figure 9. Maximum Safe Operating Area

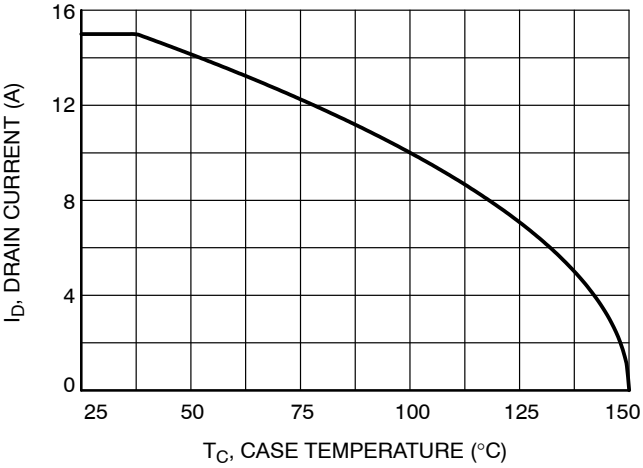


Figure 10. Maximum Drain Current vs. Case Temperature

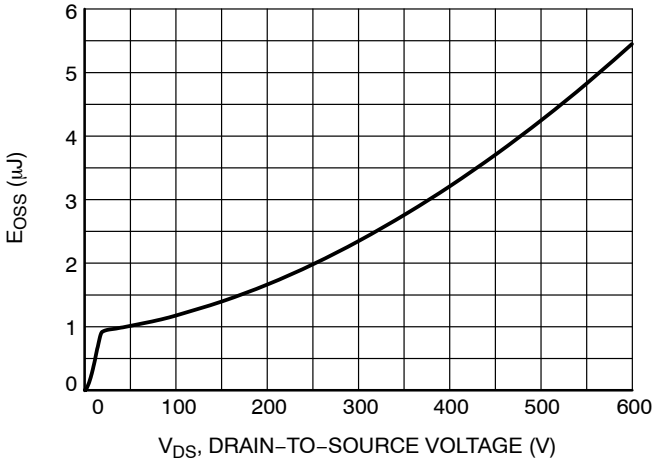


Figure 11. E_{OSS} vs. Drain−to−Source Voltage

TYPICAL CHARACTERISTICS

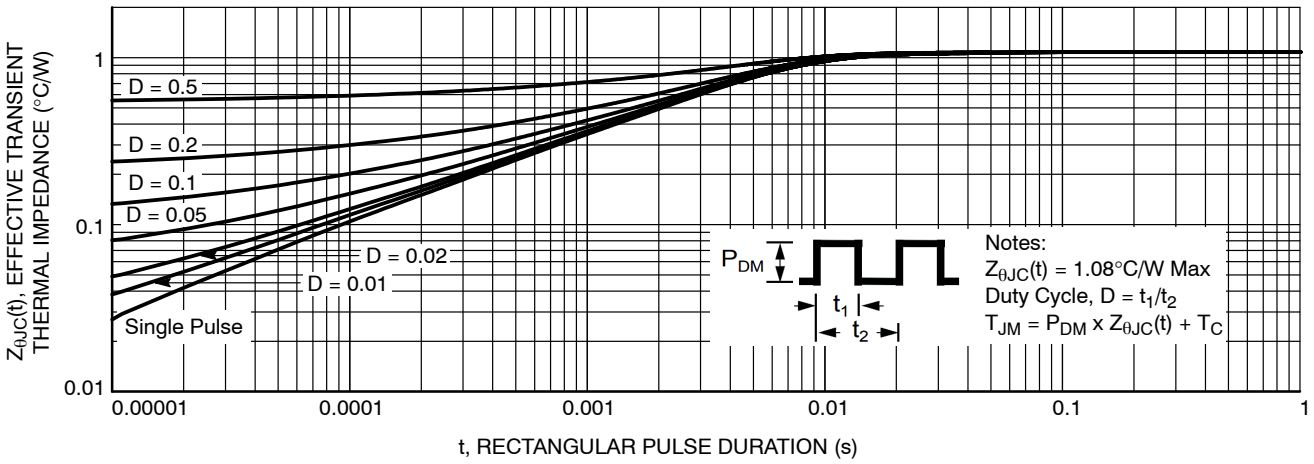


Figure 12. Transient Thermal Impedance

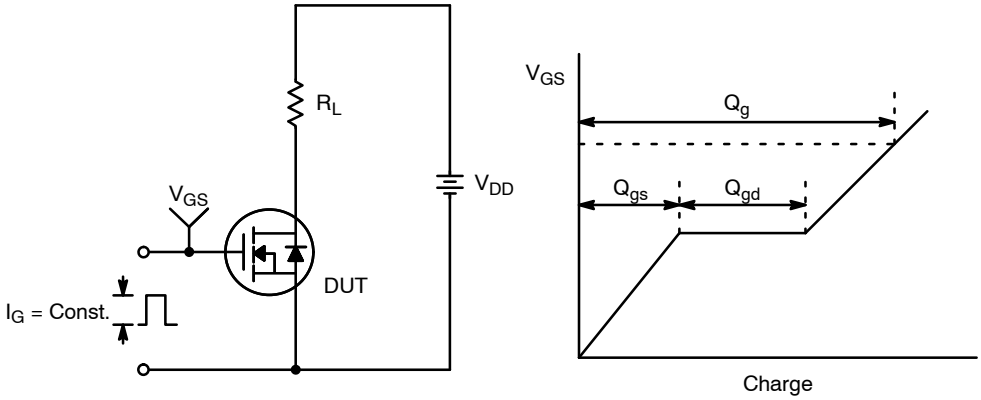


Figure 13. Gate Charge Test Circuit & Waveform

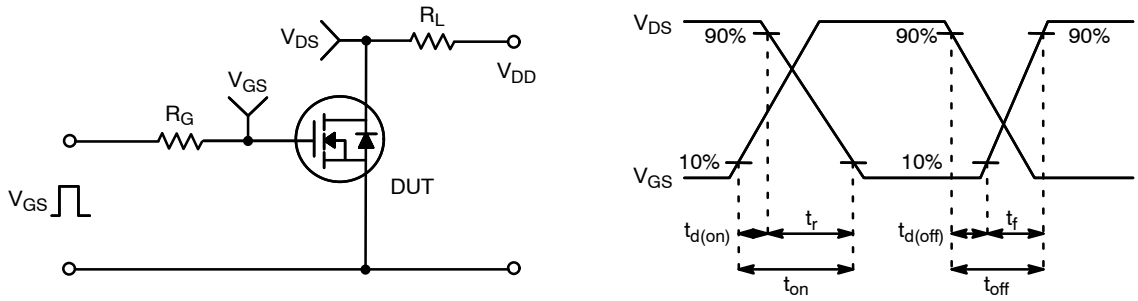


Figure 14. Resistive Switching Test Circuit & Waveforms

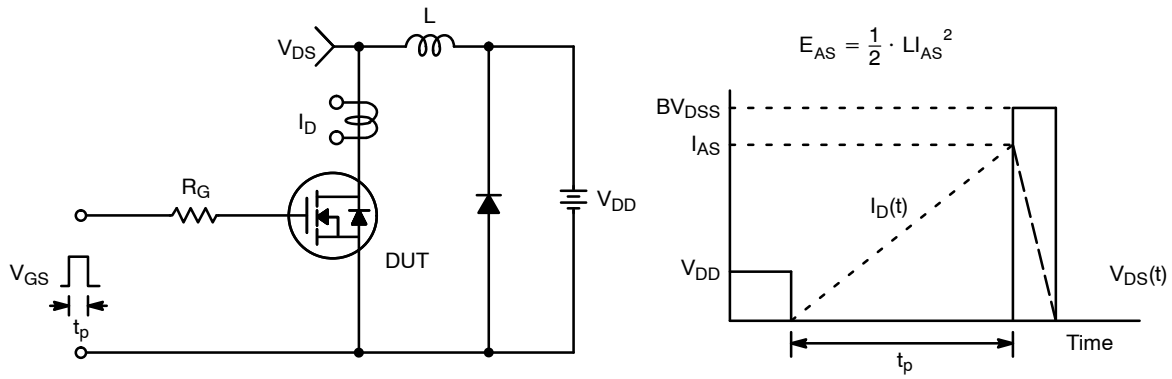


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

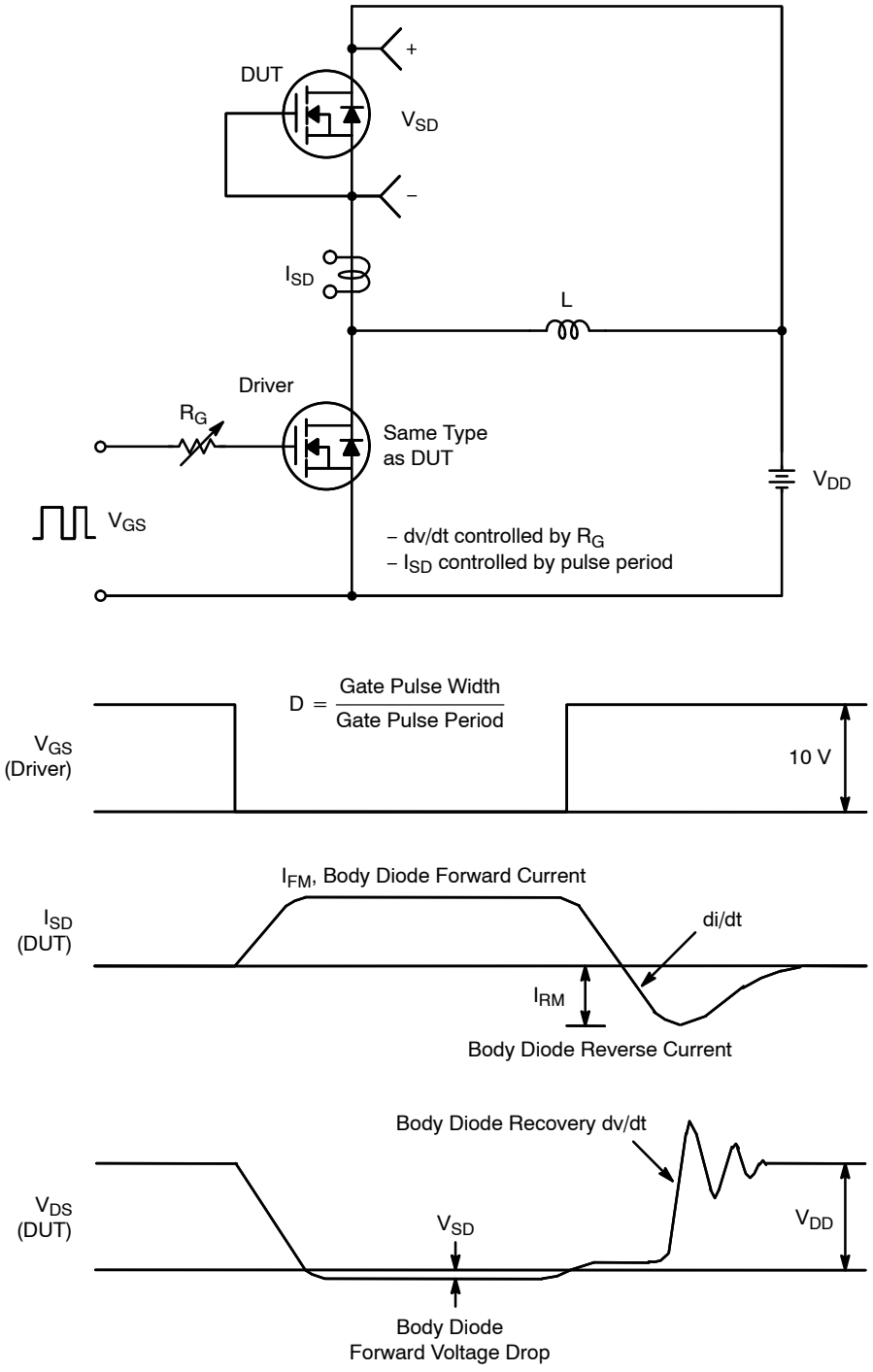
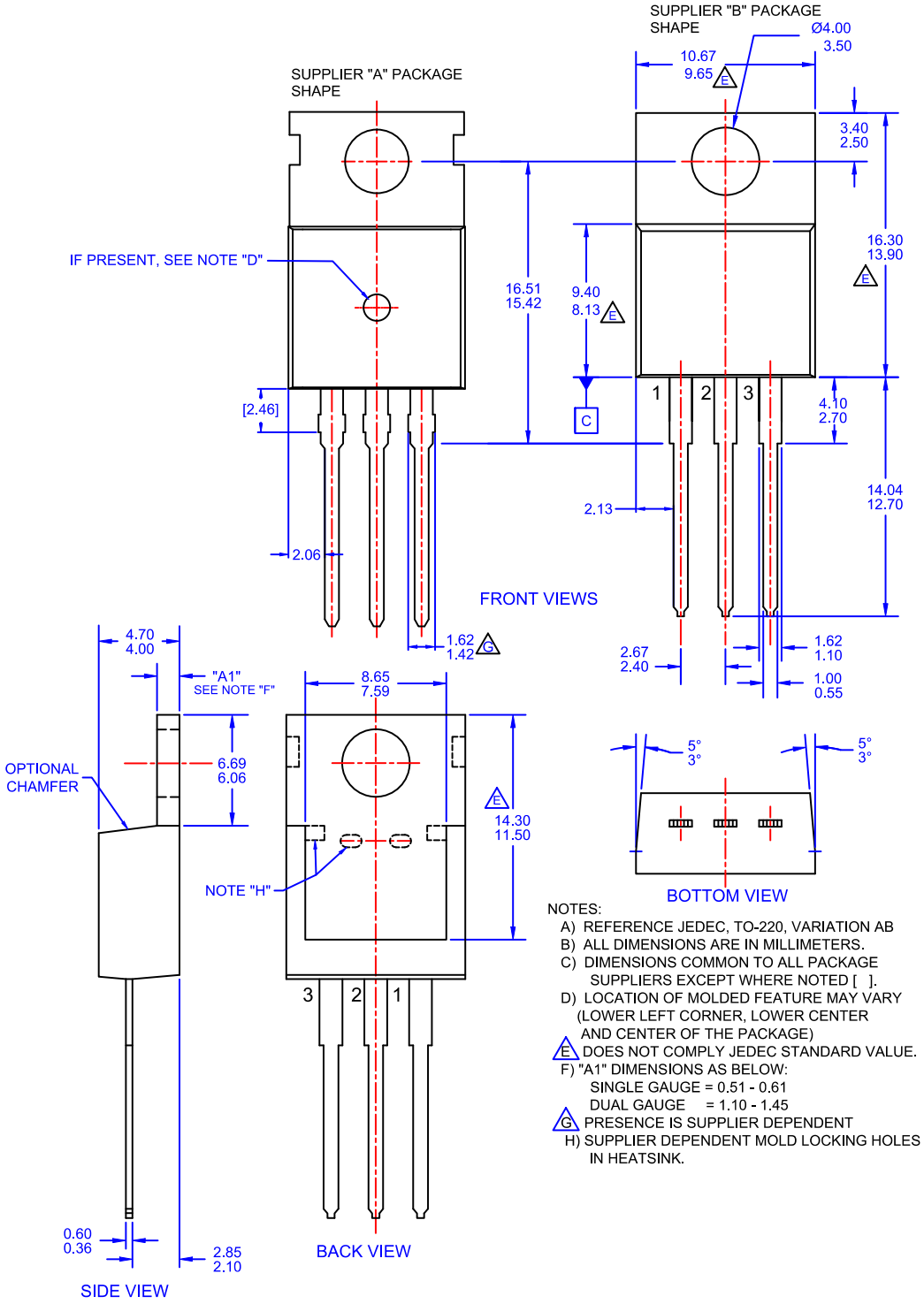


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

PACKAGE DIMENSIONS

TO−220−3LD
CASE 340AT
ISSUE A



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