

HIGH SPEED OPERATIONAL AMPLIFIER WITH SWITCH

■ GENERAL DESCRIPTION

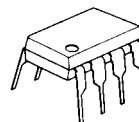
The NJM2121 is a dual operational amplifier of 2-INPUT and 1-OUTPUT with analog switch. The NJM2121 can be used as analog switch under the condition of $G_V=0\text{dB}$, as Switch + Amp in order that each gain (A or B) can be adjusted independently. Each amplifier of the NJM2121 has the same electrical characteristics as the NJM4560.

The NJM2121 is suitable for Audio, Video, Electrical musical instrument...etc.

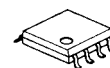
■ FEATURES

- Analog Switch Function
- Operating Voltage $(\pm 3\text{V} \sim \pm 18\text{V})$
- Slew Rate $(4\text{V}/\mu\text{s typ.})$
- Wide Unity Gain Bandwidth (14MHz typ.)
- Package Outline DIP8, DMP8, SIP8
- Bipolar Technology

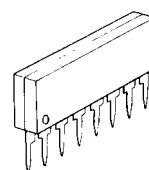
■ PACKAGE OUTLINE



NJM2121D

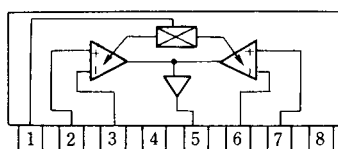
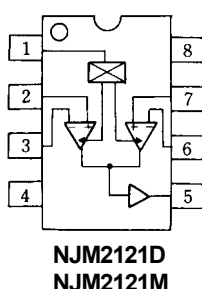


NJM2121M



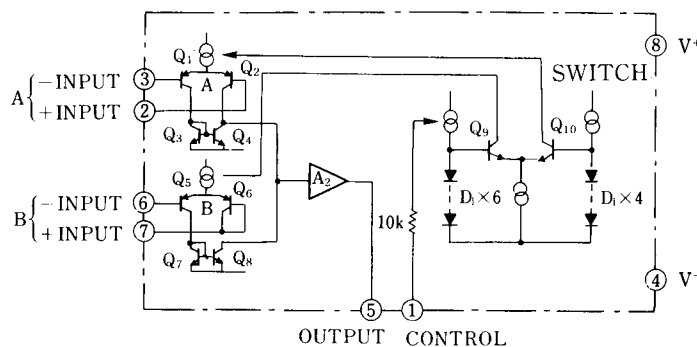
NJM2121L

■ PIN CONFIGURATION



PIN FUNCTION
 1.SW.CONTROL
 2.A +INPUT
 3.A -INPUT
 4.V-
 5.OUTPUT
 6.B -INPUT
 7.B +INPUT
 8.V+

■ EQUIVALENT CIRCUIT



NJM2121

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V^+ / V^-	± 18 (36)	V
Differential Input Voltage	V_{ID}	± 30	V
Input Voltage	V_{IC}	± 15	V
Output Current	I_O	± 50	mA
Power Dissipation	P_D	(DIP8) 500 (DMP8) 300 (SIP8) 800	mW
Operating Temperature Range	T_{opr}	-20~+75	°C
Storage Temperature Range	T_{stg}	-40~+125	°C

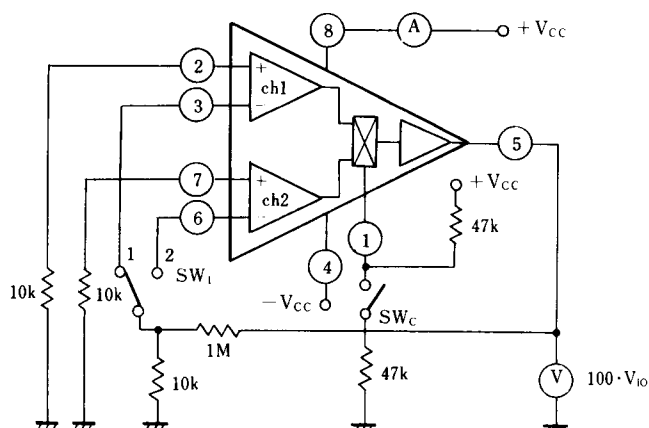
■ ELECTRICAL CHARACTERISTICS

($V^+ / V^- = \pm 15V$, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I_{CC}	V_{IN} SW ON	-	2.3	6.0	mA
		SW OFF	-	2.1	6.0	mA
Input Offset Voltage	V_{IO}	$R_S = 10k\Omega$	-	0.8	6.0	mV
Input Bias Current	I_B		-	0.2	1.0	μA
Large Signal Voltage Gain	A_V	$R_L = 2k\Omega$	-	110	-	dB
Maximum Output Voltage Swing	V_{OM}	$R_L \geq 10k\Omega$	± 12	± 14	-	V
Total Harmonic Distortion	THD	$f = 1kHz, V_O = 5V_{rms}, G_V = 20dB$	-	0.002	-	%
Supply Voltage Rejection Ratio	SVR		-	20	150	$\mu V/V$
Channel Separation	CS	$f = 1kHz$	-	82	-	dB
Unity Gain Bandwidth	f_T	$G_V = 0dB$	-	14	-	MHz
Slew Rate	SR	$G_V = 0dB, R_L = 2k\Omega / 100pF$	-	4	-	V/ μs
Equivalent Input Noise Voltage	V_{NI}	$R_S = 1k\Omega, BW = 10Hz \sim 30kHz, Flat$	-	2.0	-	μV_{rms}

■ TEST CIRCUIT

(1) I_{CC} , V_{IO} , SVR

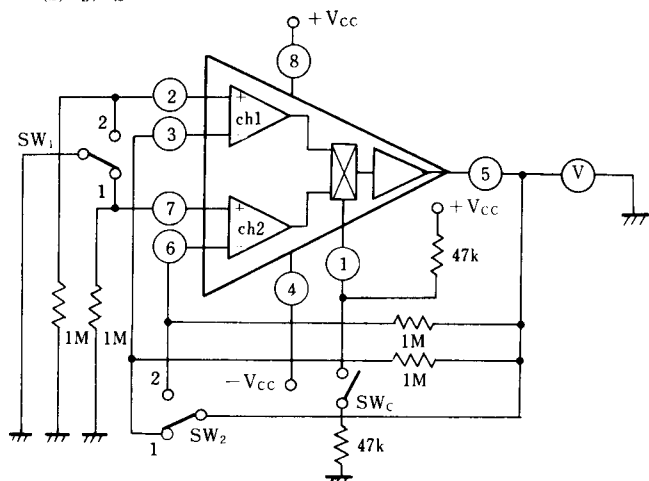


	SW _C	SW ₁	Select ch
I_{CC1} , V_{IO1} , SVR ₁	OFF	1	ch 1
I_{CC2} , V_{IO2} , SVR ₂	ON	2	ch 2

Unit Resistance : Ω

Capacity : F

(2) I_B , I_O



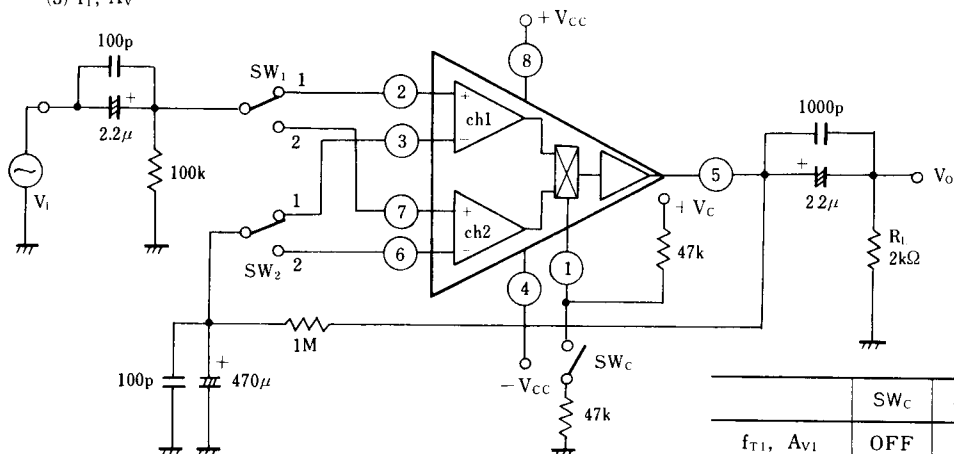
$$I_B^+ = V_{O1} / 1M\Omega$$

$$I_B^- = V_{O2} / 1M\Omega$$

$$I_{IO} = |I_B^+ - I_B^-|$$

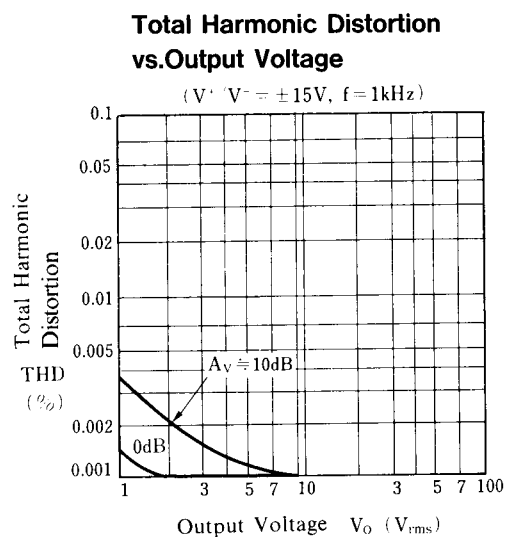
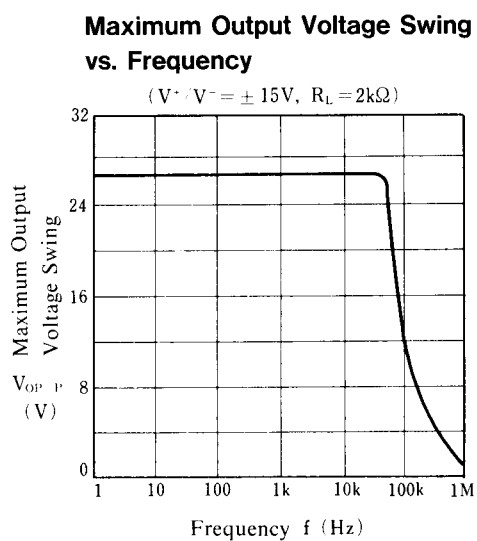
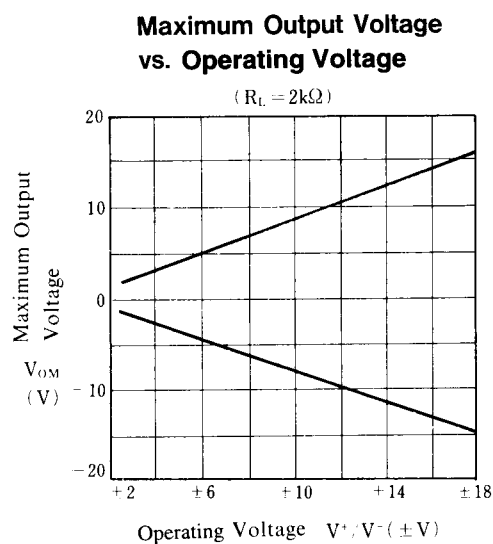
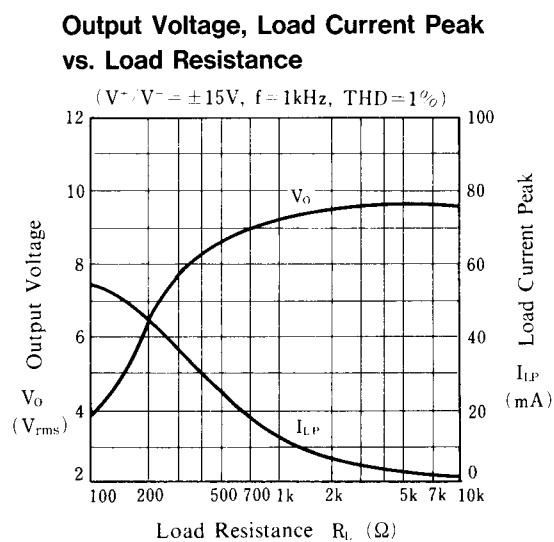
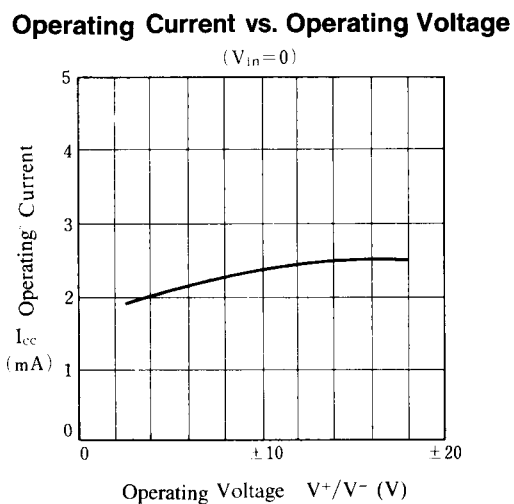
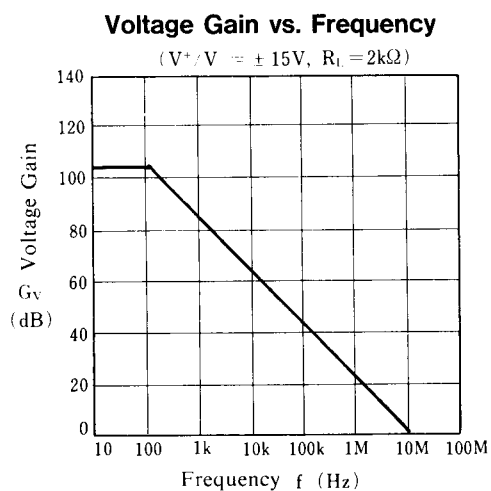
	SW _C	SW ₁	SW ₂	Select ch
V_{O1}	OFF	1	1	ch 1
V_{O1}	OFF	2	2	ch 1
V_{O2}	ON	2	2	ch 2
V_{O2}	ON	1	1	ch 2

(3) f_T , A_v



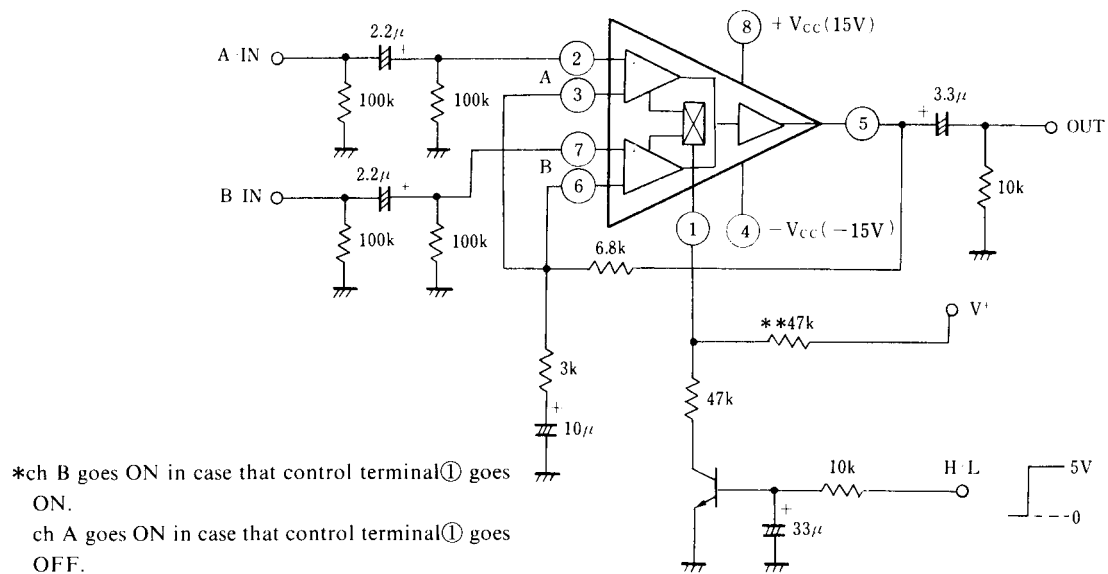
	SW _C	SW ₁	SW ₂	Select ch
f_{T1} , A_{v1}	OFF	1	1	ch 1
f_{T2} , A_{v2}	ON	2	2	ch 2

■ TYPICAL CHARACTERISTICS

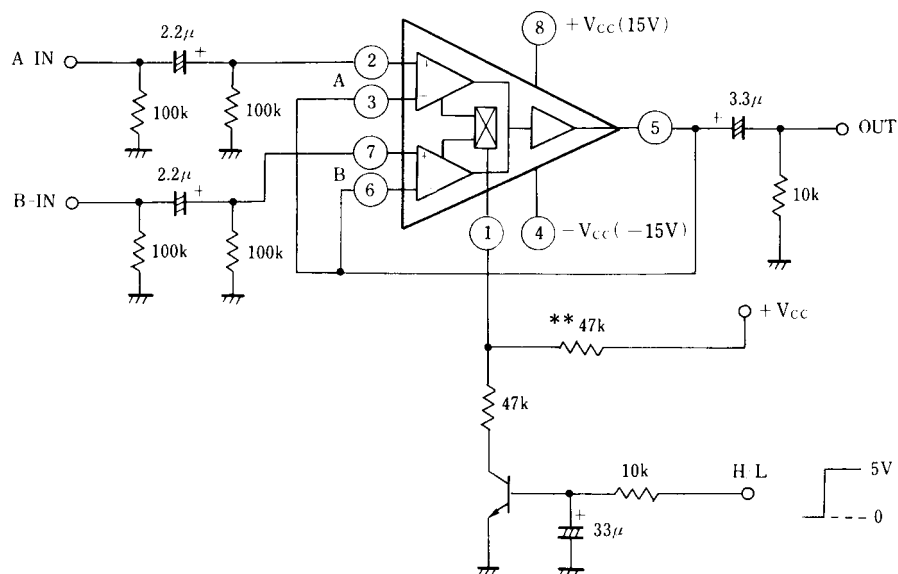


■ APPLICATION CIRCUIT

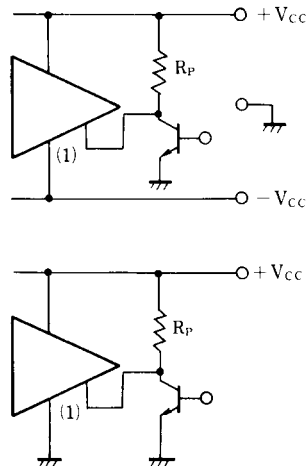
(1) $G_V=10\text{dB}$ FLAT Amp+Analog Switch Circuit



(2) Analog Switch Circuit ($G_V=0\text{dB}$ Voltage Follower Amp)



*Resistance(**) is Pull-up resistance for prevent from switching terminal① going ON by leakage of external circuit(TR...etc).



Switch signal is communicated in case that V_{cc} of Q33 goes ON, an current priming which is covered with Q33 and Q37-Q40.

2. **Prüfungsausschuss:** Der Prüfungsausschuss ist ein Gremium, das aus Vertretern der Studierenden, der Lehrenden und der Verwaltung besteht. Er ist für die Organisation der Prüfungen und die Festlegung der Prüfungsbedingungen zuständig.

[CAUTION]

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