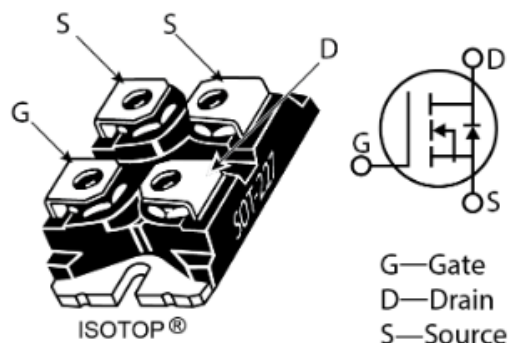


MSC040SMA120J Silicon Carbide N-Channel Power MOSFET Datasheet

1 Product Overview

This section shows the product overview for the MSC040SMA120J device.



1.1 Features

The following are key features of the MSC040SMA120J device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, $T_{J(max)} = 175\text{ }^{\circ}\text{C}$
- Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant
- Isolated voltage to 2500 V

1.2 Benefits

The following are benefits of the MSC040SMA120J device:

- High efficiency to enable lighter, more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

1.3 Applications

The MSC040SMA120J device is designed for the following applications:

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution

2 Device Specifications

This section shows the specifications for the MSC040SMA120J device.

2.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the MSC040SMA120J device.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
V_{DS}	Drain source voltage	1200	V
I_D	Continuous drain current at $T_c = 25\text{ }^\circ\text{C}$	53	A
	Continuous drain current at $T_c = 100\text{ }^\circ\text{C}$	37	
I_{DM}	Pulsed drain current ¹	105	
V_{GS}	Gate-source voltage	25 to -10	V
P_D	Total power dissipation at $T_c = 25\text{ }^\circ\text{C}$	208	W
	Linear derating factor	1.19	W/ $^\circ\text{C}$

Note:

1. Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics for the MSC040SMA120J device.

Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Typ	Max	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance		0.48	0.72	$^\circ\text{C/W}$
T_J	Operating junction temperature	-55		175	$^\circ\text{C}$
T_{STG}	Storage temperature	-55		150	
$V_{Isolation}$	RMS voltage (50-60 Hz sinusoidal waveform from terminals to mounting base for 1 minute).	2500			V
	Mounting torque, M4 screw			10	lbf-in
				1.1	N-m
W_t	Package weight		1.03		oz
			29.2		g

2.2 Electrical Performance

The following table shows the static characteristics for the MSC040SMA120J device. $T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified.

Table 3 • Static Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	1200			V
$R_{DS(on)}$	Drain-source on resistance ¹	$V_{GS} = 20\text{ V}, I_D = 40\text{ A}$		40	50	m Ω
$V_{GS(th)}$	Gate-source threshold voltage	$V_{GS} = V_{DS}, I_D = 1\text{ mA}$	1.8	2.8		V

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$\Delta V_{GS(th)}/\Delta T_J$	Threshold voltage coefficient (see Figure 11)	$V_{GS} = V_{DS}, I_D = 1 \text{ mA}$		-4.5		mV/°C
I_{OSS}	Zero gate voltage drain current	$V_{DS} = 1200 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 1200 \text{ V}, T_J = 125^\circ\text{C},$ $V_{GS} = 0 \text{ V}$			100 500	μA
I_{GSS}	Gate-source leakage current	$V_{GS} = 20 \text{ V}/-10 \text{ V}$			± 100	nA

Note:

1. Pulse test: pulse width < 380 μs , duty cycle < 2%.

The following table shows the dynamic characteristics for the MSC040SMA120J device. $T_J = 25^\circ\text{C}$ unless otherwise specified.

Table 4 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input capacitance	$V_{GS} = 0 \text{ V}, V_{DD} = 1000 \text{ V}, V_{AC} = 25 \text{ mV},$ $f = 1 \text{ MHz}$		1990		pF
C_{rss}	Reverse transfer capacitance			17		
C_{oss}	Output capacitance			156		
Q_g	Total gate charge	$V_{GS} = -5 \text{ V}/20 \text{ V}, V_{DD} = 800 \text{ V}, I_D = 40 \text{ A}$		137		nC
Q_{gs}	Gate-source charge			29		
Q_{gd}	Gate-drain charge			31		
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 800 \text{ V}, V_{GS} = 0 \text{ V}/20 \text{ V}, I_D = 40 \text{ A},$ $R_{G(ext)} = 5.3 \Omega^1$, Freewheeling diode = MSC015SDA120B		10		ns
t_r	Current rise time			10		
$t_{d(off)}$	Turn-off delay time			55		
t_f	Current fall time			25		
E_{on2}	Turn-on switching energy ²			930		μJ
E_{off}	Turn-off switching energy			585		
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 800 \text{ V}, V_{GS} = 0 \text{ V}/20 \text{ V}, I_D = 40 \text{ A},$ $R_{G(ext)} = 5.3 \Omega^1$, $T_c = 150^\circ\text{C}$, Freewheeling diode = MSC015SDA120B		10		ns
t_r	Current rise time			10		
$t_{d(off)}$	Turn-off delay time			74		
t_f	Current fall time			37		
E_{on2}	Turn-on switching energy ²			890		μJ
E_{off}	Turn-off switching energy			855		
ESR	Equivalent series resistance	$f = 1 \text{ MHz}, 25 \text{ mV}$, drain short		1.2		Ω
SCWT	Short circuit withstand time	$V_{DS} = 960 \text{ V}, V_{GS} = 20 \text{ V}, T_c = 25^\circ\text{C}$		3		μs
E_{AS}	Avalanche energy, single pulse	$V_{DS} = 150 \text{ V}, V_{GS} = 20 \text{ V}, I_D = 40 \text{ A},$ $T_c = 25^\circ\text{C}, I_L = 2.5 \text{ mH}$		2000		mJ

Notes:

1. R_G is total gate resistance excluding internal gate driver impedance.
2. E_{on2} includes energy of MSC015SDA120B freewheeling diode.

The following table shows the body diode characteristics for the MSC040SMA120J device. $T_J = 25^\circ\text{C}$ unless otherwise specified.

Table 5 • Body Diode Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{SD}	Diode forward voltage	$I_{SD} = 40\text{ A}$, $V_{GS} = 0\text{ V}$		3.9		V
		$I_{SD} = 40\text{ A}$, $V_{GS} = -5\text{ V}$		4.1		V
t_{rr}	Reverse recovery time	$I_{SD} = 40\text{ A}$, $V_{GS} = -5\text{ V}$, $V_{DD} = 800\text{ V}$, $dl/dt = -1000\text{ A}/\mu\text{s}$		100		ns
Q_{rr}	Reverse recovery charge			550		nC
I_{RRM}	Reverse recovery current			12.5		A

2.3 Typical Performance Curves

This section shows the typical performance curves for the MSC040SMA120J device.

Figure 1 • Drain Current vs. Drain-to-Source Voltage

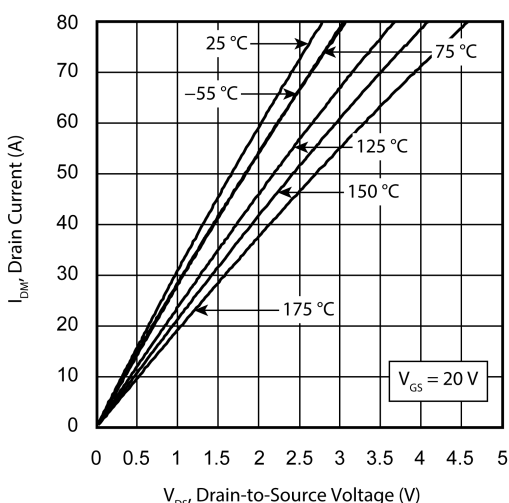


Figure 2 • Drain Current vs. Drain-to-Source Voltage

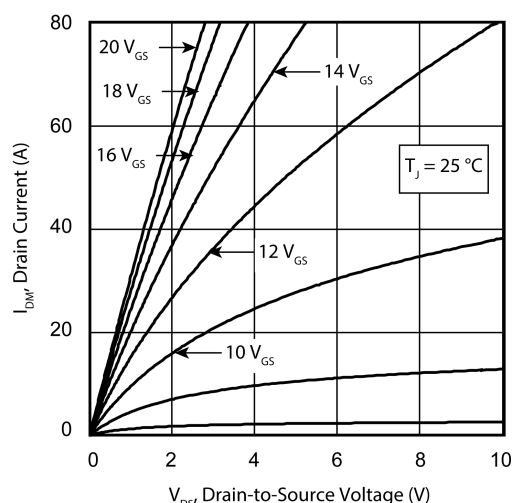


Figure 3 • Drain Current vs. Drain-to-Source Voltage

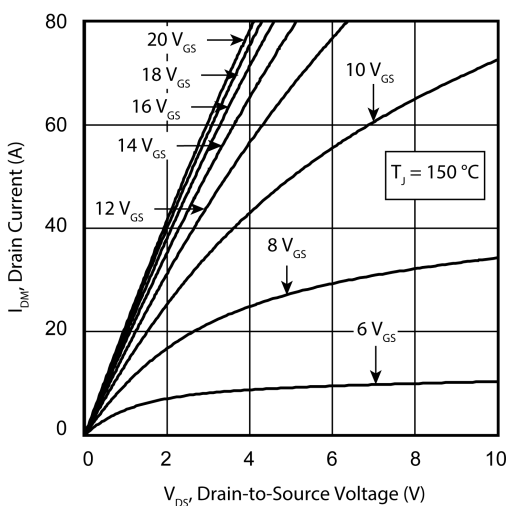


Figure 4 • Drain Current vs. Drain-to-Source Voltage

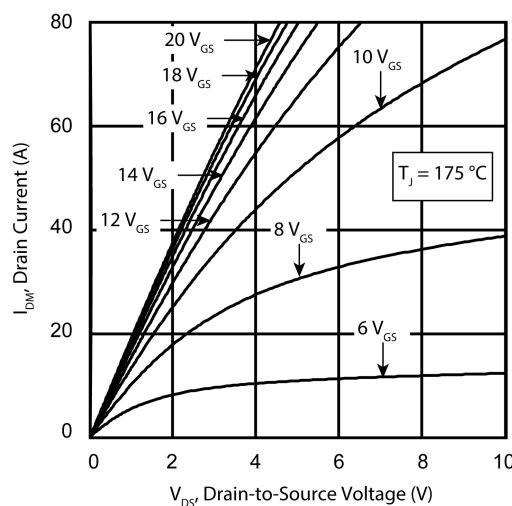


Figure 5 • $R_{DS(on)}$ vs. Junction Temperature

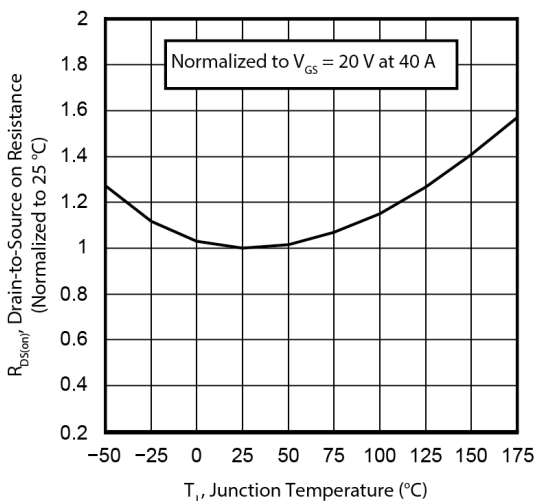


Figure 6 • Gate Charge Characteristics

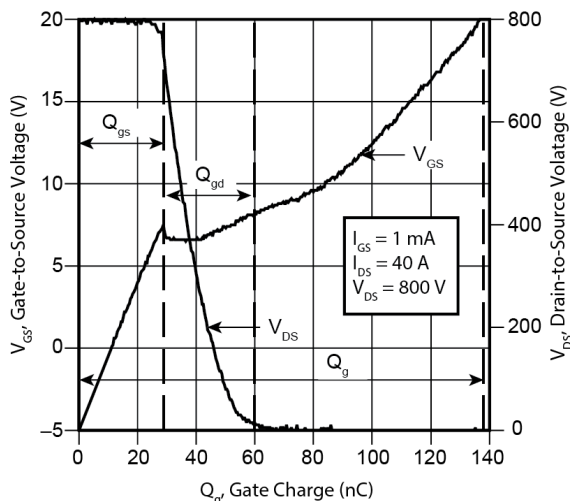


Figure 7 • Capacitance vs. Drain-to-Source Voltage

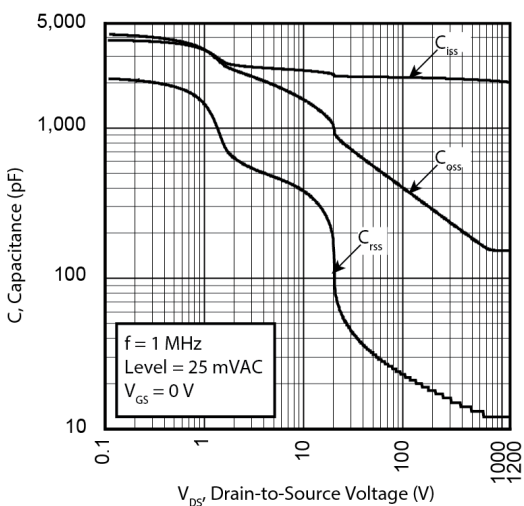


Figure 8 • IDM vs. Gate-to-Source Voltage

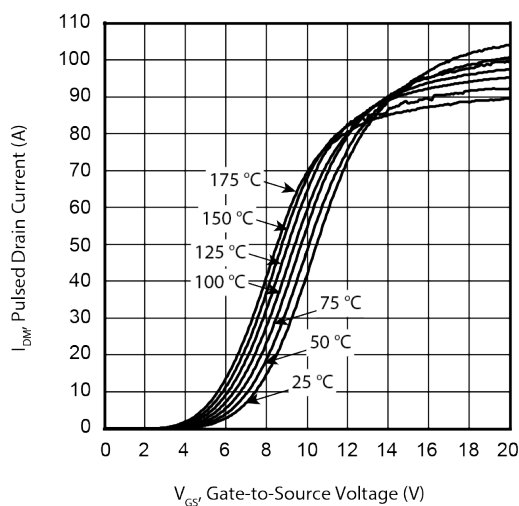


Figure 9 • IDM vs. V_DS Third Quadrant Conduction

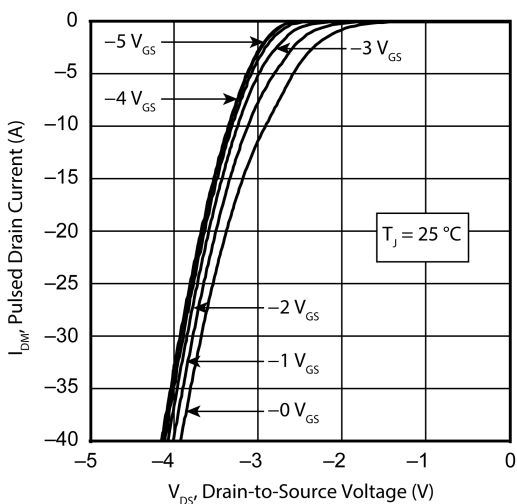


Figure 10 • IDM vs. V_DS Third Quadrant Conduction

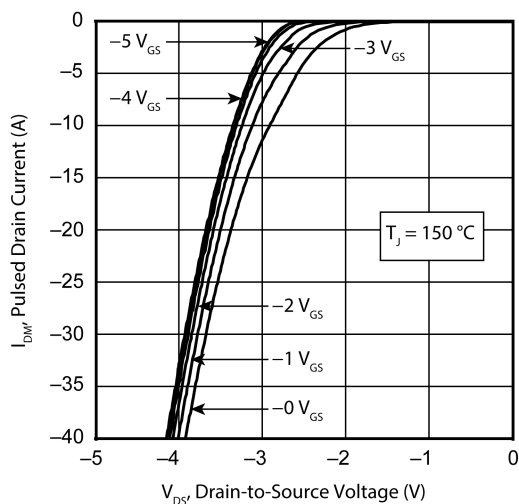


Figure 11 • V_{GS(th)} vs. Junction Temperature

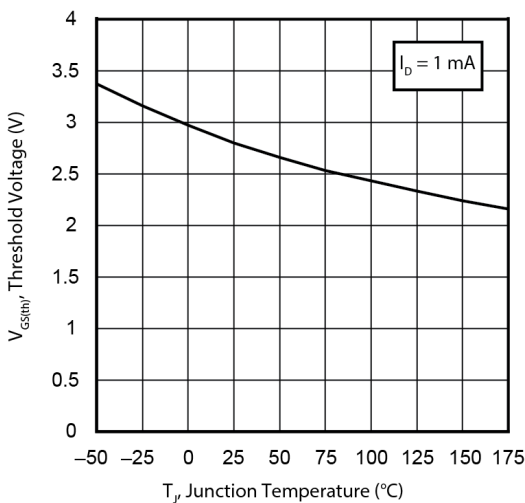


Figure 12 • Forward Safe Operating Area

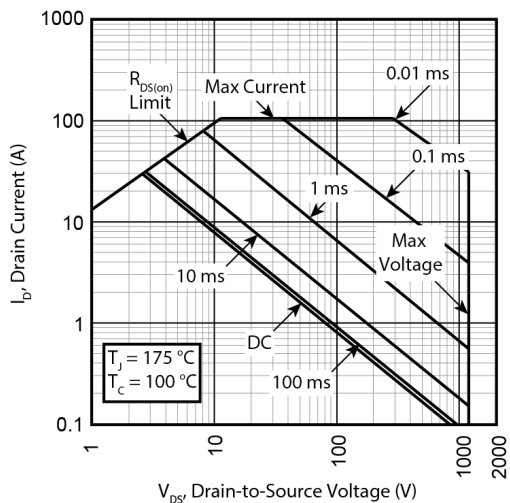
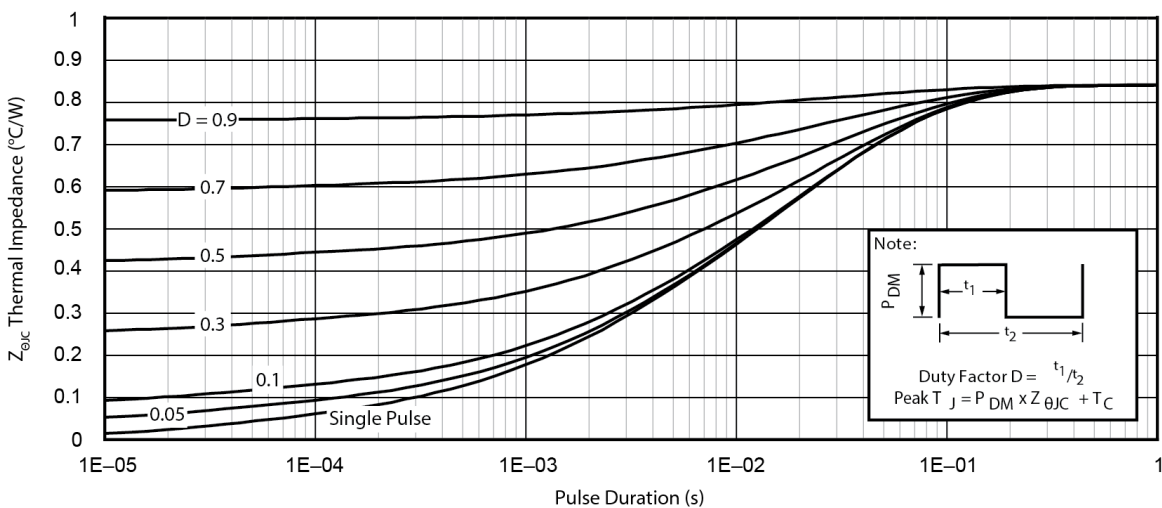


Figure 13 • Maximum Transient Thermal Impedance



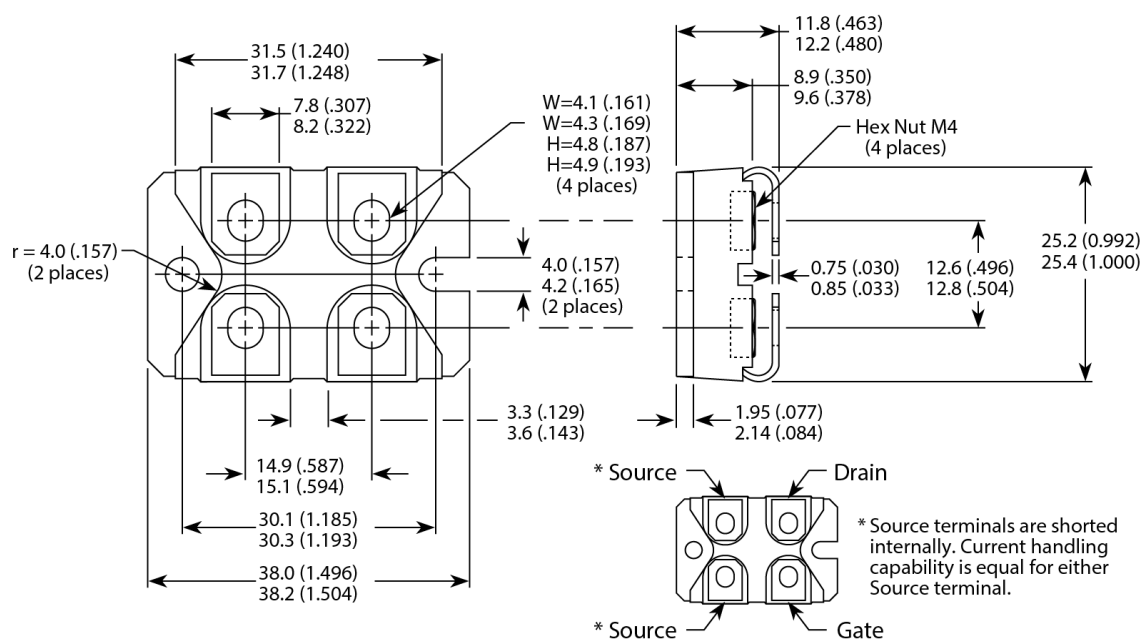
3 Package Specification

This section shows the package specification for the MSC040SMA120J device.

3.1 Package Outline Drawing

This section shows the SOT-227 package drawing for the MSC040SMA120J device. The dimensions in the figure below are in millimeters and (inches).

Figure 14 • Package Outline Drawing



**Microsemi Headquarters**

One Enterprise, Aliso Viejo,
CA 92656 USA
Within the USA: +1 (800) 713-4113
Outside the USA: +1 (949) 380-6100
Sales: +1 (949) 380-6136
Fax: +1 (949) 215-4996
Email: sales.support@microsemi.com
www.microsemi.com

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