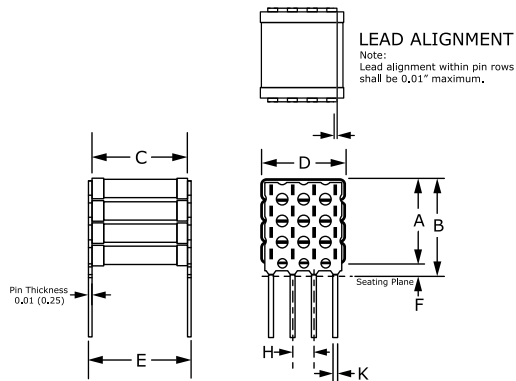


KPS-MCC Indust COG HT200C, Ceramic, 0.47 uF, 10%, 500 VDC, COG



General Information	
Series:	KPS-MCC Indust COG HT200C
Style:	Leaded Stacked Chip
Description:	Low ESR, Stacked Ceramic Chips
Features:	200C, Low ESR, High Thermal Stability, Bulk Capacitance
RoHS:	With Exemptions
REACH:	SVHC (Pb – CAS 7439-92-1)
Termination:	Silver
Lead:	Straight Leads
Notes:	Number of chips in this stack: 4

Dimensions	
D	10.16mm +/-0.635mm
L	6.35mm MIN
H	2.54mm NOM
F	1.397mm +/-0.25mm
A	10.67mm MAX
B	12.448mm MAX
C	10.16mm +/-0.635mm
E	11.18mm MAX
K	0.5mm NOM

Specifications	
Capacitance:	0.47 uF
Capacitance Tolerance:	10%
Voltage DC:	500 VDC
Dielectric Withstanding Voltage:	650 VDC
Temperature Range:	-55/+200°C
Temperature Coefficient:	COG
Dissipation Factor:	0.10% 1kHz 25C
Aging Rate:	0% Loss/Decade Hour
Insulation Resistance:	2.13 GOhms

Packaging Specifications	
Packaging:	Waffle, Box
Packaging Quantity:	25