

# MOSFET – N-Channel, SUPERFET® II

800 V, 11 A, 400 mΩ

## Product Preview FCPF400N80ZL1-F154

### Description

SUPERFET II MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. In addition, internal gate-source ESD diode allows to withstand over 2 kV HBM surge stress. Consequently, SUPERFET II MOSFET is very suitable for the switching power applications such as Audio, Laptop adapter, Lighting, ATX power and industrial power applications.

### Features

- Typ.  $R_{DS(on)} = 340 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ.  $Q_g = 43 \text{ nC}$ )
- Low  $E_{oss}$  (Typ.  $4.1 \mu\text{J} @ 400 \text{ V}$ )
- Low Effective Output Capacitance (Typ.  $C_{oss(eff.)} = 138 \text{ pF}$ )
- 100% Avalanche Tested
- ESD Improved Capacity
- These Devices are Pb-Free and are RoHS Compliant

### Applications

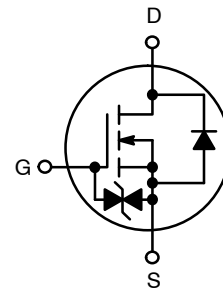
- AC-DC Power Supply
- LED Lighting



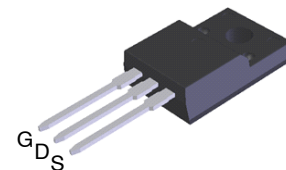
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$V_{DSS}$	$R_{DS(on)} \text{ MAX}$	$I_D \text{ MAX}$
800 V	400 mΩ @ 10 V	11 A

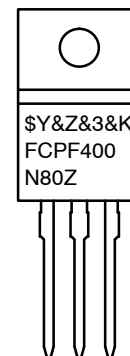


MOSFET



TO-220F Ultra Narrow Lead  
CASE 221BN

### MARKING DIAGRAM



\$Y = ON Semiconductor Logo  
&Z = Assembly Plant Code  
&3 = Data Code (Year & Week)  
&K = Lot  
FCPF400N80Z = Specific Device Code

### ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

# FCPF400N80ZL1-F154

## ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, Unless otherwise noted)

Symbol	Parameter	Value	Unit
V <sub>DSS</sub>	Drain to Source Voltage	800	V
V <sub>GSS</sub>	Gate to Source Voltage	- DC	±20
		- AC (f > 1 Hz)	±30
I <sub>D</sub>	Drain Current	- Continuous (T <sub>C</sub> = 25°C)	11*
		- Continuous (T <sub>C</sub> = 100°C)	6.9*
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	33*
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	339	mJ
I <sub>AS</sub>	Avalanche Current (Note 2)	2.2	A
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)	0.36	mJ
dv/dt	MOSFET dv/dt	100	V/ns
	Peak Diode Recovery dv/dt (Note 3)	20	V/ns
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C)	35.7
		- Derate Above 25°C	0.29
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

\*Drain current limited by maximum junction temperature.

1. Repetitive rating: pulse width limited by maximum junction temperature.
2. I<sub>AS</sub> = 2.2 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25 Ω, starting T<sub>J</sub> = 25°C.
3. I<sub>SD</sub> ≤ 11 A, di/dt ≤ 200 A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, starting T<sub>J</sub> = 25°C.

## THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction to Case, Max.	3.5	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient, Max.	62.5	°C/W

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
FCPF400N80ZL1-F154	FCPF400N80Z	TO-220F (Pb-Free)	50 Units / Tube

# FCPF400N80ZL1-F154

## ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}, T_J = 25^\circ\text{C}$	800	–	–	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 1\text{ mA}$ , Referenced to $25^\circ\text{C}$	–	0.8	–	V/ $^\circ\text{C}$
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 800\text{ V}, V_{GS} = 0\text{ V}$	–	–	25	$\mu\text{A}$
		$V_{DS} = 640\text{ V}, T_C = 125^\circ\text{C}$	–	–	250	
$I_{GSS}$	Gate to Body Leakage Current	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	–	–	$\pm 10$	$\mu\text{A}$

### ON CHARACTERISTICS

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 1.1\text{ mA}$	2.5	–	4.5	V
		$V_{GS} = V_{DS}, I_D = 0.68\text{ mA}$	2.5	–	4.5	
$R_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{ V}, I_D = 5.5\text{ A}$	–	0.34	0.4	$\Omega$
		$V_{GS} = 10\text{ V}, I_D = 7.1\text{ A}$	–	0.35	0.4	
		$V_{GS} = 10\text{ V}, I_D = 7.1\text{ A}, T_C = 150^\circ\text{C}$	–	0.89	–	
$g_{FS}$	Forward Transconductance	$V_{DS} = 20\text{ V}, I_D = 5.5\text{ A}$	–	12	–	S

### DYNAMIC CHARACTERISTICS

$C_{iss}$	Input Capacitance	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	–	1770	2350	$\text{pF}$
$C_{oss}$	Output Capacitance		–	51	70	
$C_{rss}$	Reverse Transfer Capacitance		–	0.5	–	
$C_{oss}$	Output Capacitance	$V_{DS} = 480\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	–	28	–	$\text{pF}$
$C_{oss(eff.)}$	Effective Output Capacitance	$V_{DS} = 0\text{ V to } 480\text{ V}, V_{GS} = 0\text{ V}$	–	138	–	$\text{pF}$
$Q_{g(tot)}$	Total Gate Charge at 10 V	$V_{DS} = 640\text{ V}, I_D = 11\text{ A}, V_{GS} = 10\text{ V}$ (Note 4)	–	43	56	$\text{nC}$
$Q_{gs}$	Gate to Source Gate Charge		–	8.6	–	
$Q_{gd}$	Gate to Drain "Miller" Charge		–	17	–	
ESR	Equivalent Series Resistance		$f = 1\text{ MHz}$	–	2.3	

### SWITCHING CHARACTERISTICS

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 400\text{ V}, I_D = 11\text{ A}, V_{GS} = 10\text{ V},$ $R_g = 4.7\ \Omega$ (Note 4)	–	20	50	ns
$t_r$	Turn-On Rise Time		–	12	34	
$t_{d(off)}$	Turn-Off Delay Time		–	51	112	
$t_f$	Turn-Off Fall Time		–	2.6	15	

### DRAIN-SOURCE DIODE CHARACTERISTICS

$I_S$	Maximum Continuous Source to Drain Diode Forward Current	–	–	11	A	
$I_{SM}$	Maximum Pulsed Source to Drain Diode Forward Current	–	–	33	A	
$V_{SD}$	Source to Drain Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_{SD} = 11\text{ A}$	–	–	1.2	V
$t_{rr}$	Reverse Recovery Time	$V_{DD} = 400\text{ V}, I_{SD} = 11\text{ A},$ $dI_F/dt = 100\text{ A}/\mu\text{s}$	–	395	–	ns
$Q_{rr}$	Reverse Recovery Charge		–	7.4	–	$\mu\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

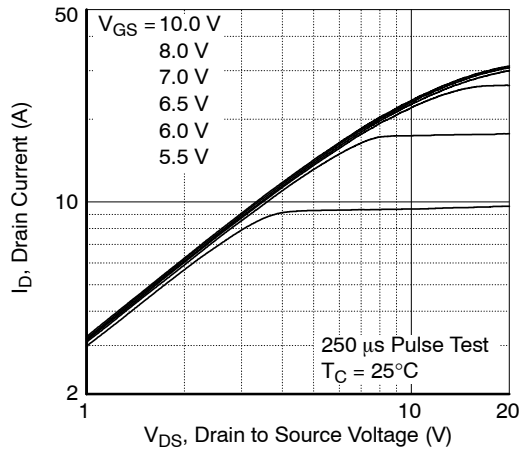


Figure 1. On-Region Characteristics

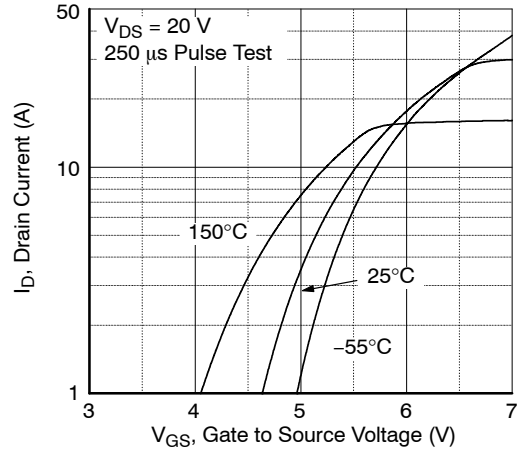


Figure 2. Transfer Characteristics

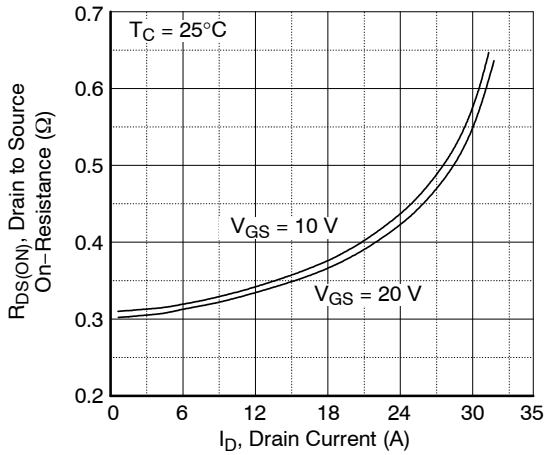


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

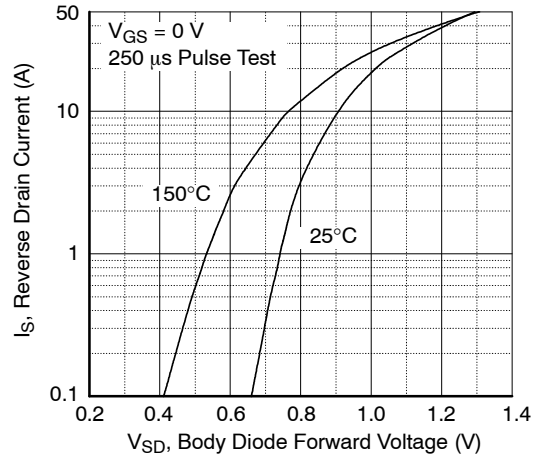


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

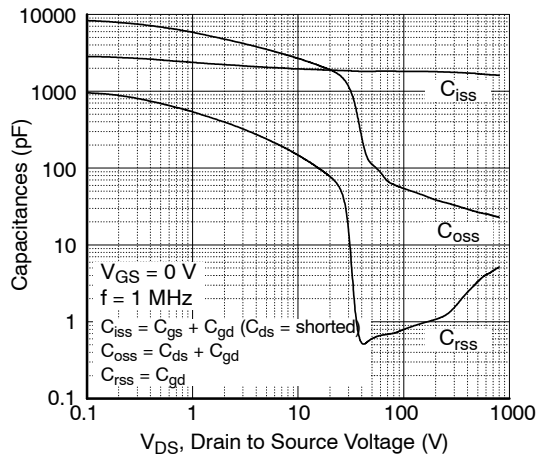


Figure 5. Capacitance Characteristics

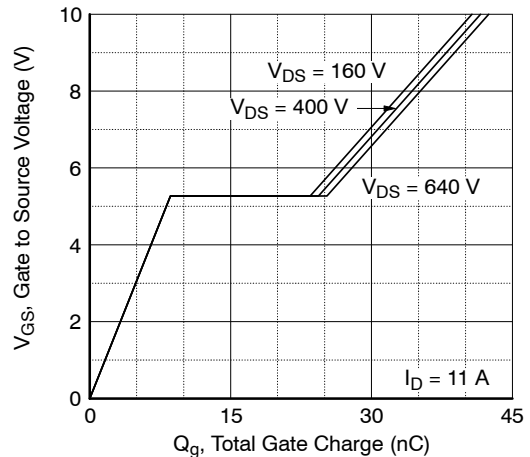


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

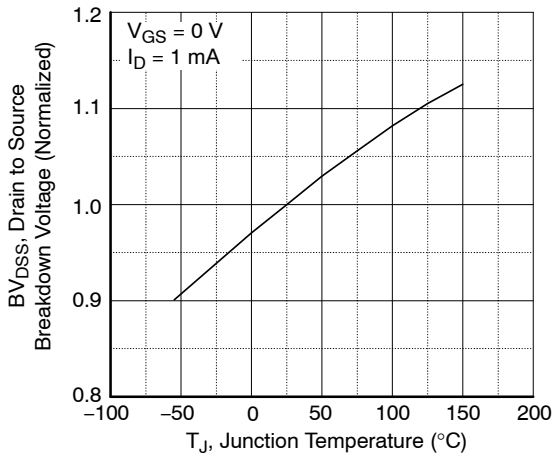


Figure 7. Breakdown Voltage Variation vs. Temperature

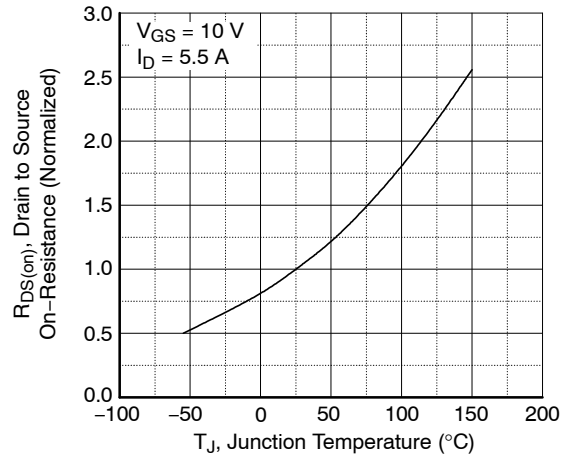


Figure 8. On-Resistance Variation vs. Temperature

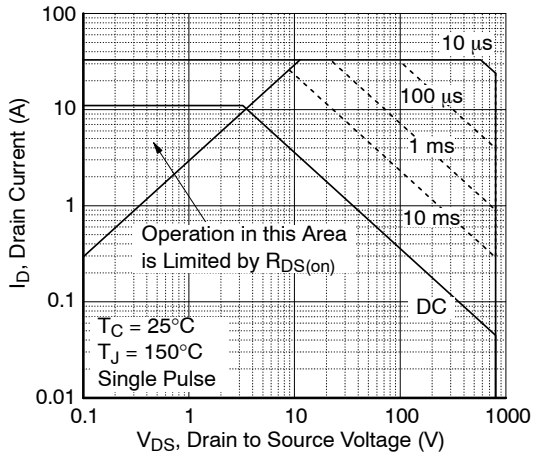


Figure 9. Maximum Safe Operating Area

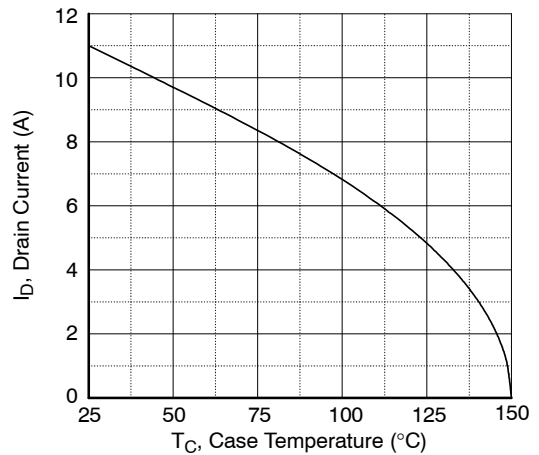


Figure 10. Maximum Drain Current vs. Case Temperature

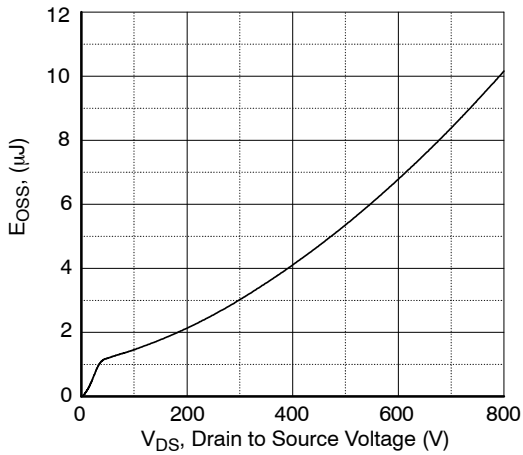


Figure 11. E<sub>OSS</sub> vs. Drain to Source Voltage

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## TYPICAL PERFORMANCE CHARACTERISTICS (continued)

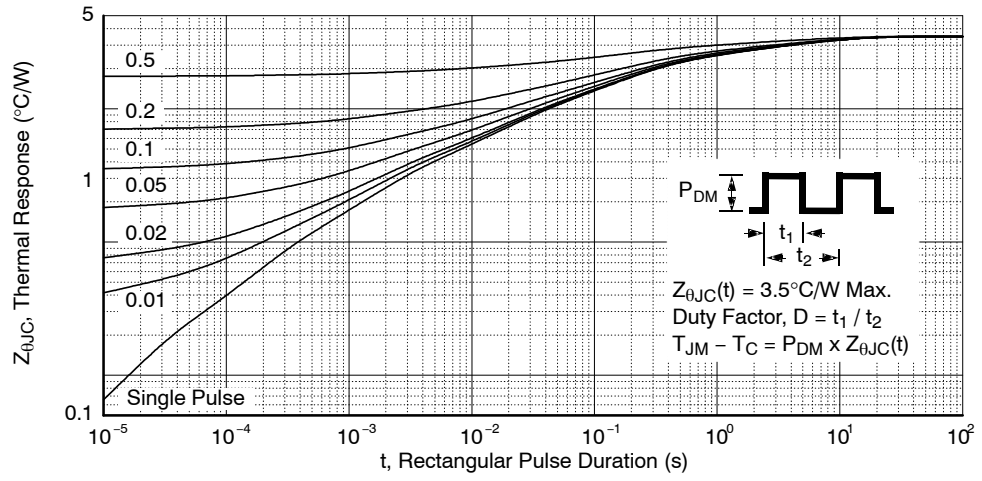


Figure 12. Transient Thermal Response Curve

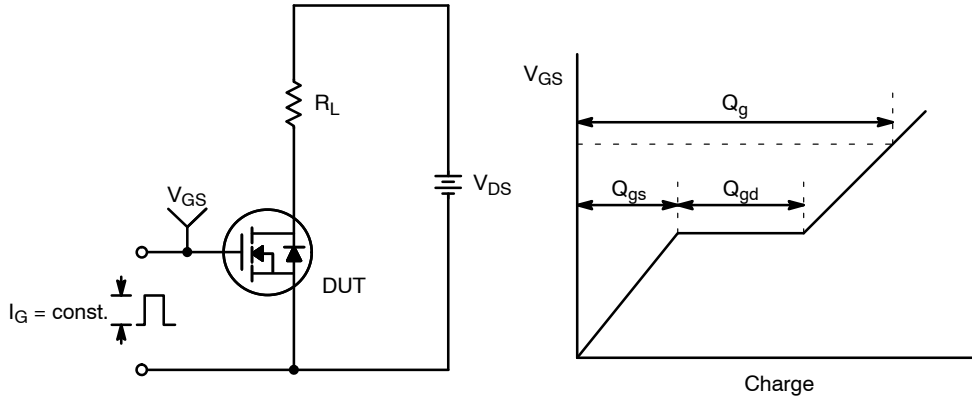


Figure 13. Gate Charge Test Circuit & Waveform

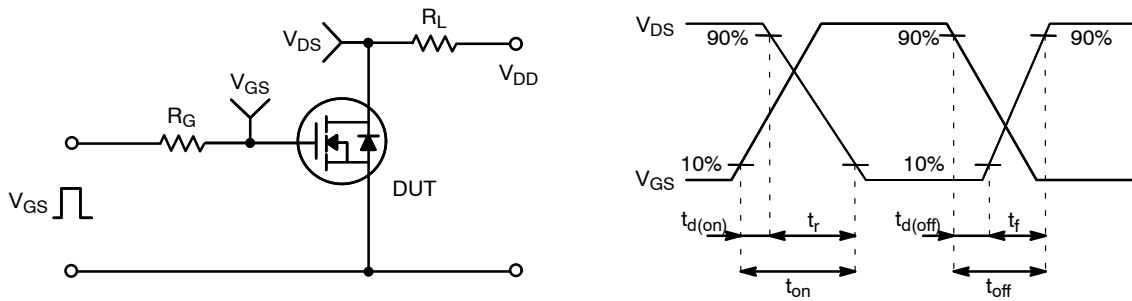


Figure 14. Resistive Switching Test Circuit & Waveforms

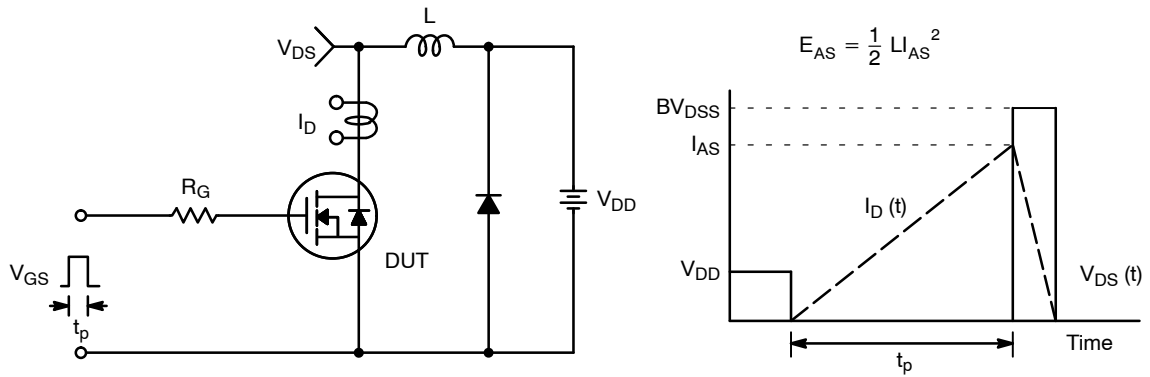
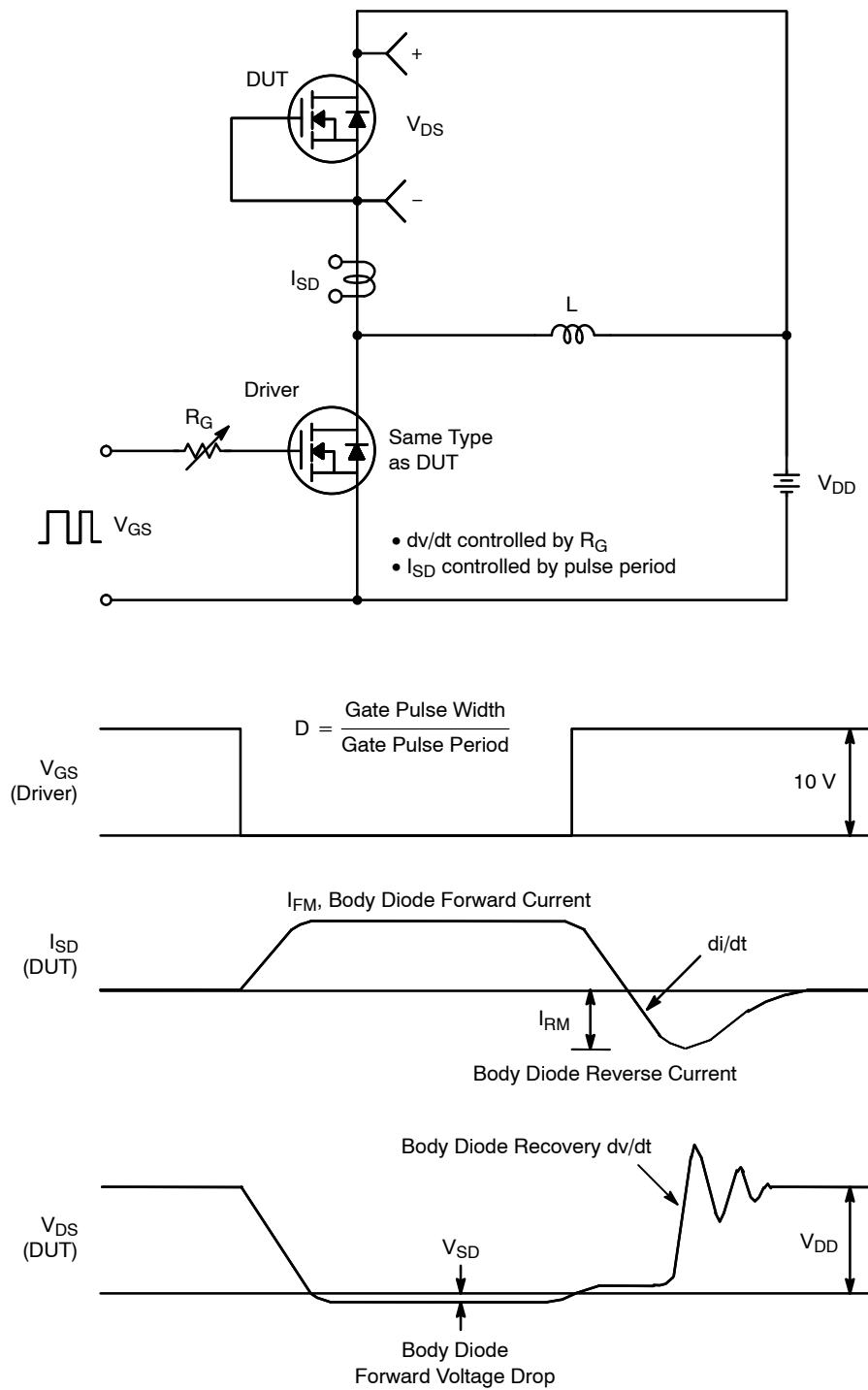


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

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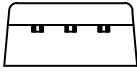


**Figure 16. Peak Recovery  $dv/dt$  Test Circuit & Waveforms**

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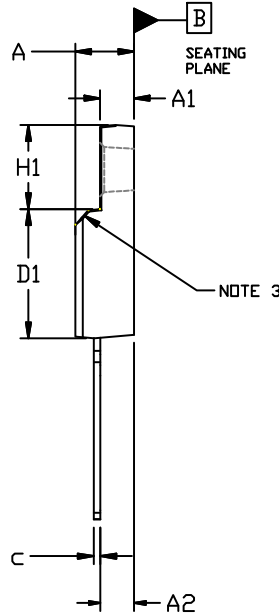
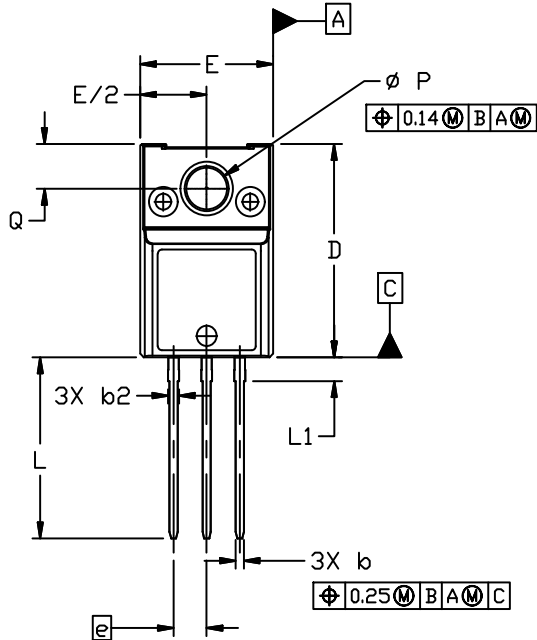
## PACKAGE DIMENSIONS

### TO-220 FULLPACK, 3-LEAD CASE 221BN ISSUE O



#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. CONTOUR UNCONTROLLED IN THIS AREA.
4. DIMENSIONS EXCLUDE BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS.



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	4.60	4.70	4.80
A1	2.50	2.60	2.70
A2	2.47	2.57	2.67
b	0.56	0.63	0.69
b2	---	---	0.90
c	0.46	0.53	0.59
D	15.80	16.00	16.20
D1	9.58	9.68	9.78
E	10.00	10.20	10.40
e	2.54 BSC		
H1	6.32 REF		
L	13.45	13.60	13.75
L1	1.70	1.80	1.90
P	3.00	3.10	3.20
Q	3.25	3.35	3.45

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