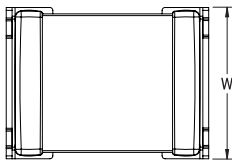
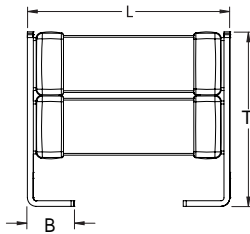


KPS SMD Comm X7R, Ceramic, 33 uF, 20%, 16 VDC, X7R, 2220-2

TOP VIEW
Single or Double Chip Stack



SIDE VIEW
Double Chip Stack



Dimensions

L	6mm +/-0.5mm
W	5mm +/-0.5mm
T	5mm +/-0.50mm
B	1.6mm +/-0.3mm

Packaging Specifications

Packaging:	T&R, 180mm, Plastic Tape
Packaging Quantity:	200

General Information

Series:	KPS SMD Comm X7R
Style:	Stacked Chip
Description:	SMD, MLCC, Stacked, Double Chip, Temperature Stable
Features:	Temperature Stable
RoHS:	Yes
Termination:	Tin
AEC-Q200:	No
Chip Size:	2220-2
Shelf Life:	78 Weeks
MSL:	1

Specifications

Capacitance:	33 uF
Measurement Condition:	120 Hz 0.5Vrms
Capacitance Tolerance:	20%
Voltage DC:	16 VDC
Dielectric Withstanding Voltage:	40 VDC
Temperature Range:	-55/+125°C
Temperature Coefficient:	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	15%, 120Hz 0.5Vrms
Dissipation Factor:	3.5% 120 Hz 0.5Vrms
Aging Rate:	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance:	15 MOhms

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