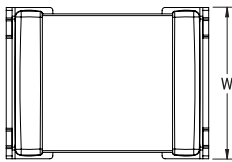
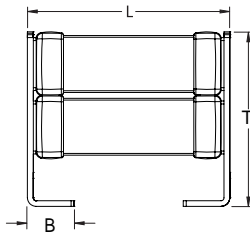


KPS SMD Comm X7R, Ceramic, 33 uF, 20%, 25 VDC, X7R, 2220-2

TOP VIEW  
Single or Double Chip Stack



SIDE VIEW  
Double Chip Stack



**Dimensions**

<b>L</b>	6mm +/-0.5mm
<b>W</b>	5mm +/-0.5mm
<b>T</b>	5mm +/-0.50mm
<b>B</b>	1.6mm +/-0.3mm

**Packaging Specifications**

<b>Packaging:</b>	T&R, 180mm, Plastic Tape
<b>Packaging Quantity:</b>	200

**General Information**

<b>Series:</b>	KPS SMD Comm X7R
<b>Style:</b>	Stacked Chip
<b>Description:</b>	SMD, MLCC, Stacked, Double Chip, Temperature Stable
<b>Features:</b>	Temperature Stable
<b>RoHS:</b>	Yes
<b>Termination:</b>	Tin
<b>AEC-Q200:</b>	No
<b>Chip Size:</b>	2220-2
<b>Shelf Life:</b>	78 Weeks
<b>MSL:</b>	1

**Specifications**

<b>Capacitance:</b>	33 uF
<b>Measurement Condition:</b>	120 Hz 0.5Vrms
<b>Capacitance Tolerance:</b>	20%
<b>Voltage DC:</b>	25 VDC
<b>Dielectric Withstanding Voltage:</b>	62.5 VDC
<b>Temperature Range:</b>	-55/+125°C
<b>Temperature Coefficient:</b>	X7R
<b>Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):</b>	15%, 120Hz 0.5Vrms
<b>Dissipation Factor:</b>	3.5% 120 Hz 0.5Vrms
<b>Aging Rate:</b>	3% Loss/Decade Hour: Referee Time is 1000 Hours
<b>Insulation Resistance:</b>	15 MOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.