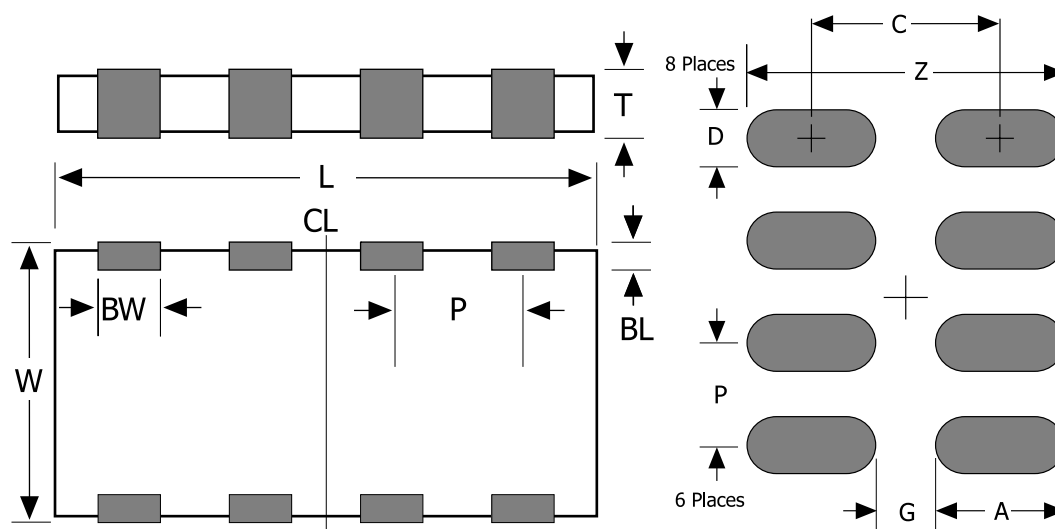


Ceramic Capacitor Array, 0.022 uF +/- 10%, 50V, 1632



Dimensions (mm)

Specifications

ID	Dimension	Tolerance	Application:	Standard Array
L	3.20	+/-0.20	Capacitance:	0.022 uF
W	1.60	+/-0.20	Tolerance:	10%
T	1.350	max	Voltage:	50V
BW	0.400	+/-0.20	Chip Size:	0612
BL	0.10	-0.5	Temperature Coefficient:	X7R
P	0.800	+/-0.10	Termination Type:	Nickel Barrier
A	1.20	ref	Terminations/Elements:	8/4
Z	2.80	ref	Circuit Type:	Parallel
D	0.52	ref	Marking:	Unmarked
G	0.40	ref	Packaging:	T&R, 7 in.
C	1.60	ref	Package Quantity:	4000