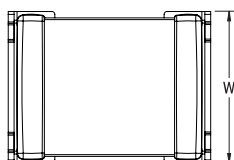
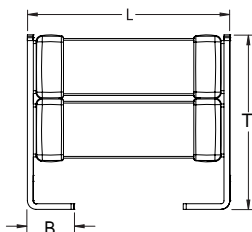


KPS SMD Comm X8L HT150C, Ceramic, 10 uF, 20%, 25 VDC, X8L, 1210-2

TOP VIEW  
Single or Double Chip Stack



SIDE VIEW  
Double Chip Stack



### Dimensions

<b>L</b>	3.5mm +/-0.3mm
<b>W</b>	2.6mm +/-0.3mm
<b>T</b>	6.15mm +/-0.15mm
<b>B</b>	0.8mm +/-0.15mm

### Packaging Specifications

<b>Packaging:</b>	T&R, 180mm, Plastic Tape
<b>Packaging Quantity:</b>	300

### General Information

<b>Series:</b>	KPS SMD Comm X8L HT150C
<b>Style:</b>	Stacked Chip
<b>Description:</b>	SMD, MLCC, Stacked, Double Chip, High Temperature
<b>Features:</b>	High Temperature
<b>RoHS:</b>	Yes
<b>Termination:</b>	Tin
<b>AEC-Q200:</b>	No
<b>Chip Size:</b>	1210-2
<b>Shelf Life:</b>	78 Weeks
<b>MSL:</b>	1

### Specifications

<b>Capacitance:</b>	10 uF
<b>Measurement Condition:</b>	1 kHz 1.0Vrms
<b>Capacitance Tolerance:</b>	20%
<b>Voltage DC:</b>	25 VDC
<b>Dielectric Withstanding Voltage:</b>	62.5 VDC
<b>Temperature Range:</b>	-55/+150°C
<b>Temperature Coefficient:</b>	X8L
<b>Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):</b>	+15% (-55C to +125C), +15/-40% (125C to 150C), 1kHz 1.0Vrms
<b>Dissipation Factor:</b>	2.5% 1 kHz 1.0Vrms
<b>Aging Rate:</b>	3% Loss/Decade Hour: Referee Time is 1000 Hours
<b>Insulation Resistance:</b>	50 MOhms