



SAW Components

Data Sheet B3740

Data Sheet

An abstract, grayscale background graphic featuring a globe with a grid pattern, overlaid with a large, stylized, and slightly blurred "EPCOS" logo. The logo is rendered in a light gray, almost white, color, giving it a three-dimensional appearance as if it's floating or attached to the globe. The overall effect is a modern, technological aesthetic.



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Low-loss Filter

433,92 MHz

Data Sheet

Features

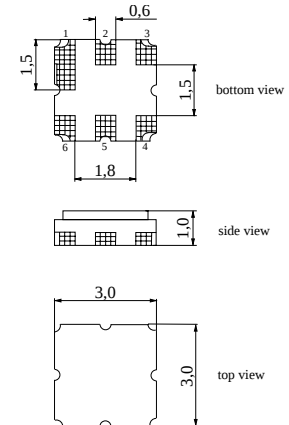
- RF low-loss filter for remote control receivers
- Package for **Surface Mounted Technology (SMT)**
- Balanced and unbalanced operation possible
- Passivation layer: Elpas
- Compliant to EU RoHs Directive (2002/95/EC)
- Lead free soldering compatible with J - STD20C

Terminals

- Ni, gold plated

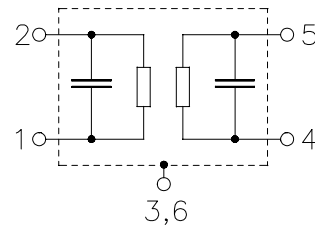
typ. dimensions in mm, approx. weight 0,037 g

Ceramic package **DCC6E**



Pin configuration¹⁾

- 1 Input (recommended) or Input Ground
- 2 Input Ground (recommended) or Input
- 4 Output (recommended) or Output Ground
- 5 Output Ground (recommended) or Output
- 3,6 to be grounded



Type	Ordering code	Marking and package according to	Packing according to
B3740	B39431-B3740-H110	C61157-A7-A143	F61074-V8168-Z000

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T_A	-45/+120	°C	
Storage temperature range	T_{stg}	-45/+120	°C	
DC voltage	V_{DC}	6	V	
Source power	P_S	10	dBm	source impedance 50 Ω

¹⁾ The recommended pin configuration usually offers best suppression of electrical crosstalk. The filter characteristics refer to this configuration.



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Characteristics

Reference temperature:

$$T_A = -45 \dots +95^\circ \text{C}$$

Terminating source impedance:

$$Z_S = 50 \, \Omega \text{ and matching network}$$

Terminating load impedance:

$$Z_L = 50 \, \Omega \text{ and matching network}$$

		min.	typ.	max.	
Center frequency	f_c	—	433,92	—	MHz
(center frequency between 3 dB points)					
Minimum insertion attenuation	$\alpha_{\min}$	—	2,5	3,5	dB
including loss in matching elements ($Q_L = 47$)					
Pass band (relative to $\alpha_{\min}$)					
433,74 ... 434,10 MHz		—	0,8	3,0	dB
Relative attenuation (relative to $\alpha_{\min}$)	α_{rel}				
10,00 ... 414,00 MHz		50	58	—	dB
414,00 ... 423,50 MHz		45	53	—	dB
423,50 ... 431,52 MHz		35	40	—	dB
431,52 ... 432,90 MHz		20	24	—	dB
432,90 ... 433,10 MHz		15	24	—	dB
434,92 ... 444,00 MHz		15	19	—	dB
444,00 ... 810,00 MHz		42	50	—	dB
810,00 ... 1500,00 MHz		55	65	—	dB
1500,00 ... 2500,00 MHz		50	60	—	dB
Impedance for pass band matching ¹⁾					
Input: $Z_{\text{IN}} = R_{\text{IN}} \parallel C_{\text{IN}}$		—	390 $\parallel$ 2,1	—	$\Omega \parallel \text{pF}$
Output: $Z_{\text{OUT}} = R_{\text{OUT}} \parallel C_{\text{OUT}}$		—	390 $\parallel$ 2,1	—	$\Omega \parallel \text{pF}$

¹⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After the SAW filter is removed and input impedance into the input matching / output matching network is calculated.

The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details, we refer to EPCOS application note #4.



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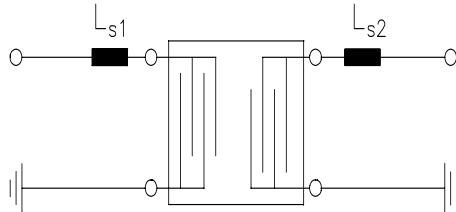
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Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)



$$L_{s1} = 39 \text{ nH}$$

$$L_{s2} = 39 \text{ nH}$$

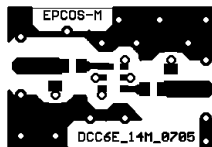
Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem.

Grounding loops are created if input- and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

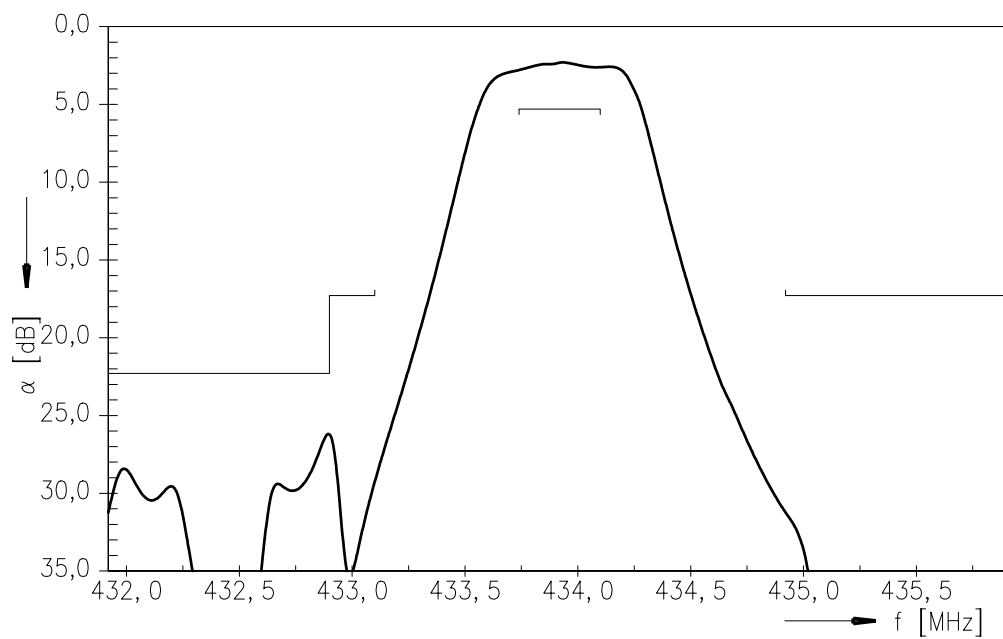
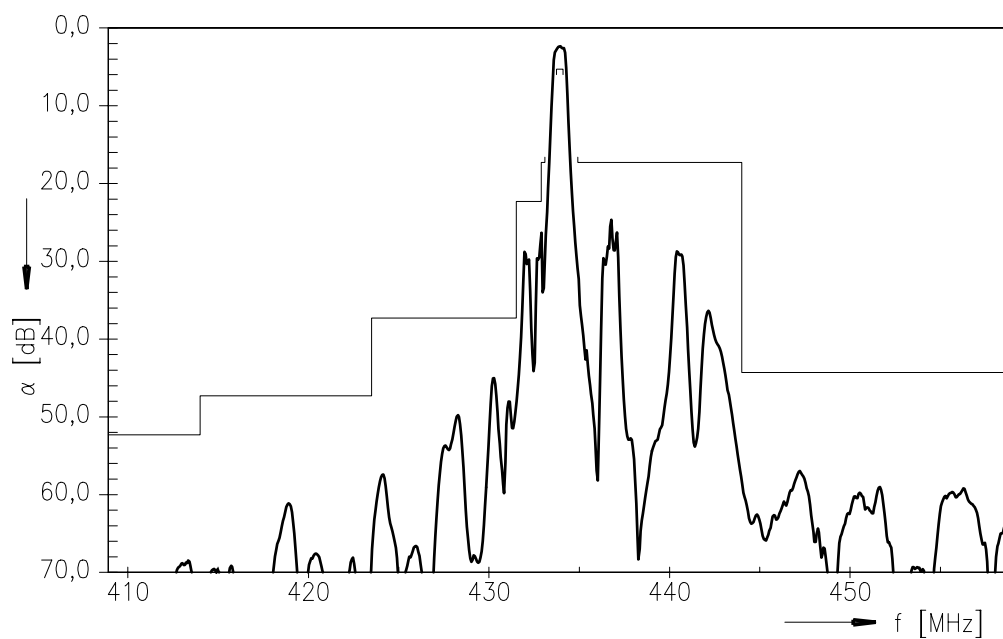
The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection.

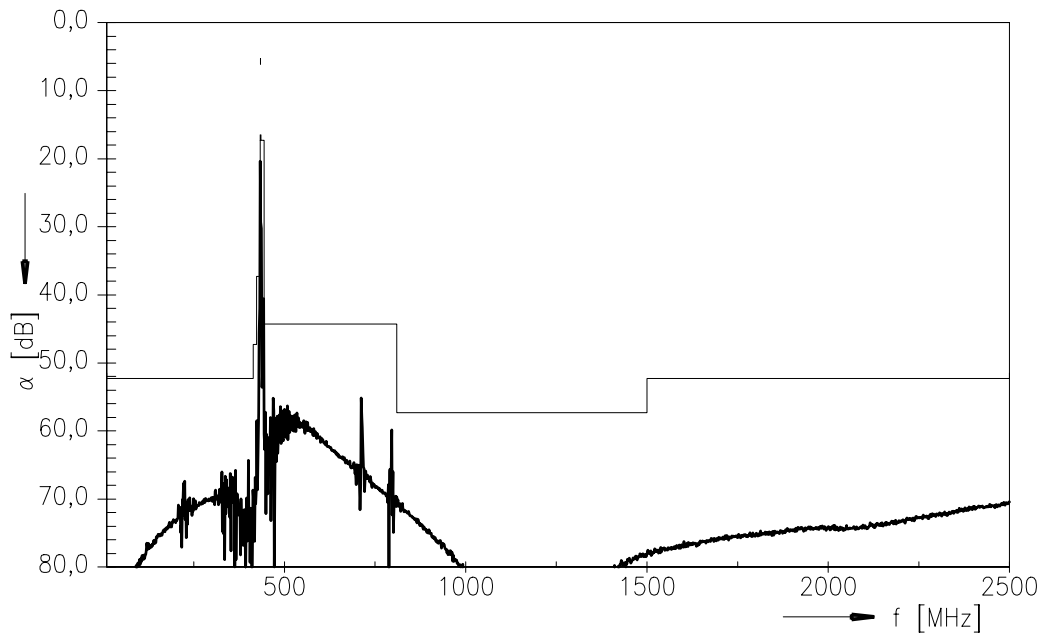


Optimised PCB layout for SAW filters in DCC6E package, pinning 1,4 (top side, scale 1:1)

The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

**SAW Components****B3740****Low-loss Filter****433,92 MHz****Data Sheet****Frequency response****Frequency response (wideband)**

**SAW Components****B3740****Low-loss Filter****433,92 MHz****Data Sheet****Frequency response (ultimate rejection)****Published by EPCOS AG****Surface Acoustic Wave Components Division, SAW CE AE PD****P.O. Box 80 17 09, D-81617 München**

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