

2.5Vdc CMOS COMPATIBLE SMD CRYSTAL CLOCK OSCILLATOR



3.2 x 2.5 x 1.1mm

ASE2 SERIES

FEATURES:

- Compact and low in height
- Low current consumption
- Tri state function
- Suitable for high density SMT., Reflow capable
- Tight stability option
- Seam sealed package

APPLICATIONS:

- CCD clock for VTR camera
- Equipment connected to PC or PC cards
- Thin equipment

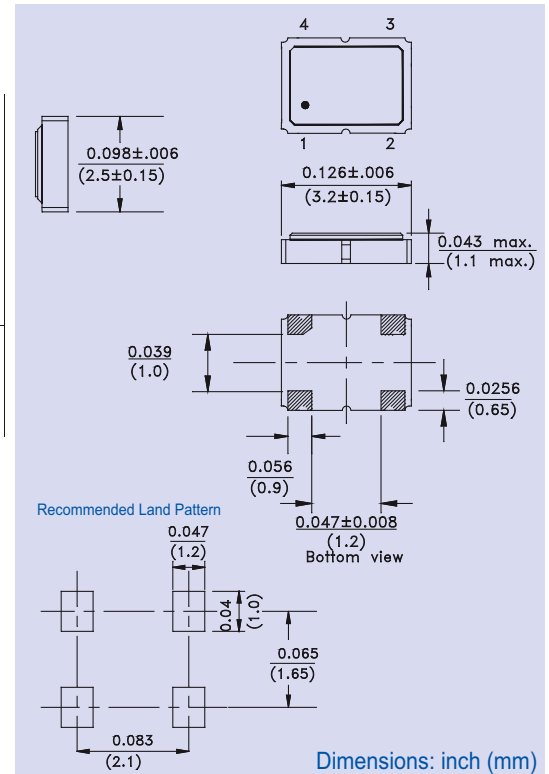
STANDARD SPECIFICATIONS:

PARAMETERS	
Frequency Range	0.75 ~ 75 MHz
Operating Temperature	-20°C to + 70°C (See Options)
Storage Temperature	-55°C to + 100°C
Overall Frequency Stability	± 100 ppm max. (See Options)
Supply Voltage (Vdd)	2.5 Vdc ± 5%
Input Current	See Table
Symmetry	40/60% @ 50%Vdd
Rise and Fall Time (Tr/Tf)	See Table
Output Load	15 pF (max)
Output Voltage	VOH = 0.9* Vdd min. VOL = 0.1* Vdd max.
Tri-State Function	"1" (VIH >= 2.2 Vdc) or open: Oscillation "0" (VIL < 0.8 Vdc) : Hi Z

TABLE		
Freq. (MHz)	Idd max. (max. mA)	Tr/Tf max (nSec)
~ 20 MHz	5	10 max.
~ 40 MHz	9	10 max.
~ 60 MHz	11	10 max.
~ 75 MHz	14	10 max.

Note: Decoupling capacitor shall be connected between Vdd and GND for best performance and stability.

OUTLINE DRAWING:



MARKING:

- XX.X RS (XX.X First 3 digits of frequency)
R Freq. Stability option (*)
L Temperature option (*)
- ASE2 ZYW (Z: month A to L; Y: year 4 for 2004
W: Traceability code (A-Z))

PIN	FUNCTION
1	Tri-State
2	GND/Case
3	Output
4	Vdd

OPTIONS AND PART IDENTIFICATION (Left blank if standard):

ASE2 - Frequency - Temperature - Overall Frequency Stability - Packaging

Temperature options:

L for -40°C to +85°C

Overall Frequency stability options:

C for ± 50 ppm max.

K for ± 30 ppm max. @ -20°C to 70°C only

R for ± 25 ppm max @ -20°C to +70°C only

Packaging option: T for Tape and Reel (1,000pcs/reel, Dia. 180mm)

ABRACON IS
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REVISED: 11.15.06