

Click [here](#) for the 3D model.

Dimensions

D	10mm +/-0.5mm
L	12.6mm +/-0.3mm
W	0.8 - 1.1mm
F	0.2mm MAX
A	10.3mm +/-0.2mm
B	10.3mm +/-0.2mm
C	11mm +/-0.2mm
G	0.35mm +/-0.2mm
P	4.6mm +/-0.2mm

Packaging Specifications

Packaging	T&R, 380mm
Packaging Quantity	400

General Information

Series	A767
Dielectric	Polymer Aluminum
Style	SMD Can
Description	Surface Mount, Polymer Aluminum
RoHS	Yes
Lead	V-Chip
AEC-Q200	No
Halogen Free	true

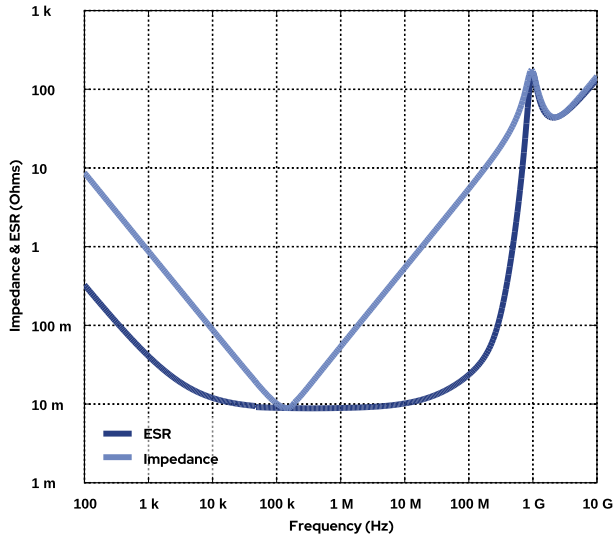
Specifications

Capacitance	180 uF
Capacitance Tolerance	20%
Voltage DC	35 VDC, 40.2 VDC (Surge)
Temperature Range	-55/+105°C
Rated Temperature	105°C
Life	2000 Hrs
ESR	28 mOhms (100kHz 20C)
Impedance	28 mOhms
Ripple Current	2600 mAmps (100kHz 105C)
Leakage Current	1260 uA (2min 20°C)
High Temperature Solder	true

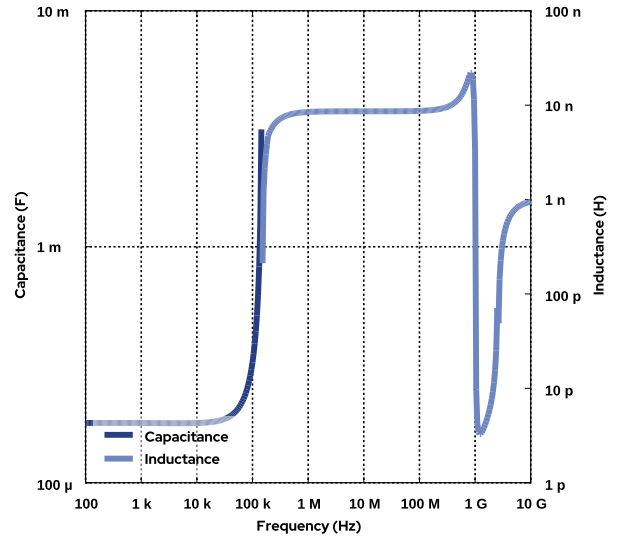
Simulations

For the complete simulation environment please visit [K-SIM](#).

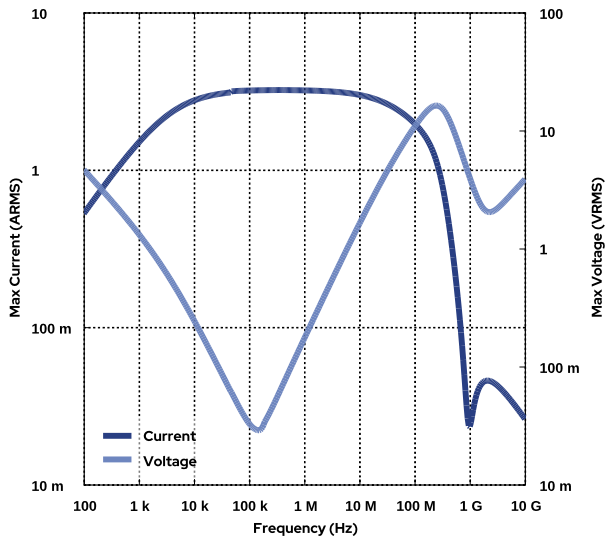
Impedance and ESR



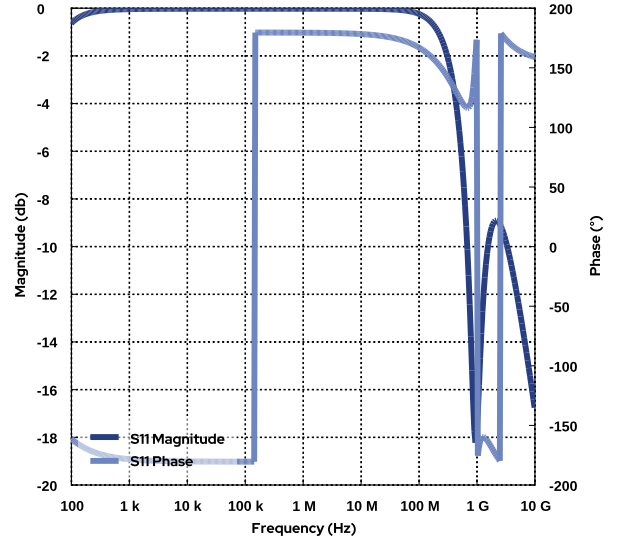
Capacitance and Inductance

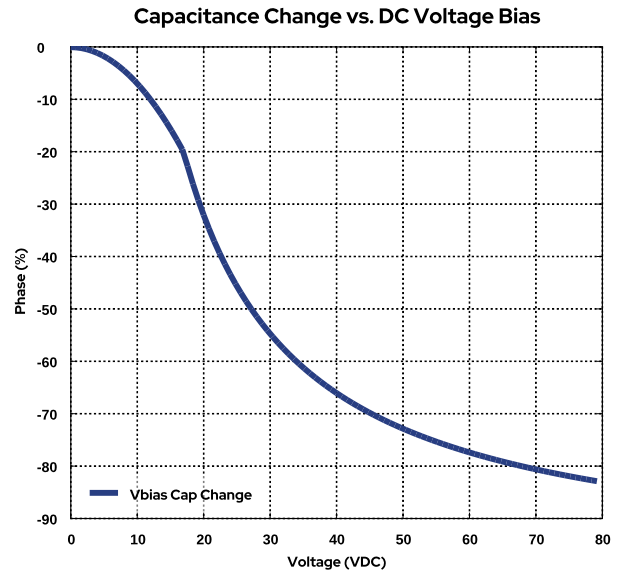
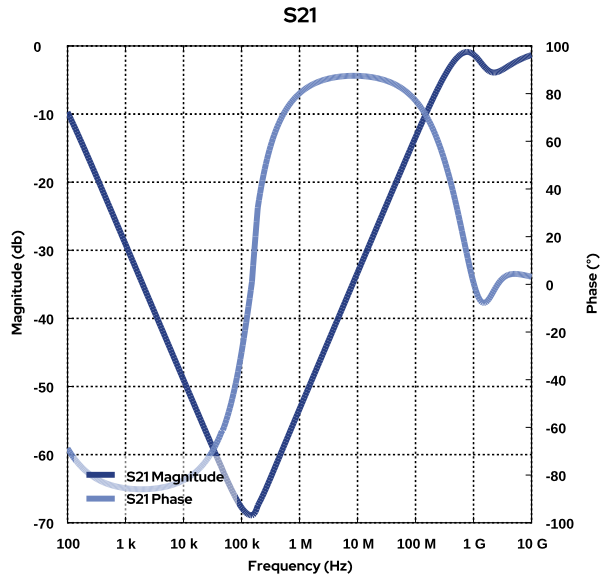


Current and Voltage



S11





These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.