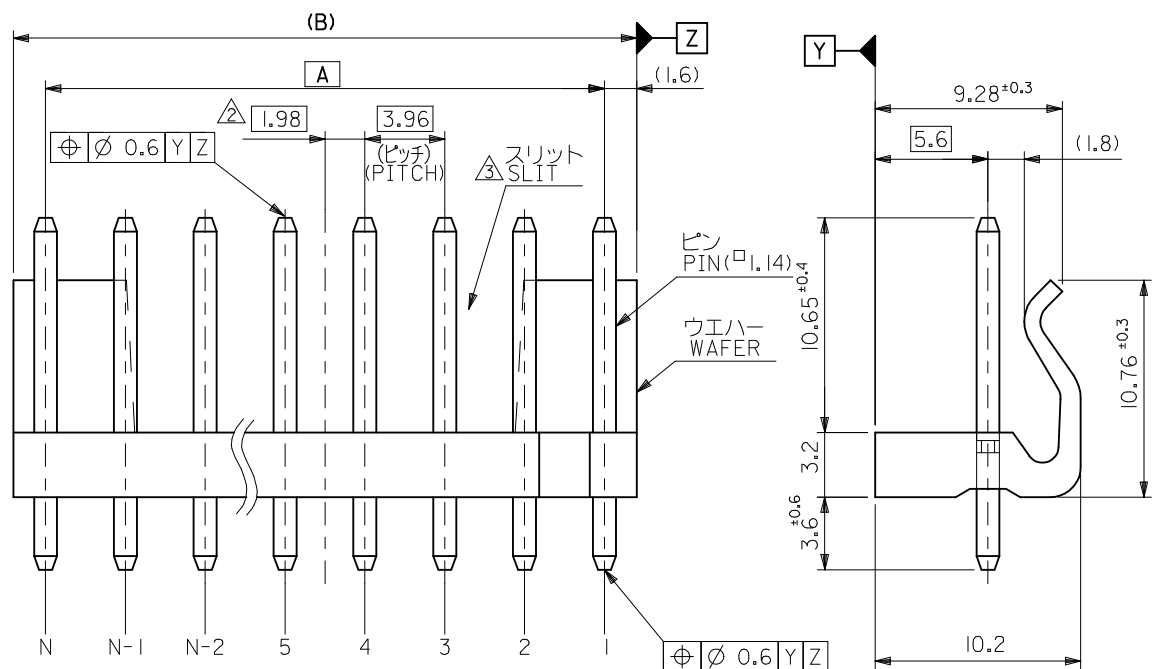


基板取付穴推奨寸法(±1.6)
RECOMMENDED P.C.BOARD HOLE DIM.



注記
NOTES:

1. 嵌合相手 (MATES): 5195-N(5194T, TL)
 △ ウエハーの極数が偶数の場合に適用。
 APPLY EVEN CIRCUIT PRODUCTS.
 △ ウエハーの極数が2、3極の場合はロック部スリットなし。
 NO SLIT IS PROVIDED FOR 2 AND 3CKTS.

11.12	7.92	3	2	2	53406-9910
B	A	ウエハー数 NO. OF CRYS.	ピン数 NO. OF PINS	ピン未挿入極 VOID CKT.	MATERIAL NO.
				MODEL NO.	53406-**10

材料 ピン: 黄銅、銅下地銀メッキ
 MATERIAL PIN: BRASS, TIN OVER
 COPPER PLATING.
 ウエハー: PBTP 15% G.F.
 WAFER UL94V-0

REVISED EC NO: J2015-1133 DRWN: JFURUKAWA 2015/03/12 CHKD: TASAKAWA 2015/03/12 APPR: KMORIKAWA 2015/03/17	GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE MM ONLY		SCALE 4:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION
	0.25 UNDER	UNDER	±0.03	DRAWN BY K. TOJO	DATE '93/1/28	TITLE KK 3.96 WAFER ASSY WITH LOCK		
	0.25 OVER	0.5 UNDER	±0.05	CHECKED BY S. KUNISHI	DATE '95/8/25			
	0.5 OVER	1.0 UNDER	±0.1	APPROVED BY M. FUKUSHIMA	DATE '95/8/25			
	1.0 OVER	10 UNDER	±0.2	MATERIAL NO.	DOCUMENT NO.	SD-53406-001	SHEET NO. 1 OF 1	
10 OVER	30 UNDER	±0.25	SEE TABLE					
30 OVER		±0.3	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			