

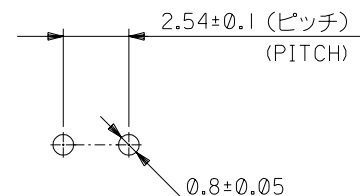
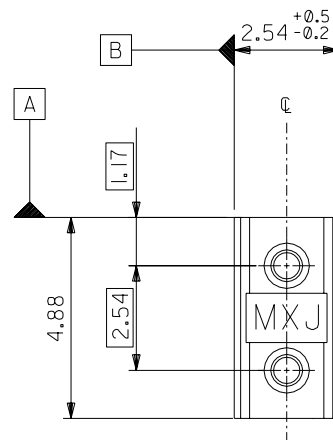
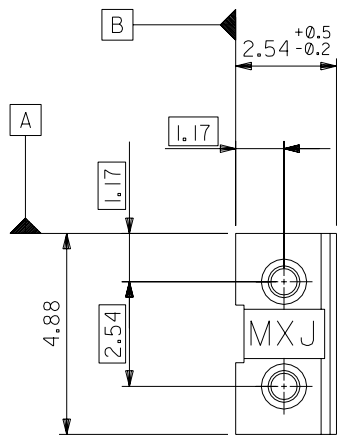
DWG. NO. SD-5547-NA

E

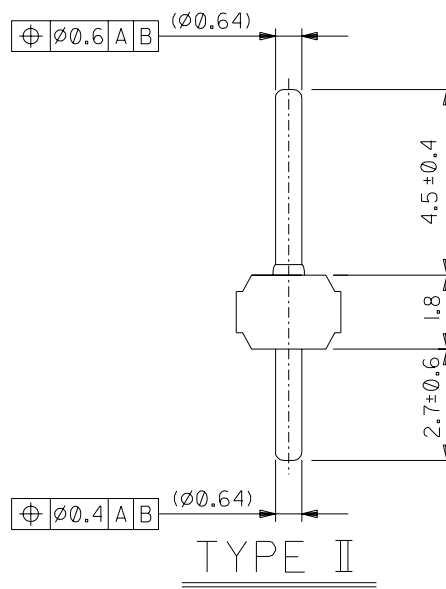
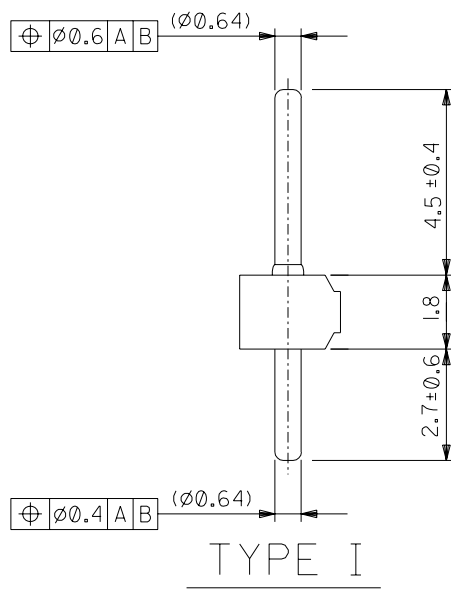
D

ALL DIMENSIONS IN METRIC DO NOT SCALE DRAWING

A



基板取付穴推奨寸法
RECOMMENDED P.C.B. LAYOUT
(SCALE: 5-1)



						材料 MATERIAL	注記参照 SEE NOTES	molex MOLEX-JAPAN CO.,LTD. 日本モレックス株式会社
						仕上げ FINISH	注記参照 SEE NOTES	
						適用電線範囲 WIRE RANGE	—	REVISE ONLY ON CAD SYSTEM
						被覆外径 INS. RANGE	—	TITLE 名称
						DRAWN BY	CHK'D BY	2.54 DUAL ROW
						DATE	DATE	MICRO SHUNT WAFER ASS'Y
						DR. CHK.	DATE	DWG. NO. (SHEET 2 OF 2)
						REVISION RECORD	REVISION RECORD	SD-5547-NA
						GENERAL TOLERANCES	GENERAL TOLERANCES	REV B

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