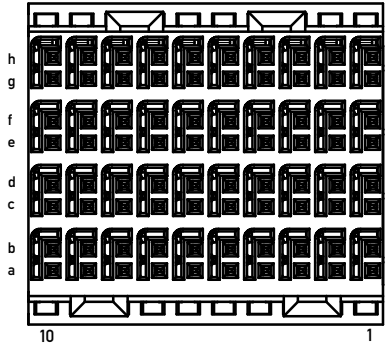
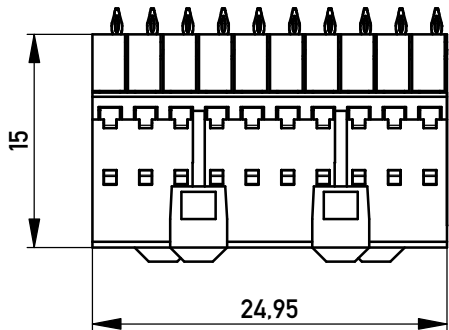
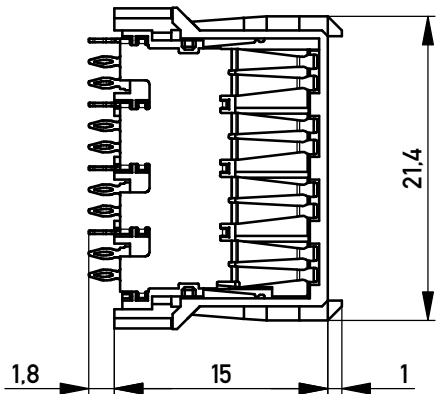
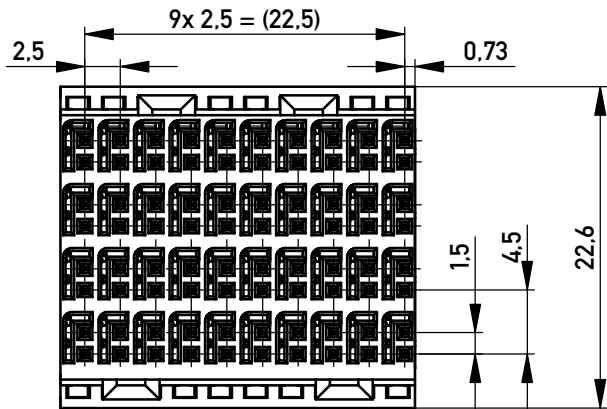


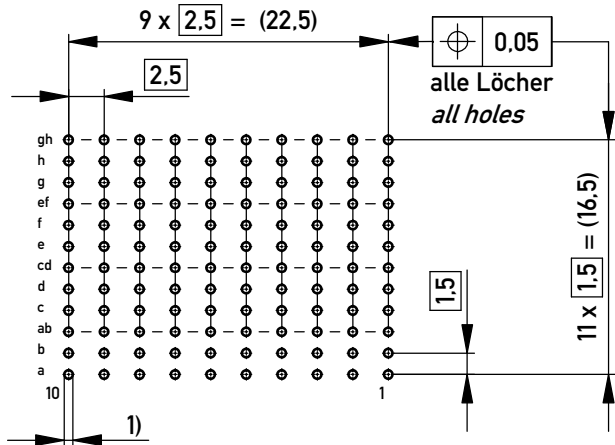
Bestückungsplan
contact layout



a - h = bestückt
a - h = assembled



Lochbild für Leiterplatte (Bestückungsseite)
Board hole pattern (component mounting side)



1) $\varnothing 0.6 \pm 0.05$ Durchmesser des metallisierten Loches
 $\varnothing 0.6 \pm 0.05$ diameter of finished plated-through hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 164062 Nr.1
Metal plating of plated-through hole see drawing 164062 No.1

Fehlende Maße und Angaben nach IEC 61076-4-101
Missing information and dimensions per IEC 61076-4-101

134974	EN	natur / ecru	18mm
Ident. - Nr.	Anschluss termination	Gehäusefarbe housing colour	Abstand Leiterplatte board to board distance
Information:		Tolerances	Scale 2:1
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g		03.05.2018	134970
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Class		ERMZD	